



Customer Information Notification

202101030I : MC56F84xxx Chip Errata Rev Dec 2020 Update

Note: This notice is NXP Company Proprietary.

Issue Date: Feb 25, 2021 **Effective date:**Feb 26, 2021

Dear Emma Tempest,



Here is your personalized notification about a NXP general announcement.

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Change Category

<input type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input type="checkbox"/> Test Process	<input type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Equipment	<input checked="" type="checkbox"/> Errata
<input type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Location	<input type="checkbox"/> Electrical spec./Test coverage
<input type="checkbox"/> Firmware	<input type="checkbox"/> Other			

PCN Overview

Description

NXP Semiconductors announces chip errata update to revision Dec 2020 for MC56F84xxx. The revision history included in the updated document provides a detailed description of the changes. Changes are summarized below.

MC56F84xxx Chip Errata has the following changes:

? Updated workaround description in the following errata:

? e11484: POR: Residual voltage on VDD may cause POR unsuccessful

The MC56F84xxx errata is attached to this notice, and can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/additional-mpu-mcus-architectures/digital-signal-controllers/32-bit-56f8xxx-families/digital-signal-controllers:MC56F84xxx?tab=Documentation_Tab&linkline=Errata

Reason

The errata was modified to provide more accurate workaround to avoid the errata issue.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name technical support
e-mail address tech.support@nxp.com

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NXP Quality Management Team.

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NXP Semiconductors
High Tech Campus, 5656 AG Eindhoven, The Netherlands

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