# MOSFET - Power, N-Channel, SUPERFET® III, FAST

650 V, 250 mΩ, 13 A

# NTPF250N65S3H

#### **Description**

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provides superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III FAST MOSFET series helps minimize various power systems and improve system efficiency.

#### **Features**

- $700 \text{ V} @ \text{T}_{\text{J}} = 150^{\circ}\text{C}$
- Typ.  $R_{DS(on)} = 201 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ.  $Q_g = 24 \text{ nC}$ )
- Low Effective Output Capacitance (Typ. C<sub>oss(eff.)</sub> = 229 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

#### **Applications**

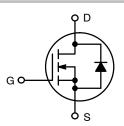
- Computing / Display Power Supplies
- Telecom / Server Power Supplies
- Industrial Power Supplies
- Lighting / Charger / Adapter

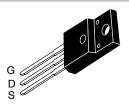


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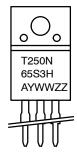
V <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX	
650 V	250 mΩ @ 10 V	13 A	





TO-220 FULLPAK CASE 221D

#### **MARKING DIAGRAM**



T250N65S3H = Specific Device Code

A = Assembly Location

Y = Year WW = Work Week ZZ = Lot Code

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

## ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C, Unless otherwise specified)

Symbol	Parame	Value	Unit	
$V_{DSS}$	Drain to Source Voltage		650	V
$V_{GSS}$	Gate to Source Voltage	DC	±30	V
		AC (f > 1 Hz)	±30	V
I <sub>D</sub>	Drain Current	Continuous (T <sub>C</sub> = 25°C)	13*	Α
		Continuous (T <sub>C</sub> = 100°C)	8*	
I <sub>DM</sub>	Drain Current	Pulsed (Note 1)	36*	Α
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		108	mJ
I <sub>AS</sub>	Avalanche Current (Note 2)	Avalanche Current (Note 2)		Α
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		1.06	mJ
dv/dt	dv/dt MOSFET dv/dt		120	V/ns
	Peak Diode Recovery dv/dt (Note 3)	20		
$P_{D}$	Power Dissipation	(T <sub>C</sub> = 25°C)	29	W
		Derate Above 25°C	0.23	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temperature for Solderin	260	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
\*Drain current limited by maximum junction temperature.

## THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	4.23	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient, Max.		

## PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Shipping
NTPF250N65S3H	T250N65S3H	TO-220 FULLPAK	50 Units / Tube

<sup>1.</sup> Repetitive rating: pulse-width limited by maximum junction temperature. 2.  $I_{AS} = 2.9 \text{ A}$ ,  $R_G = 25 \Omega$ , starting  $T_J = 25^{\circ}\text{C}$ . 3.  $I_{SD} \le 6.5 \text{ A}$ , di/dt  $\le 200 \text{ A/}\mu\text{s}$ ,  $V_{DD} \le 400 \text{ V}$ , starting  $T_J = 25^{\circ}\text{C}$ .

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHARACT	ERISTICS			•	•	
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V, } I_D = 1 \text{ mA, } T_J = 25^{\circ}\text{C}$	650			V
		V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA, T <sub>J</sub> = 150°C	700			V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 10 mA, Referenced to 25°C		0.63		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V			1	μΑ
		$V_{DS} = 520 \text{ V}, T_{C} = 125^{\circ}\text{C}$		0.7		
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA
N CHARACTE	ERISTICS		•			
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 1.1 \text{ mA}$	2.4		4.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.5 A		201	250	mΩ
9FS	Forward Transconductance	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 6.5 A		14		S
YNAMIC CHA	RACTERISTICS				•	•
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 400 \text{ V}, V_{GS} = 0 \text{ V}, f = 250 \text{ kHz}$		1261		pF
C <sub>oss</sub>	Output Capacitance			19		pF
C <sub>oss(eff.)</sub>	Effective Output Capacitance	$V_{DS}$ = 0 V to 400 V, $V_{GS}$ = 0 V		229		pF
C <sub>oss(er.)</sub>	Energy Related Output Capacitance	$V_{DS}$ = 0 V to 400 V, $V_{GS}$ = 0 V		33		pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V			24		nC
Q <sub>gs</sub>	Gate to Source Gate Charge	$V_{DS} = 400 \text{ V}, I_{D} = 6.5 \text{ A}, V_{GS} = 10 \text{ V}$ (Note 4)		5.9		nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge	(Note 1)		6.8		nC
ESR	Equivalent Series Resistance	f = 1 MHz		0.9		Ω
WITCHING CH	IARACTERISTICS					
t <sub>d(on)</sub>	Turn-On Delay Time			18		ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD}$ = 400 V, $I_{D}$ = 6.5 A, $V_{GS}$ = 10 V, $R_{g}$ = 12 $\Omega$		8.2		ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, H_g = 12 \Omega$ (Note 4)		54		ns
t <sub>f</sub>	Turn-Off Fall Time			4.2		ns
OURCE-DRAI	N DIODE CHARACTERISTICS		•			
I <sub>S</sub>	Maximum Continuous Source to Drain [	Maximum Continuous Source to Drain Diode Forward Current			13	Α
I <sub>SM</sub>	Maximum Pulsed Source to Drain Diode Forward Current				36	Α
$V_{SD}$	Source to Drain Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 6.5 A	: 0 V, I <sub>SD</sub> = 6.5 A		1.2	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>DD</sub> = 400 V, I <sub>SD</sub> = 6.5 A,		233		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F/dt = 100 A/\mu s$		2.5		μС

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

#### **TYPICAL CHARACTERISTICS**

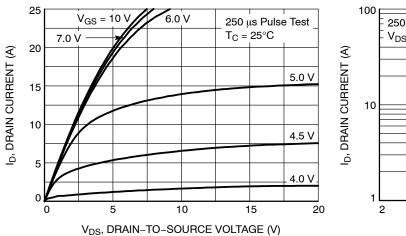


Figure 1. On-Region Characteristics

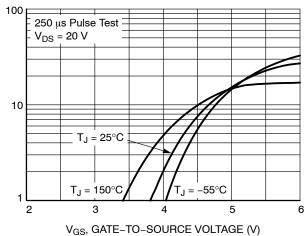


Figure 2. Transfer Characteristics

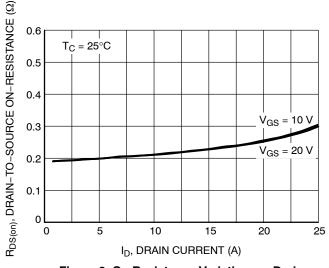


Figure 3. On Resistance Variation vs. Drain Current and Gate Voltage

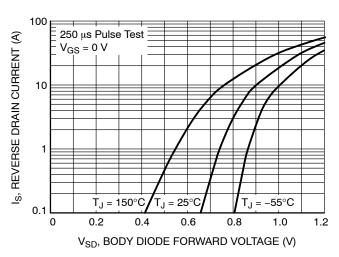


Figure 4. Diode Forward Voltage Variation vs. Source Current and Temperature

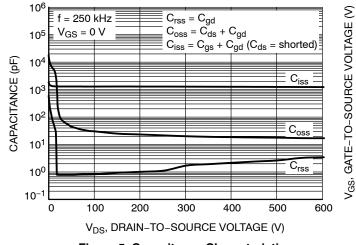


Figure 5. Capacitance Characteristics

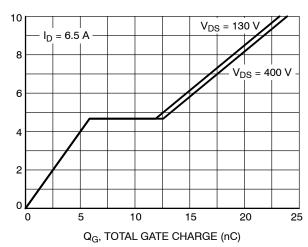


Figure 6. Gate Charge Characteristics

#### **TYPICAL CHARACTERISTICS**

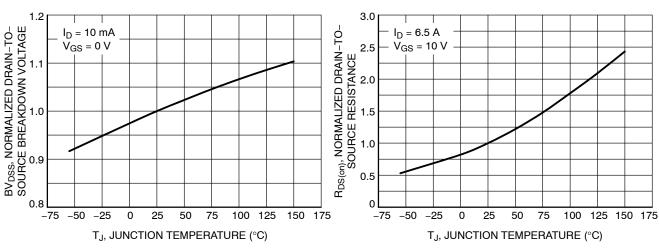
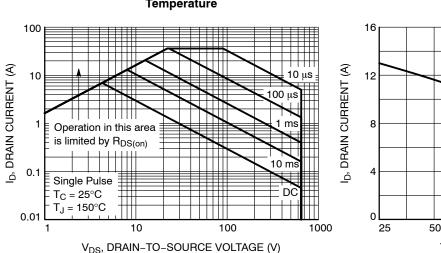
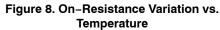


Figure 7. Breakdown Voltage Variation vs. Temperature





 $V_{DS}$ , DRAIN-TO-SOURCE VOLTAGE (V) Figure 11. E<sub>oss</sub> vs. Drain-to-Source Voltage



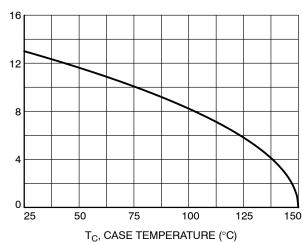


Figure 10. Maximum Drain Current vs. Case Temperature

## **TYPICAL CHARACTERISTICS**

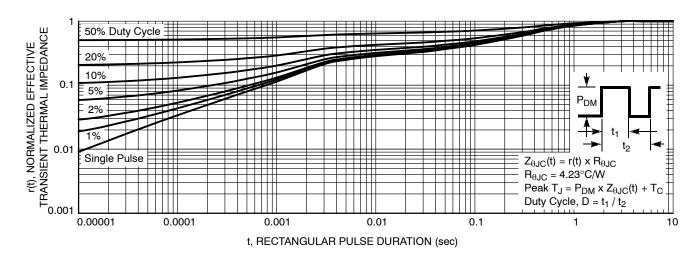


Figure 12. Transient Thermal Response Curve

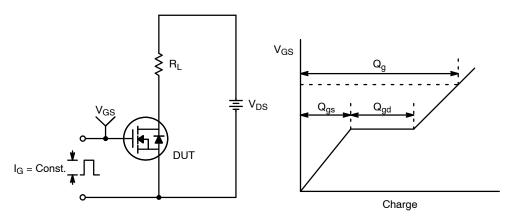


Figure 13. Gate Charge Test Circuit & Waveform

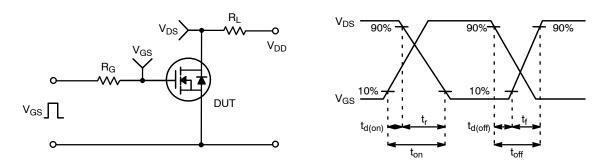


Figure 14. Resistive Switching Test Circuit & Waveforms

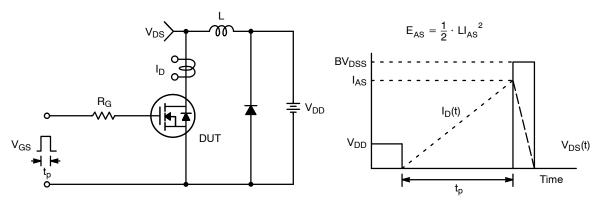


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

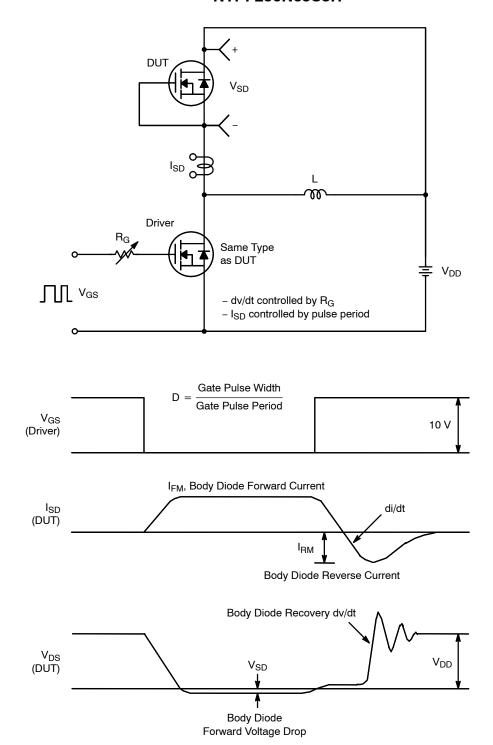


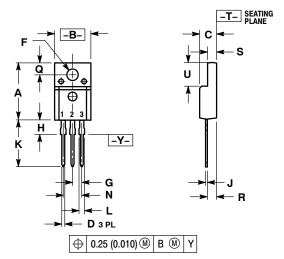
Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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#### PACKAGE DIMENSIONS

#### TO-220 FULLPAK

CASE 221D-03 ISSUE K



#### NOTES

- DIMENSIONING AND TOLERANCING PER ANSI
  Y14 5M 1982
- 2. CONTROLLING DIMENSION: INCH
- 221D-01 THRU 221D-02 OBSOLETE, NEW STANDARD 221D-03.

	INCHES		MILLIN	METERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.617	0.635	15.67	16.12	
В	0.392	0.419	9.96	10.63	
С	0.177	0.193	4.50	4.90	
D	0.024	0.039	0.60	1.00	
F	0.116	0.129	2.95	3.28	
G	0.100 BSC		2.54 BSC		
Н	0.118	0.135	3.00	3.43	
J	0.018	0.025	0.45	0.63	
K	0.503	0.541	12.78	13.73	
L	0.048	0.058	1.23	1.47	
N	0.200 BSC		5.08	BSC	
Q	0.122	0.138	3.10	3.50	
R	0.099	0.117	2.51	2.96	
S	0.092	0.113	2.34	2.87	
U	0.239	0.271	6.06	6.88	

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