



## Final Product Change Notification

202105041F01 : S12ZVC/A/19/12/64/96 FAB SITE EXPANSION (NXP-ATMC to TSMC10)

**Note:** This notice is NXP Company Proprietary.



**Issue Date:** Jun 09, 2021 **Effective date:** Sep 07, 2021

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For detailed information we invite you to [view this notification online](#)

### Change Category

- Wafer Fab Process
- Assembly Process
- Product Marking
- Test Process
- Design
- Wafer Fab Materials
- Assembly Materials
- Mechanical Specification
- Test Equipment
- Errata
- Wafer Fab Location
- Assembly Location
- Packing/Shipping/Labeling
- Test Location
- Electrical spec./Test coverage
- Firmware
- Other: Reference Manual and Errata updated to include TSMC10 mask set information.

## PCN Overview

### Description

NXP Semiconductors is announcing the introduction of Taiwan Semiconductor Manufacturing Company Fab 10 (TSMC10), Shanghai, China as a dual source wafer manufacturing location for the S912ZVC/A/19/12/64/96.

NXP Semiconductors requires the use of Flex part numbers to maximize supply continuity. Without the use of Flex part numbers, backlog will have to be converted from one fab sourced device to another fab sourced device as capacity dictates.

The Data Sheet & Reference Manual for S912ZVC/A/19/12/64/96 has been updated by adding the TSMC10 mask set (0P81C & 1P81C) in section 1.6.1 Part ID Assignments and the Part Ordering Information's mask set identifier suffix

"S12ZVC\_0N23N, Mask Set Errata for Mask 0N23N" have been updated to cover both N23N and P81C mask sets.

Errata e8188: ADC: High current in Stop Mode was also revised in the updated document.

The S912ZVC/A/19/12/64/96 Data Sheet, Reference Manual and Errata is attached with this notification or can be found at [https://www.nxp.com/products/processors-and-microcontrollers/additional-mpu-mcus-architectures/s12-magniv-mixed-signal-mcus/s12zvc-mixed-signal-mcu-for-automotive-industrial-can-applications:S12ZVC?tab=Documentation\\_Tab](https://www.nxp.com/products/processors-and-microcontrollers/additional-mpu-mcus-architectures/s12-magniv-mixed-signal-mcus/s12zvc-mixed-signal-mcu-for-automotive-industrial-can-applications:S12ZVC?tab=Documentation_Tab)

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02, SEM-PW-08, SEM-PW-13

### Reason

The Fab manufacturing site capacity expansion to TSMC10 will improve NXP's ability to meet increasing customer demand and still maintain supply from the original Fab (NXP-ATMC).

#### **Identification of Affected Products**

Top Side Marking

The mask marking for TSMC10 will reflect P81C, while the mask marking for ATMC will remain N23N.

### **Product Availability**

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#### **Sample Information**

Samples are available from Jun 25, 2021

#### **Production**

Planned first shipment Sep 13, 2021

### **Anticipated Impact on Form, Fit, Function, Reliability or Quality**

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No Impact on form, fit, function, reliability or quality

#### **Data Sheet Revision**

A new datasheet will be issued

#### **Disposition of Old Products**

Fab Expansion. No depletion of inventory required.

### **Additional information**

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Self qualification: [view online](#)

Additional documents: [view online](#)

### **Timing and Logistics**

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In compliance with JEDEC J-STD-046, your acknowledgement of this change is expected by Jul 09, 2021.

### **Contact and Support**

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For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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NXP Quality Management Team.

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