



2107131096 Si72xx-B-xx-IB MSL upgrade to MSL1 on TO-92 Package

Bulletin Issue Date: Jul 13, 2021

Effective Date: Jul 13, 2021

Description of Change

Silicon Labs is pleased to announce the improvement of MSL rating from MSL 2 to MSL 1 on Si72xx-B-xx-IB TO-92 packaged products

Reason for Change

Silicon labs has qualified these packages to MSL 1 to improve customer experience.

Product Identification

Existing Part #
SI7201-B-09-IB
SI7201-B-09-IBR
SI7201-B-11-IB
SI7201-B-11-IBR
SI7201-B-92-IB
SI7201-B-92-IBR
SI7202-B-04-IB
SI7202-B-04-IBR
SI7211-B-00-IB
SI7211-B-00-IBR

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at <http://www.silabs.com>.

Customer Actions Needed:

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, "how-to" and design documents, product change notices (PCN) and other valuable content available only to registered users. <http://www.silabs.com/profile>



Si7200 TO92 Qualification Report

The information contained in this document is CONFIDENTIAL and PROPRIETARY to Silicon Labs and is intended only for the internal use of Silicon Labs. Any other use or reproduction of any part of this document is prohibited without Silicon Labs' written consent. Any use of this document outside of Silicon Labs is solely at the risk of the user. Silicon Labs disclaims all warranties concerning the accuracy of the information contained in this document. This document is version controlled; printed or electronically saved versions of this documents may be obsolete. Any misuse of this document should be reported to DL.QualitySystems@silabs.com

Part Rev A, TSMC Fabrication, JCET Assembly							
Test Name	Test Condition	Qualification	Lot ID or Start	Fail/Pass or End	Notes	Summary	Status
Test Group A – Accelerated Environment Stress Tests							
HAST	JA110 130°C, 85%RH Vcc=5.5V, 96 hours	3 lots, N=>25	Q041850	0/80	1	3 lots 0/240	Pass
			Q042124	0/80	1		
			Q042128	0/80	1		
Temp Cycle	JA104 Cond C: -65°C to 150°C 500 cycles	3 lots, N=>25	Q041852	0/80	1	3 lots 0/240	Pass
			Q042126	0/80	1		
			Q042130	0/80	1		
HTSL	JA103 150°C, 1000hr	3 lots, N=>25	Q041853	0/50	1	3 lots 0/150	Pass
			Q042127	0/50	1		
			Q042131	0/50	1		
uHAST	JA118 130°C, 85%RH Vcc=5.5V, 96 hours	3 lots, N=>25	Q041851	0/82	1	3 lots 0/246	Pass
			Q042125	0/82	1		
			Q042129	0/82	1		
Pre-Conditioning	MSL 1 Bake: 125°C, 24h Soak: 85°C, 85%RH, 168h Reflow : 260°C, 3 times	3 lots, N=>25	Q048099	0/100	2	3 lots 0/300	Pass
			Q048100	0/100	2		
			Q048101	0/100	2		
Test Group B – Accelerated Lifetime Simulation Tests							
HTOL	JA108 T _j ≥ 125°C, Dynamic Vcc=5.5V, 1000 hours	3 lots, N=>77	Q040022	0/102		3 lots 0/279	Pass
			Q040618	0/79			
			Q041163	0/98			
ELFR	JA108 T _j ≥ 125°C, Dynamic Vcc=5.5V, 48 hours	3 lots, N=>500	Q040713	0/808		3 lots 0/2421	Pass
			Q040714	0/804			
			Q041373	0/809			
Test Group E – Electrical Verification							
ESD-HBM	JS-001	1 lot, N=>3	Q040357			2500 V	Class 2
ESD-CDM	JESD22-C101	1 lot, N=>3	Q041389			1000 V	Class IV
Latch Up	JESD78 ±100mA Overvoltage = 8.2V	1 lot, N=>3	Q041394 Q041395	25 °C 125 °C			Pass Pass

Notes:

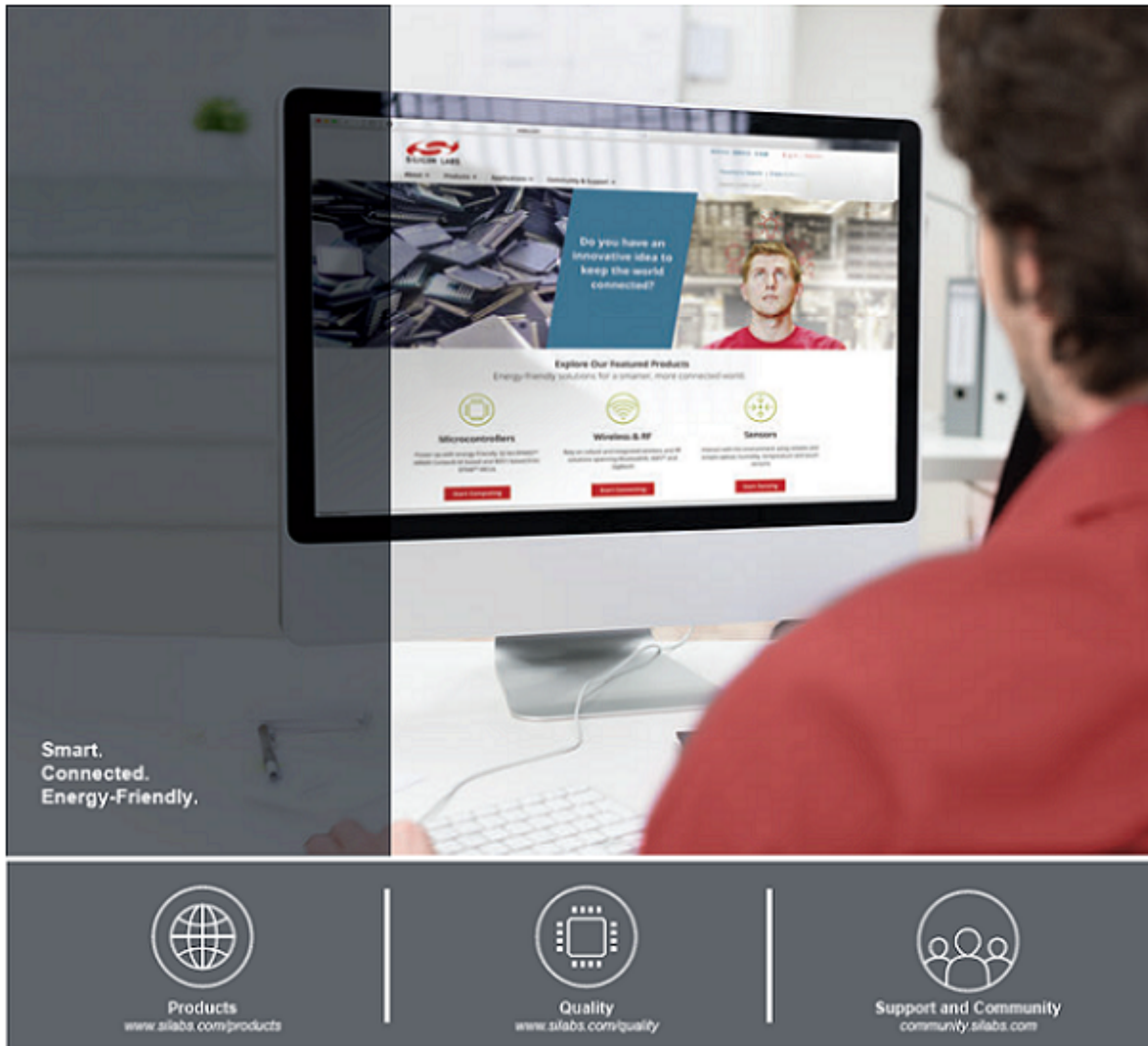
- Parts are Pre-conditioned at MSL2/260°C
- Secondary MSL study done to confirm product passed MSL 1



Si7200 TO92 Qualification Report

The information contained in this document is CONFIDENTIAL and PROPRIETARY to Silicon Labs and is intended only for the internal use of Silicon Labs. Any other use or reproduction of any part of this document is prohibited without Silicon Labs' written consent. Any use of this document outside of Silicon Labs is solely at the risk of the user. Silicon Labs disclaims all warranties concerning the accuracy of the information contained in this document. This document is version controlled; printed or electronically saved versions of this documents may be obsolete. Any misuse of this document should be reported to DL.QualitySystems@silabs.com

Part Rev A, TSMC Fabrication, JCET Assembly							
Test Name	Test Condition	Qualification	Lot ID or Start	Fail/Pass or End	Notes	Summary	Status
This report applies to the following part numbers:							
Si72xx-B-xx-EB/R							
Si72xx-B-xx-FB/R							
Si72xx-B-xx-IB/R							



Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOModem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701

<http://www.silabs.com>