

Product Change Notification / SYST-01POHR719

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02-Nov-2021

Product Category:

Power Management - Power Switches

PCN Type:

Document Change

Notification Subject:

Data Sheet - MIC94040/1/2/3 Data Sheet Document Revision

Affected CPNs:

SYST-01POHR719_Affected_CPN_11022021.pdf SYST-01POHR719_Affected_CPN_11022021.csv

Notification Text:

SYST-01POHR719

Microchip has released a new Product Documents for the MIC94040/1/2/3 Data Sheet of devices. If you are using one of these devices please read the document located at $\frac{MIC94040}{1/2/3}$ Data Sheet.

Notification Status: Final

Description of Change:

- Converted Micrel document MIC94040/1/2/3 to Microchip data sheet template DS20006607A.
- Minor grammatical text changes throughout.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 02 Nov 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.
Markings to Distinguish Revised from Unrevised Devices: N/A
Attachments:
MIC94040/ 1/ 2/ 3 Data Sheet
Please contact your local Microchip sales office with questions or concerns regarding this notification.
Terms and Conditions:
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SYST-01POHR719 - Data Sheet - MIC94040/1/2/3 Data Sheet Document Revision

Affected Catalog Part Numbers (CPN)

MIC94040YFL-TR

MIC94041YFL-TR

MIC94042YFL-TR

MIC94043YFL-TR

Date: Tuesday, November 02, 2021



28 m Ω R_{DS(ON)} 3A High-Side Load Switch in 1.2 mm x 1.2 mm FDFN Package

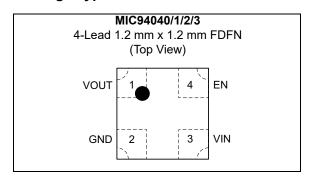
Features

- 28 mΩ R_{DS(ON)}
- · 3A Continuous Operating Current
- Space-Saving 1.2 mm x 1.2 mm 4-Lead FDFN Package
- Input Voltage Range: 1.7V to 5.5V
- Internal Level Shift for CMOS/TTL Control Logic
- · Ultra-Low Quiescent Current
- · Micropower Shutdown Current
- Soft-Start: MIC94042, MIC94043
- Load Discharge Circuit: MIC94041, MIC940483
- · Ultra-Fast Turn-Off Time
- -40°C to +125°C Junction Operating Temperature

Applications

- · Cellular Phones
- Portable Navigation Devices (PND)
- · Personal Media Players (PMP)
- Ultra-Mobile PCs
- Portable Instrumentation
- · Other Portable Applications
- PDΔ
- · Industrial and Datacom Equipment

Package Type



General Description

The MIC94040, MIC94041, MIC94042, and MIC94043 are a family of high-side load switches designed to operate from 1.7V to 5.5V input voltage. The load switch pass element is an internal $28~m\Omega~R_{DS(ON)}$ P-channel MOSFET which enables the device to support up to 3A of continuous current. Additionally, the load switch supports 1.5V logic level control and shutdown features in a tiny 1.2 mm x 1.2 mm 4-lead FDFN package.

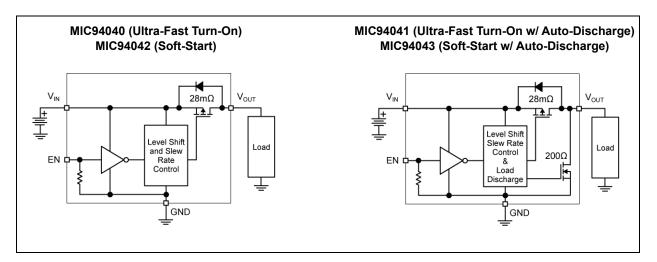
The MIC94040 and MIC94041 feature rapid turn on, while the MIC94042 and MIC94043 provide a slew rate controlled soft-start turn-on of 100 μ s. The soft-start feature is provided to prevent an in-rush current event from pulling down the input supply voltage.

The MIC94041 and MIC94043 feature an active load discharge circuit which switches in a 200Ω load when the switch is disabled to automatically discharge a capacitive load.

An active pull-down on the enable input keeps the MIC94040/1/2/3 in a default OFF state until the enable pin is pulled above 1.2V. Internal level shift circuitry allows low voltage logic signals to switch higher supply voltages. The enable voltage can be as high as 5.5V and is not limited by the input voltage.

The MIC94040/1/2/3 operating voltage range makes them ideal for Lithium ion and NiMH/NiCad/Alkaline battery powered systems, as well as non-battery powered applications. The devices provide low quiescent current and low shutdown current to maximize battery life.

Typical Application Circuits



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

Input Voltage (V _{IN})	+6V
Enable Voltage (V _{EN})	+6V
Continuous Drain Current (I _D) (Note 1)	
T _A = +25°C	±3A
T _A = +85°C	±2A
Pulsed Drain Current (I _{DP}) (Note 2)	
Continuous Diode Current (I _S) (Note 3)	–50 mA
ESD Rating (HBM, Note 4)	
Operating Ratings ††	
Input Voltage (V _{IN})	+1.7V to +5.5V

† Notice: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.

†† Notice: The device is not guaranteed to function outside its operating ratings.

- Note 1: With thermal contact to PCB. See Thermal Considerations section.
 - 2: Pulse width <300 µs with <2% duty cycle.
 - 3: Continuous body diode current conduction (reverse conduction, i.e. V_{OUT} to V_{IN}) is not recommended.
 - 4: Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5 k Ω in series with 100 pF.

ELECTRICAL CHARACTERISTICS

Electrical Characteristics: $T_A = +25$ °C, **bold** values indicate -40°C $\le T_A \le +85$ °C, unless noted.

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Enable Threshold Voltage	V _{EN_TH}	0.4	_	1.2	V	V_{IN} = 1.7V to 4.5V, I_{D} = -250 μ A
Ouissant Cumant		_	0.1	1		$V_{\rm IN}$ = $V_{\rm EN}$ = 5.5V, $I_{\rm D}$ = OPEN Measured on $V_{\rm IN}$ MIC94040/1
Quiescent Current	IQ	_	7	10	μA	$V_{\rm IN}$ = $V_{\rm EN}$ = 5.5V, $I_{\rm D}$ = OPEN Measured on $V_{\rm IN}$ MIC94042/3
Enable Input Current	I _{EN}	1	2.5	4	μA	$V_{IN} = V_{EN} = 5.5V$, $I_D = OPEN$
Quiescent Current (Shutdown)	I _{SHUT-Q}	-	0.1	1	μA	V_{IN} = +5.5V, V_{EN} = 0V, I_{D} = OPEN Measured on V_{IN}
OFF State Leakage Current	I _{SHUT-SWITCH}	_	0.1	1	μA	V_{IN} = +5.5V, V_{EN} = 0V, I_{D} = SHORT Measured on V_{IN} , Note 1
		_	28	55		V_{IN} = +5.0V, I_{D} = -100 mA, V_{EN} = 1.5V
		_	30	60		V_{IN} = +4.5V, I_{D} = -100 mA, V_{EN} = 1.5V
P-Channel Drain-to-Source ON Resistance		1	33	65	0	V_{IN} = +3.6V, I_{D} = -100 mA, V_{EN} = 1.5V
	R _{DS(ON)}		45	90	mΩ	V_{IN} = +2.5V, I_{D} = -100 mA, V_{EN} = 1.5V
		_	72	145		V _{IN} = +1.8V, I _D = -100 mA, V _{EN} = 1.5V
		_	82	160		V _{IN} = +1.7V, I _D = -100 mA, V _{EN} = 1.5V

Note 1: Measured on the MIC94040YFL and MIC94042YFL.

ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Characteristics: $T_A = +25^{\circ}C$, **bold** values indicate $-40^{\circ}C \le T_A \le +85^{\circ}C$, unless noted.

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions
Load Discharge Resistance	R _{DISCHARGE}	_	250	400	Ω	V _{IN} = +3.6V, I _{TEST} = 1 mA, V _{EN} = 0V MIC94041/3
Dynamic Electrical Ch	aracteristics					
Turn-On Delay	•	_	0.97	1.5		V _{IN} = +3.6V, I _D = -100 mA, V _{EN} = 1.5V MIC94040, MIC94041
	ton_dly	50	106	185	μs	V _{IN} = +3.6V, I _D = -100 mA, V _{EN} = 1.5V MIC94042, MIC94043
Town On Die a Time	^t ON_RISE	0.5	0.9	5		V _{IN} = +3.6V, I _D = -100 mA, V _{EN} = 1.5V MIC94040, MIC94041
Turn-On Rise Time		50	116	200	μs	V _{IN} = +3.6V, I _D = -100 mA, V _{EN} = 1.5V MIC94042, MIC94043
Turn-Off Delay Time	t _{OFF_DLY}	_	100	200	ns	$V_{IN} = +3.6V$, $I_{D} = -100$ mA, $V_{EN} = 0V$
Turn-Off Fall Time	t _{OFF_FALL}	_	20	100	ns	$V_{IN} = +3.6V$, $I_{D} = -100$ mA, $V_{EN} = 0V$

Note 1: Measured on the MIC94040YFL and MIC94042YFL.

TEMPERATURE SPECIFICATIONS

Parameters	Symbol	Min.	Тур.	Max.	Units	Conditions	
Temperature Ranges							
Junction Temperature Range	T_J	-4 0	_	+125	°C	_	
Storage Temperature Range	T _S	- 55	_	+150	°C	_	
Package Thermal Resistances							
Thermal Resistance, 4-Ld FDFN 1.2 mm x 1.2 mm	θЈС	_	90	_	°C/W	_	

Note 1: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction to air (i.e., T_A, T_J, θ_{JA}). Exceeding the maximum allowable power dissipation will cause the device operating junction temperature to exceed the maximum +125°C rating. Sustained junction temperatures above +125°C can impact the device reliability.

2.0 TYPICAL PERFORMANCE CURVES

Note:

The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

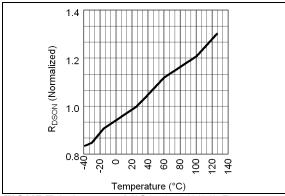


FIGURE 2-1: MIC94040/1/2/3 R_{DS(ON)} Variance vs. Temperature.

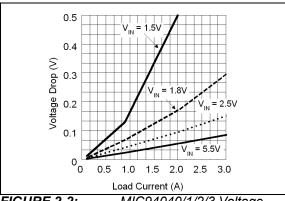


FIGURE 2-2: MIC94040/1/2/3 Voltage Drop vs. Load Current.

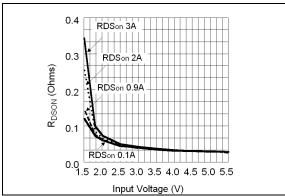


FIGURE 2-3: MIC94040/1/2/3 On Resistance vs. Input Voltage.

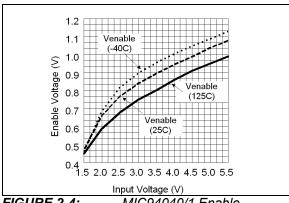


FIGURE 2-4: MIC94040/1 Enable Threshold vs. Input Voltage.

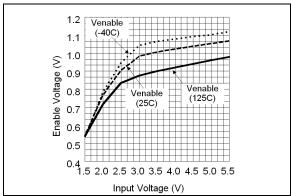


FIGURE 2-5: MIC94042/3 Enable Threshold vs. Input Voltage.

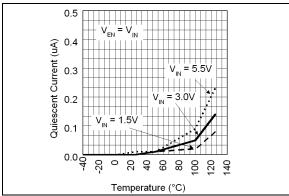
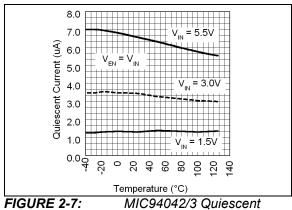


FIGURE 2-6: MIC94040/1 Quiescent Current vs. Temperature.



Current vs. Temperature.

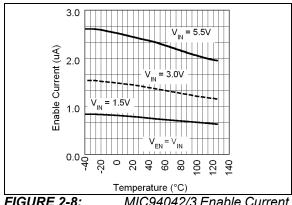


FIGURE 2-8: MIC94042/3 Enable Current vs. Temperature.

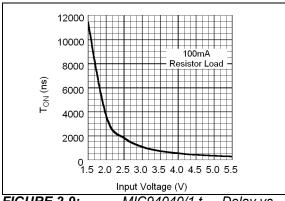


FIGURE 2-9: MIC94040/1 t_{ON} Delay vs. Input Voltage.

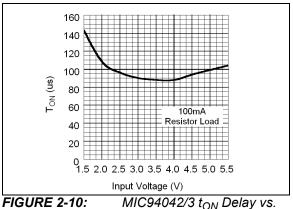


FIGURE 2-10: Input Voltage.

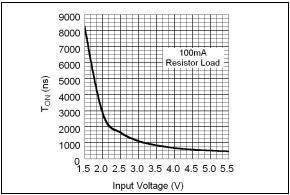


FIGURE 2-11: MIC94040/1 Rise Time vs. Input Voltage.

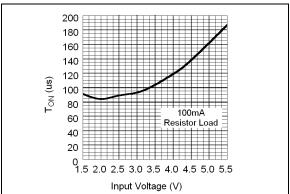


FIGURE 2-12: MIC94042/3 Rise Time vs. Input Voltage.

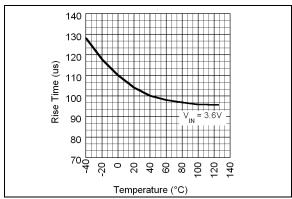


FIGURE 2-13: MIC94040/1 Turn-On Rise Time vs. Temperature.

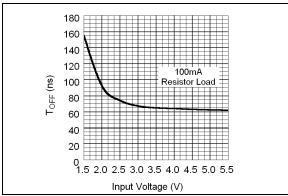


FIGURE 2-14: MIC94042/3 t_{OFF} Delay vs. Input Voltage.

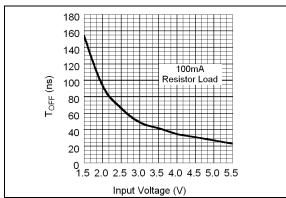


FIGURE 2-15: MIC94040/1/2/3 t_{OFF} Delay vs. Input Voltage.

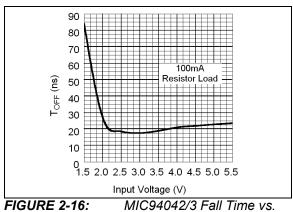


FIGURE 2-16: Input Voltage.

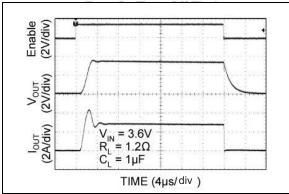


FIGURE 2-17: MIC94040 Turn-On/Turn-Off Timing.

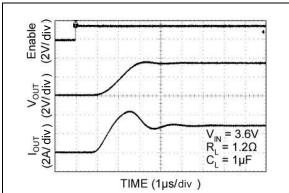


FIGURE 2-18: MIC94040 Turn-On/Turn-Off Timing.

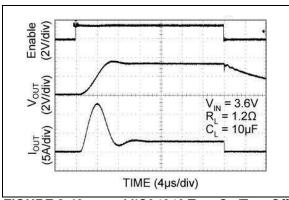
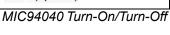


FIGURE 2-19: MIC9 Timing.



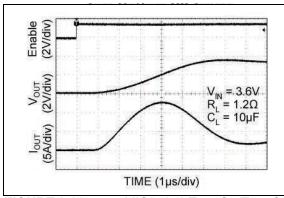


FIGURE 2-20: MIC94040 Turn-On/Turn-Off Timing.

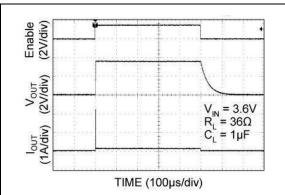


FIGURE 2-21: MIC94040 Turn-On/Turn-Off Timing.

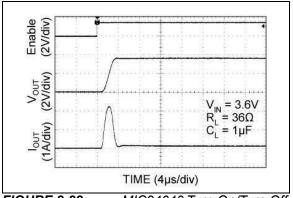


FIGURE 2-22: MIC94040 Turn-On/Turn-Off Timing.

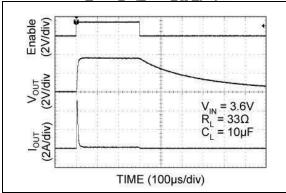


FIGURE 2-23: MIC94040 Turn-On/Turn-Off Timing.

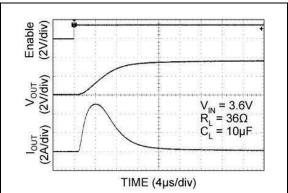


FIGURE 2-24: MIC94040 Turn-On/Turn-Off Timing.

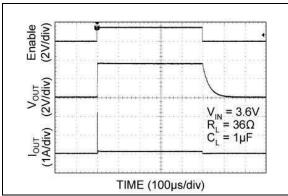


FIGURE 2-25: MIC94041 Turn-On/Turn-Off Timing.

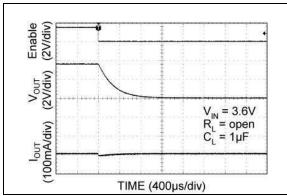


FIGURE 2-26: MIC94041 Turn-On/Turn-Off Timing.

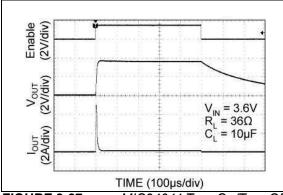


FIGURE 2-27: MIC94041 Turn-On/Turn-Off Timing.

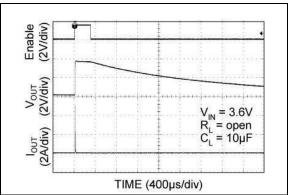


FIGURE 2-28: MIC94041 Turn-On/Turn-Off Timing.

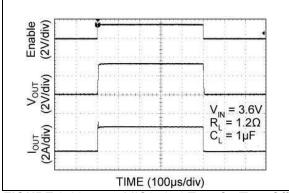


FIGURE 2-29: MIC94041 Turn-On/Turn-Off Timing.

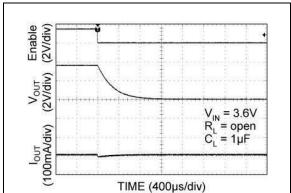


FIGURE 2-30: MIC94041 Turn-On/Turn-Off Timing.

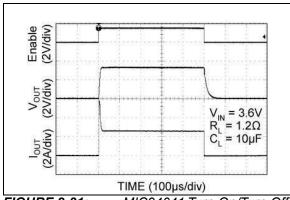


FIGURE 2-31: MIC94041 Turn-On/Turn-Off Timing.

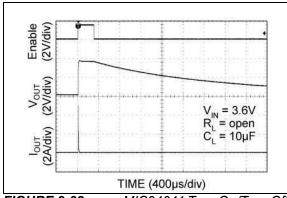


FIGURE 2-32: MIC94041 Turn-On/Turn-Off Timing.

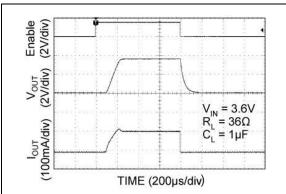


FIGURE 2-33: MIC94042 Turn-On/Turn-Off Timing.

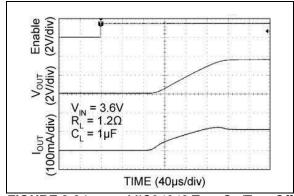


FIGURE 2-34: MIC94042 Turn-On/Turn-Off Timing.

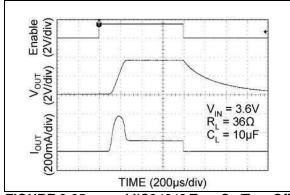


FIGURE 2-35: MIC94042 Turn-On/Turn-Off Timing.

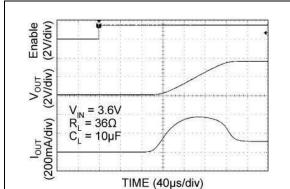


FIGURE 2-36: MIC94042 Turn-On/Turn-Off Timing.

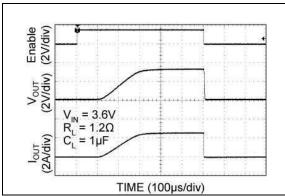


FIGURE 2-37: MIC94042 Turn-On/Turn-Off Timing.

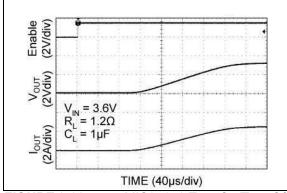


FIGURE 2-38: MIC94042 Turn-On/Turn-Off Timing.

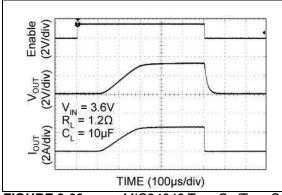


FIGURE 2-39: MIC94042 Turn-On/Turn-Off Timing.

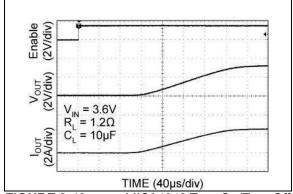


FIGURE 2-40: MIC94042 Turn-On/Turn-Off Timing8.

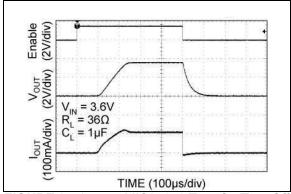


FIGURE 2-41: MIC94043 Turn-On/Turn-Off Timing.

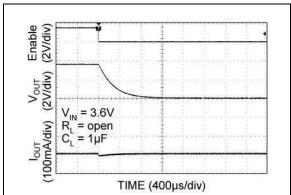


FIGURE 2-42: MIC94043 Turn-On/Turn-Off Timing.

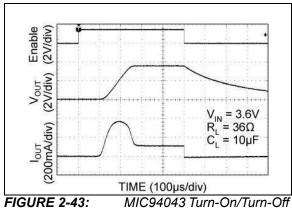
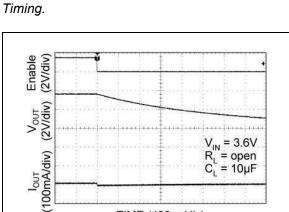


FIGURE 2-43: MIC94043 Tu



TIME (400μs/div)

FIGURE 2-44: MIC94043 Turn-On/Turn-Off
Timing.

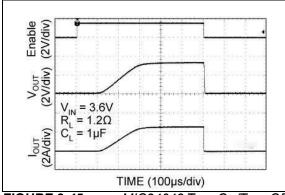


FIGURE 2-45: MIC94043 Turn-On/Turn-Off Timing.

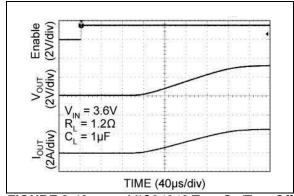


FIGURE 2-46: MIC94043 Turn-On/Turn-Off Timing.

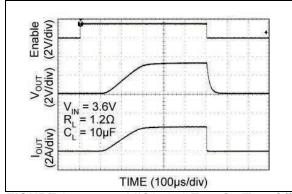


FIGURE 2-47: MIC94043 Turn-On/Turn-Off Timing.

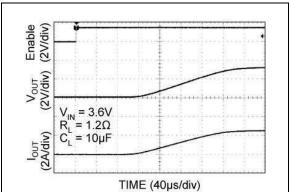


FIGURE 2-48: MIC94043 Turn-On/Turn-Off Timing.

3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1.

TABLE 3-1: PIN FUNCTION TABLE

Pin Number	Pin Name	Description				
1	V _{OUT}	Drain of P-Channel MOSFET.				
2	GND	Ground. Should be connected to electrical ground.				
3	V _{IN}	Source of P-Channel MOSFET.				
4	EN	Enable (Input): Active-high CMOS/TTL control input for switch. Internal ~2 M Ω pull-down resistor. Output will be off if this pin is left floating.				

4.0 APPLICATION INFORMATION

4.1 Power Dissipation Considerations

As with all power switches, the current rating of the switch is limited mostly by the thermal properties of the package and the PCB on which it's mounted. There is a simple Ohm's law type relationship between thermal resistance, power dissipation, and temperature that are analogous to an electrical circuit.

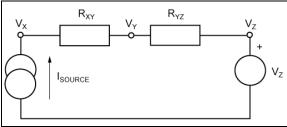


FIGURE 4-1: Simple Electrical Circuit.

From this simple circuit, one can calculate V_X if one knows I_{SOURCE} , V_Z , and the resistor values for R_{XY} and R_{YZ} using Equation 4-1.

EQUATION 4-1:

$$V_X = I_{SOURCE} \times (R_{XY} + R_{YZ}) + V_Z$$

Thermal circuits can be considered using these same rules and can be drawn similarly by replacing current sources with power dissipation (in Watts), resistance with thermal resistance (in °C/W), and voltage sources with temperature (in °C).

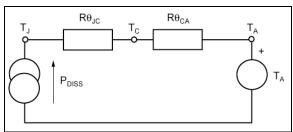


FIGURE 4-2: Simple Thermal Circuit.

By replacing the variables in the equation for V_X , one can find the junction temperature (T_J) from power dissipation, ambient temperature, and then know thermal resistance of the PCB $(R\theta_{CA})$ and the package $(R\theta_{JC})$.

EQUATION 4-2:

$$T_J = P_{DISS} \times (R\theta_{JC} + R\theta_{CA}) + T_A$$

 P_{DISS} is calculated as ${\rm I_{SWITCH}}^2$ x ${\rm R_{SW(MAX)}}.$ $R\theta_{JC}$ is found in the Temperature Specifications section of this data sheet and $R\theta_{CA}$ (the PCB thermal resistance) values for various PCB copper areas is discussed in Designing with Low Dropout Voltage Regulators.

4.1.1 AN EXAMPLE

A switch is intended to drive a 2A load and is placed on a PCB that has a ground plane area of at least 25 mm by 25 mm (625 mm 2). The voltage source is a Li-ion battery with a lower operating threshold of 3V and the ambient temperature of the assembly can be up to 50°C.

Summary of variables:

- I_{SW} = 2A
- V_{IN} = 3V to 4.2V
- T_A = 50°C
- $R\theta_{JC} = 90^{\circ}C/W$
- Rθ_{CA} = 53°C/W (as read from Figure 4-3)

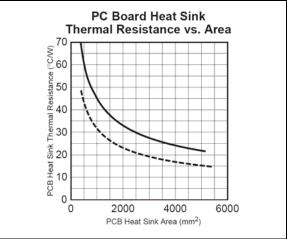


FIGURE 4-3:

Excerpt from the LDO Book.

EQUATION 4-3:

$$P_{DISS} = I_{SW}^{2} \times R_{SW(MAX)}$$

The worst case switch resistance ($R_{SW(MAX)}$) at the lowest V_{IN} of 3V is not available in the data sheet, so the next lowest value of V_{IN} is used.

 $R_{SW(MAX)}$ at 2.5V is 90 m $\!\Omega.$

If this were a figure for worst case $R_{SW(MAX)}$ for $25^{\circ}\text{C},$ an additional consideration is to allow for the maximum junction temperature of $125^{\circ}\text{C},$ the actual worst case resistance in this case can be 30% higher (See Figure 2-1). However, $90~\text{m}\Omega$ is the maximum over temperature.

EQUATION 4-4:

$$T_J = 2^2 \times 0.090 \times (90 + 53) + 50 = 101^{\circ}C$$

This is below the maximum of 125°C.

5.0 PACKAGING INFORMATION

5.1 Package Marking Information



TABLE 5-1: MARKING CODES

Part Number	Marking Code	Features		
MIC94040YFL-TR	P4	Fast Turn-On		
MIC94041YFL-TR	P1	Fast Turn-On, Load Discharge		
MIC94042YFL-TR	P2	Soft-Start		
MIC94043YFL-TR	P3	Soft-Start, Load Discharge		

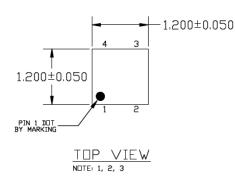
Legend	Y YY WW NNN @3 *	Product code or customer-specific information Year code (last digit of calendar year) Year code (last 2 digits of calendar year) Week code (week of January 1 is week '01') Alphanumeric traceability code Pb-free JEDEC® designator for Matte Tin (Sn) This package is Pb-free. The Pb-free JEDEC designator ((e3)) can be found on the outer packaging for this package. Pin one index is identified by a dot, delta up, or delta down (triangle)
Note:	be carried characters the corpor	nt the full Microchip part number cannot be marked on one line, it will dover to the next line, thus limiting the number of available for customer-specific information. Package may or may not include ate logo. (_) and/or Overbar (_) symbol may not be to scale.

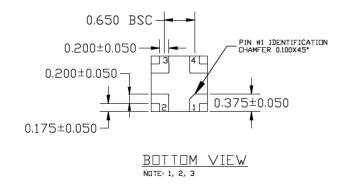
4-Lead FDFN Package Outline & Recommended Land Pattern

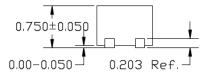
тттт.е

4 LEAD FDFN 1.2x1.2 mm PACKAGE (Flip Chip) OUTLINE & RECOMMENDED LAND PATTERN

DRAWING #	FDFN1212-4LD-PL-1	UNIT	MM
Lead Frame	NiPdAu	Lead Finish	NiPdAu



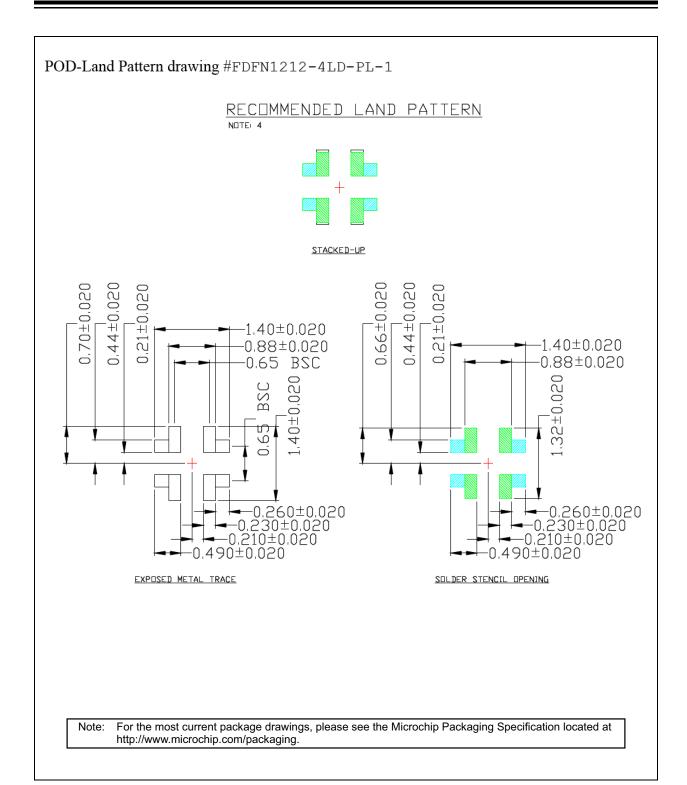




NOTE:

- 1. MAX PACKAGE WARPAGE IS 0.05 MM
- 2. MAX ALLOWABLE BURR IS 0.076MM IN ALL DIRECTIONS
- 3. PIN #1 IS ON TOP WILL BE LASER MARKED
- 4. CYAN SHADED AREAS INDICATE OPTIONAL SOLDER STENCIL OPENING FOR IMPROVED THERMAL PERFORMANCE

ote: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging.



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APPENDIX A: REVISION HISTORY

Revision A (November 2021)

- Converted Micrel document MIC94040/1/2/3 to Microchip data sheet template DS20006607A.
- Minor grammatical text changes throughout.

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

				Example	s:	
Device	<u>X</u>	<u>XX</u>	- <u>XX</u>	a) MIC940	040YFL-TR:	MIC94040, -40°C to +125°C
Part No.	Junction Temp. Range	Package	Media Type	h) M410046	044VEL TD:	Temperature Range, 4-Lead FDFN, 5,000/Reel
		mΩ R _{DS(ON)} 3A Higl h Fast Turn-On	h-Side Load Switch	b) MIC940	041YFL-TR:	MIC94041, –40°C to +125°C Temperature Range, 4-Lead FDFN, 5,000/Reel
Device:	MIC94041: 28 wit	mΩ R _{DS(ON)} 3A Higl h Fast Turn-On and	Load Discharge	c) MIC940	042YFL-TR:	MIC94042, –40°C to +125°C Temperature Range,
201100.	wit	mΩ R _{DS(ON)} 3A High h Soft-Start		d) MIC940	043YFL-TR:	4-Lead FDFN, 5,000/Reel MIC94043, –40°C to +125°C
	MIC94043: 28 wit	$m\Omega$ $R_{DS(ON)}$ 3A High Soft-Start and Loa	n-Side Load Switch d Discharge			Temperature Range, 4-Lead FDFN, 5,000/Reel
Junction Temperature	Y = -40°C to	+125°C, RoHS-Com	npliant			
Range:				Note 1:	catalog part nu	l identifier only appears in the umber description. This identifier is ing purposes and is not printed on
Package:	FL = 4-Lead 1	.2 mm x 1.2 mm FDI	FN		the device pac Sales Office fo	skage. Check with your Microchip or package availability with the
Media Type:	TR = 5,000/Re	eel			Tape and Ree	i option.

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