

N-channel SiC power MOSFET

V_{DSS}	1200V
R _{DS(on)} (Typ.)	62mΩ
I _D *1	26A
P_D	115W

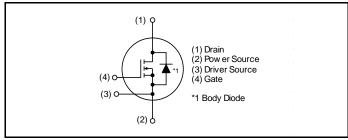
Outline



Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

•Inner circuit



Please note Driver Source and Power Source are not exchangeable. Their exchange might lead to malfunction.

Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Typo	Tape width (mm)	-
Type	Basic ordering unit (pcs)	30
	Taping code	C15
	Marking	SCT4062KR

◆Absolute maximum ratings (T. = 25°C).

•Absolute maximum ratings (1 _c = 25	0)			
Parameter		Symbol	Value	Unit
Drain - source voltage		V_{DSS}	1200	V
Continuous drain and source current	*1		26	А
$T_c = 100$ °C	$V_{GS} = V_{GS_on}$	I _D , I _S *1	18	А
Pulsed drain current	$V_{GS} = V_{GS_on}$	I _{D,pulse} *2	52	А
Body diode pulsed forward current	$V_{GS} = 0 V$	I _{S,pulse} *3	26	А
Body diode surge forward current	$V_{GS} = 0 V$	I _{S,pulse} *4	52	А
Gate - source voltage (DC)		V_{GSS}	-4 to +21	V
Gate - source surge voltage (t _{surge} < 300ns)		$V_{\rm GSS_surge}^{*5}$	-4 to +23	V
Recommended turn-on gate - source drive voltage		$V_{GS_on}^{^{*6}}$	+15 to +18	V
Recommended turn-off gate - source drive voltage		V_{GS_off}	0	V
Virtual junction temperature		T_{vj}	175	°C
Range of storage temperature		T _{stg}	-40 to +175	°C

ullet Electrical characteristics (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Values	Unit	
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown	V	$V_{GS} = 0 \text{ V}, I_D = 5.3 \text{mA}$				V
voltage	$V_{(BR)DSS}$	T _{vj} = 25°C	1200	-	-	V
		$V_{GS} = 0 \text{ V}, V_{DS} = 1200 \text{V}$				
Zero Gate voltage Drain current	I _{DSS}	T _{vj} = 25°C	-	1	80	μΑ
Diam current		T _{vj} = 150°C	-	10	-	
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +21V , V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current		$V_{GS} = -4V$, $V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	V _{GS (th)} *7	$V_{DS} = 10V, I_D = 6.45 \text{mA}$	2.8	1	4.8	V
		$V_{GS} = 18V, I_{D} = 12A$				_
Static Drain - Source on - state resistance	R _{DS(on)} *8	T _{vj} = 25°C	-	62	81	mΩ
on state resistance		T _{vj} = 150°C	-	124	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	-	4	-	Ω

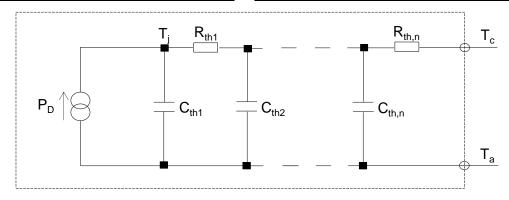
●Thermal resistance

Parameter	Symbol	Values			Unit
r atametet		Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R _{thJC} *9	-	0.98	1.3	K/W

● Typical Transient Thermal Characteristics

Symbol	Value	Unit
R _{th1}	1.7 ×10 ⁻¹	
R _{th2}	4.1 ×10 ⁻¹	K/W
R _{th3}	4.0 ×10 ⁻¹	

Symbol	Value	Unit
C _{th1}	3.6 ×10 ⁻⁴	
C _{th2}	1.5 × 10 ⁻³	Ws/K
C _{th3}	6.5 × 10 ⁻³	



Electrical characteristics ($T_{vj} = 25$ °C unless otherwise specified)

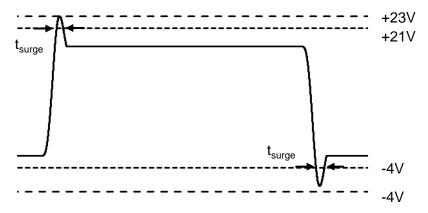
Doromotor	Symbol Conditions	Values			Unit	
Parameter		Conditions	Min.	Тур.	Max.	Offic
Transconductance	g fs *8	$V_{DS} = 10V, I_{D} = 12A$	-	8.3	-	S
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	1498	-	
Output capacitance	C _{oss}	V _{DS} = 800V	-	45	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	3	-	
Effective output capacitance, energy related	C _{o(er)}	$V_{GS} = 0V$ $V_{DS} = 0V \text{ to } 800V$	1	54	1	pF
Total Gate charge	Q _g *8	$V_{DS} = 800V$ $I_{D} = 12A$	ı	64	ı	
Gate - Source charge	Q _{gs} *8	$V_{GS} = 18V$	-	14	ı	nC
Gate - Drain charge	Q _{gd} *8	See Fig. 1-1, 1-2.	-	17	-	
Turn - on delay time	t _{d(on)} *8	$V_{DS} = 800V$ $I_{D} = 12A$	-	4.4	-	
Rise time	t _r *8	$V_{GS} = +18V / 0V$	-	11	-	200
Turn - off delay time	t _{d(off)} *8	$R_G = 0\Omega$, L = 250µH E_{on} includes diode	-	22	-	ns
Fall time	t _f *8	reverse recovery $L_{\sigma} = 50 \text{nH}, C_{\sigma} = 10 \text{pF}$	-	10	-	
Turn - on switching loss	E _{on} *8	See Fig. 2-1, 2-2, 2-3.	_	132	-	., 1
Turn - off switching loss	E _{off} *8		-	6	-	μJ

●Body diode electrical characteristics (Source-Drain) (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Values		Unit
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Forward voltage	V _{SD} *8	$V_{GS} = 0V, I_D = 12A$	ı	3.3	ı	V
Reverse recovery time	t _{rr} *8	$I_F = 12A$ $V_R = 800V$	ı	8.1	1	ns
Reverse recovery charge	Q _{rr} *8	di/dt = 3800A/µs	ı	105	ı	nC
Peak reverse recovery current	I _{rrm} *8	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	-	26	-	А

^{*1} Limited by maximum T_{vi} and for Max. R_{thJC} .

^{*5} Example of acceptable V_{GS} waveform



Please note especially when using driver source that V_{GSS_surge} must be in the range of absolute maximum rating.

- * 6 Please be advised not to use SiC-MOSFETs with V_{GS} below 10V as doing so may cause thermal runaway.
- *7 Tested after applying $V_{GS} = 21V$ for 100ms.
- *8 Pulsed
- *9 Measured conformable to JESD51-14.

See the application note "rthjc_measurement_and_usage_an-e.pdf". Link

 $URL: https://fscdn.rohm.com/en/products/databook/applinote/discrete/common/rthjc_measurement_and_usage_an-e.pdf$

^{*2} $P_W \le 10\mu s$, Duty cycle $\le 1\%$

^{*3} Only for body-diode, Repititive pulse, PW ≤ 500ns, Duty cycle ≤ 5%

^{*4} When used as a protective function, PW \leq 10 μ s

20

0

25

•Electrical characteristic curves

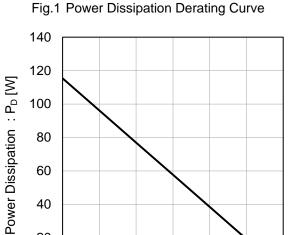


Fig.2 Maximum Safe Operating Area

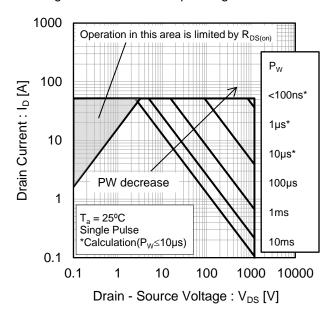


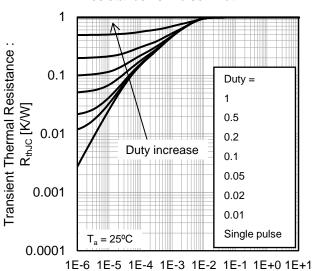
Fig.3 Typical Transient Thermal Resistance vs. Pulse Width

75

125

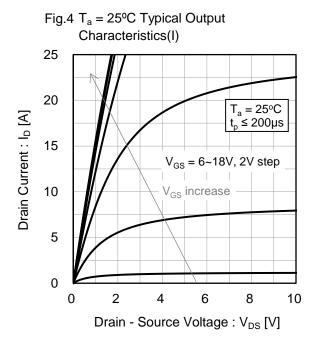
Case Temperature : T_C [°C]

175



1E-4 1E-3 1E-2 1E-1 1E+0 1E+1

Pulse Width : P_W [s]



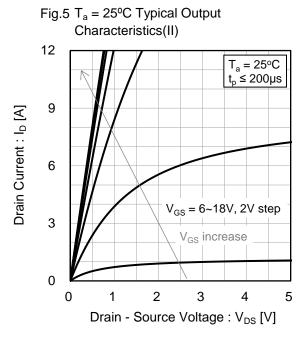
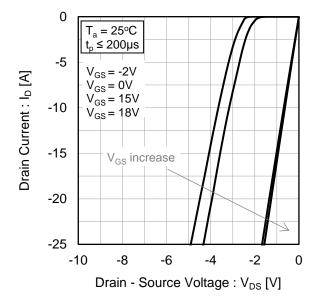


Fig.6 T_a = 25°C 3rd Quadrant Characteristics



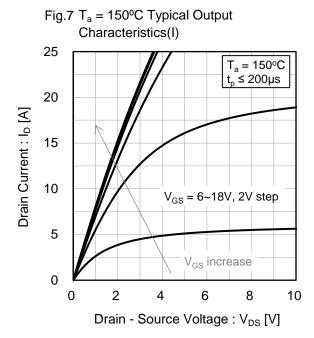
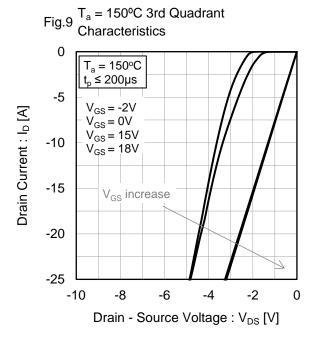
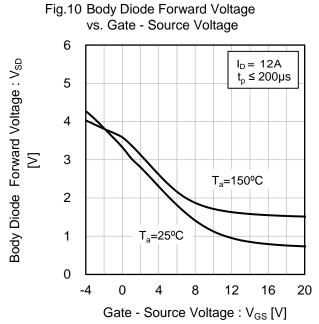


Fig.8 $T_a = 150^{\circ}C$ Typical Output Characteristics(II) 12 $T_a = 150^{\circ}C$ t_p ≤ 200µs 9 Drain Current: Ip [A] 6 3 V_{GS} = 6~18V, 2V step V_{GS} increase 0 3 0 5 Drain - Source Voltage: V_{DS} [V]





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Fig.11 Typical Transfer Characteristics (I)

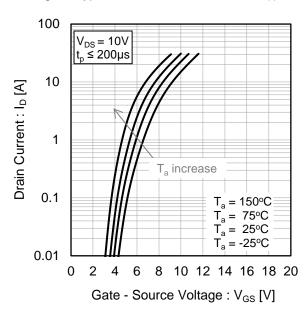


Fig.12 Typical Transfer Characteristics (II)

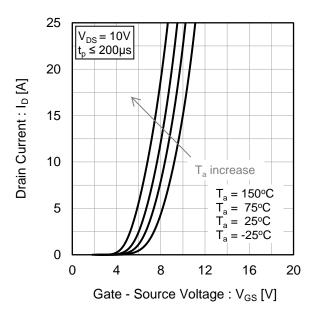


Fig.13 Gate Threshold Voltage vs. Junction Temperature

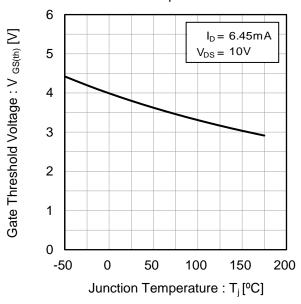


Fig.14 Transconductance vs. Drain Current

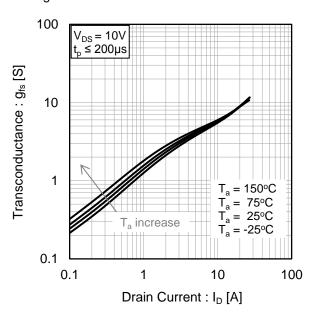


Fig.15 Static Drain - Source On - State Resistance vs. Gate - Source Voltage

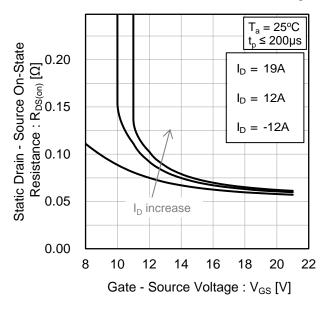


Fig.16 Static Drain - Source On - State Resistance vs. Junction Temperature

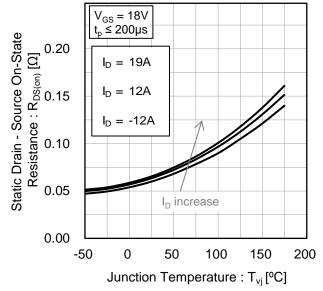


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current

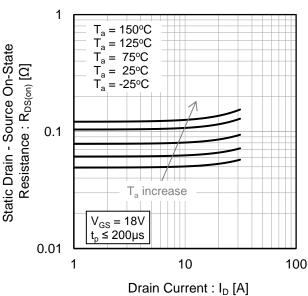
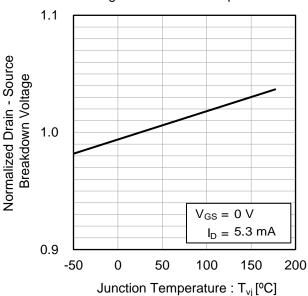
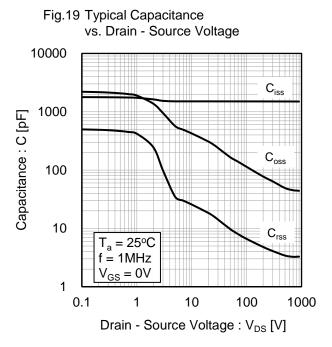


Fig.18 Normalized Drain - Source Breakdown Voltage vs. Junction Temperature





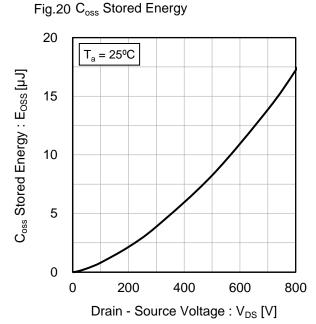


Fig.21 Dynamic Input Characteristics

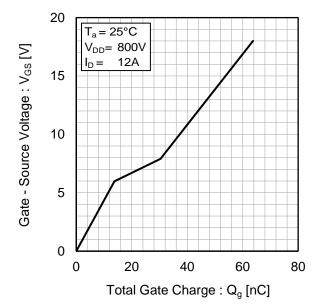


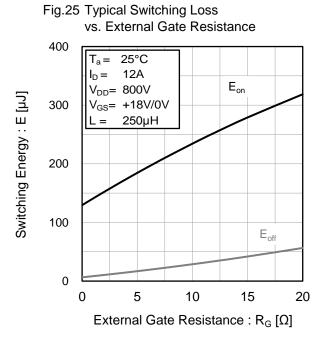
Fig.22 Typical Switching Time vs. External Gate Resistance 100 25°C 12A $I_D =$ V_{DD}= 800V 80 V_{GS}= +18V/0V $t_{d(off)}$ Switching Time: t [ns] 250µH 60 40 $t_{d(on)}$ ¦ t_{r_} 20 0 5 10 15 20

External Gate Resistance : R_G [Ω]

vs. Drain - Source Voltage 400 25°C 12A V_{GS}= +18V/0V Switching Energy: E [µJ] 300 $R_G = 0\Omega$ 250µH L = 200 E_{on} 100 $\mathsf{E}_{\mathrm{off}}$ 0 400 200 600 800 Drain - Source Voltage: V_{DS} [V]

Fig.23 Typical Switching Loss

Fig.24 Typical Switching Loss vs. Drain Current 400 25°C 800V +18V/0V Switching Energy: E [µJ] 300 0Ω $R_G =$ 250µH 200 E_{on} 100 $\mathsf{E}_{\mathrm{off}}$ 0 0 5 10 15 20 25 Drain Current: I_D [A]



Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

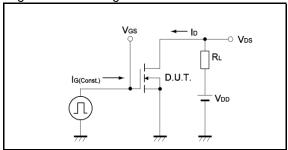


Fig.2-1 Switching Characteristics Measurement Circuit

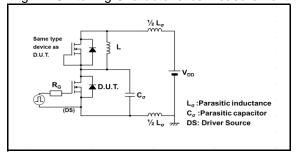


Fig.2-3 Waveforms for Switching Energy Loss

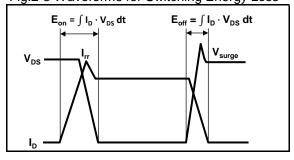


Fig.3-1 Reverse Recovery Time Measurement Circuit

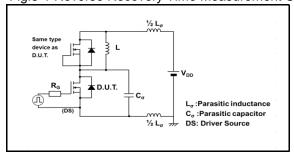


Fig.1-2 Gate Charge Waveform

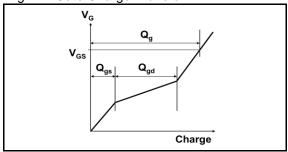


Fig.2-2 Waveforms for Switching Time

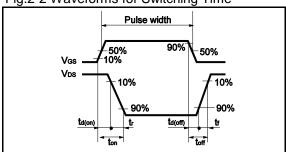
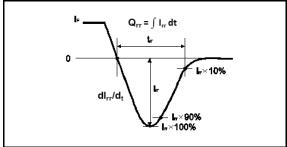
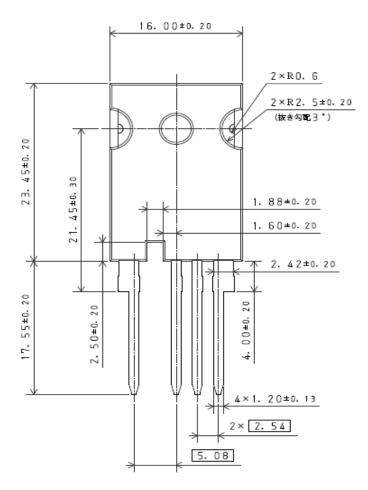
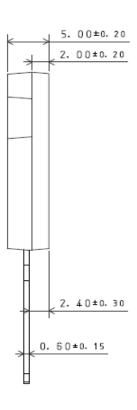


Fig.3-2 Reverse Recovery Waveform

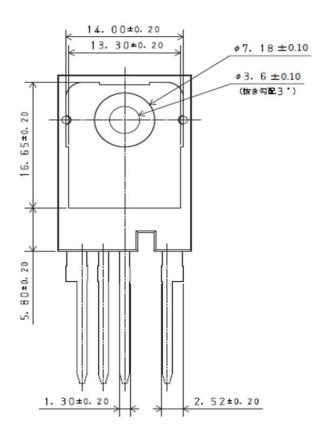


Package Dimensions



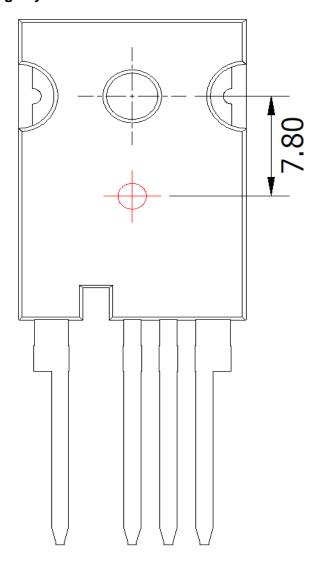


Unit: mm



Unit: mm

●Die Bonding Layout



- •Front view of the packaging.
- ·Dimensions are design values.
- ·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

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