

SCT4013DR N-channel SiC power MOSFET

V _{DSS}	750V
R _{DS(on)} (Typ.)	13mΩ
I_{D}^{*1}	105A
P _D	312W

• Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating ; RoHS compliant

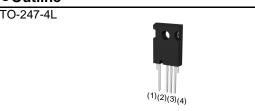
Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

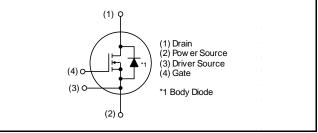
•Absolute maximum ratings ($T_c = 25^{\circ}C$)

Symbol Value Parameter Unit V Drain - source voltage V_{DSS} 750 Continuous drain and source current 105 А I_D, I_S^{*1} $V_{GS} = V_{GS_{on}}$ $T_c = 100^{\circ}C$ 74 А *2 Pulsed drain current $V_{GS} = V_{GS_{on}}$ 233 А I_{D,pulse} *3 $V_{GS} = 0 V$ 105 А Body diode pulsed forward current I_{S,pulse} *4 Body diode surge forward current $V_{GS} = 0 V$ 233 А I_{S,pulse} V_{GSS} V Gate - source voltage (DC) -4 to +21 Gate - source surge voltage (t_{surge} < 300ns) -4 to +23 V $V_{\text{GSS_surge}}$ +15 to +18 V Recommended turn-on gate - source drive voltage $V_{GS_{on}}$ V Recommended turn-off gate - source drive voltage V_{GS off} 0 T_{vi} Virtual junction temperature 175 °C °C Range of storage temperature -40 to +175 T_{stg}

Outline



Inner circuit



Please note Driver Source and Power Source are not exchangeable. Their exchange might lead to malfunction.

Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Tuno	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	30
	Taping code	C15
	Marking	SCT4013DR

•Electrical characteristics ($T_{vj} = 25^{\circ}C$ unless otherwise specified)

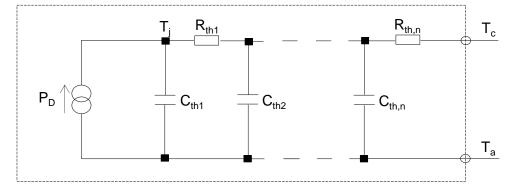
Doromotor	Sumbol	Conditions		Unit			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown	V	$V_{GS} = 0 V, I_D = 18.6 mA$				V	
voltage	V _{(BR)DSS}	T _{vj} = 25°C	750	-	-	V	
		$V_{GS} = 0 V, V_{DS} = 750V$					
Zero Gate voltage Drain current	I_{DSS}	T _{vj} = 25°C	-	1	80	μA	
		T _{vj} = 150°C	-	10	-		
Gate - Source leakage current	I _{GSS+}	V_{GS} = +21V , V_{DS} = 0V	-	-	100	nA	
Gate - Source leakage current		$V_{GS} = -4V$, $V_{DS} = 0V$	-	-	-100	nA	
Gate threshold voltage	$V_{GS(th)}{}^{*7}$	$V_{DS} = 10V, I_{D} = 30.8mA$	2.8	-	4.8	V	
		$V_{GS} = 18V, I_{D} = 58A$					
Static Drain - Source on - state resistance	${\sf R}_{\sf DS(on)}$ *8	T _{vj} = 25°C	-	13.0	16.9	mΩ	
		T _{vj} = 150°C	-	22.2	-		
Gate input resistance	R_G	f = 1MHz, open drain	-	1	-	Ω	

•Thermal resistance

Parameter	Symbol		Unit		
	Symbol	Min.	Тур.	Max.	Onit
Thermal resistance, junction - case	R_{thJC}^{*9}	-	0.37	0.48	K/W

•Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	1.1 ×10 ⁻¹		C _{th1}	7.8 ×10 ⁻⁴	
R _{th2}	1.3 ×10 ⁻¹	K/W	C _{th2}	3.1 ×10 ⁻³	Ws/K
R _{th3}	1.3 ×10 ⁻¹		C _{th3}	3.8 ×10 ⁻³	





•Electrical characteristics ($T_{vj} = 25$ °C unless otherwise specified)

Deremeter	Sympol	Symbol Conditions		Values			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Transconductance	g _{fs} *8	$V_{DS} = 10V, I_{D} = 58A$	-	32	-	S	
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	4580	-		
Output capacitance	C _{oss}	V _{DS} = 500V	-	203	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	10	-		
Effective output capacitance, energy related	C _{o(er)}	$V_{GS} = 0V$ $V_{DS} = 0V$ to 500V	-	263	-	pF	
Total Gate charge	Q _g *8	$V_{DS} = 500V$ $I_{D} = 58A$	-	170	-		
Gate - Source charge	Q _{gs} *8	$V_{GS} = 18V$	-	39	-	nC	
Gate - Drain charge	Q _{gd} *8	See Fig. 1-1, 1-2.	-	42	-		
Turn - on delay time	t _{d(on)} *8	$V_{DS} = 500V$ $I_{D} = 58A$	-	17	-		
Rise time	t _r *8	V _{GS} = +18V / 0V	-	32	-	20	
Turn - off delay time	t _{d(off)} *8	$R_G = 6.8\Omega, L = 250\mu H$ E _{on} includes diode	-	82	-	ns	
Fall time	t _f *8	reverse recovery $L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF	-	17	-		
Turn - on switching loss	E _{on} *8	See Fig. 2-1, 2-2, 2-3.	-	500	-		
Turn - off switching loss	E _{off} *8		-	310	-	μJ	



•Body diode electrical characteristics (Source-Drain) (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Unit		
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Onit
Forward voltage	V_{SD}^{*8}	$V_{GS} = 0V, I_D = 58A$	-	3.3	-	V
Reverse recovery time	t _{rr} *8	$I_F = 58A$ $V_R = 500V$	-	16	-	ns
Reverse recovery charge	Q _{rr} *8	di/dt = 3300A/µs	-	290	-	nC
Peak reverse recovery current	^{*8} ا	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	-	36	-	А

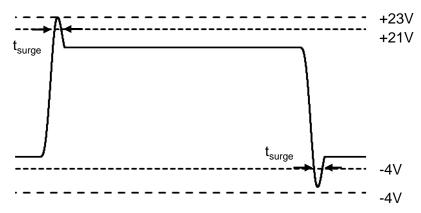
*1 Limited by maximum T_{vj} and for Max. R_{thJC} .

*2 $P_W \le 10\mu s$, Duty cycle $\le 1\%$

*3 Only for body-diode, Repititive pulse, PW \leq 500ns, Duty cycle \leq 5%

*4 When used as a protective function, PW \leq 10µs

*5 Example of acceptable V_{GS} waveform



Please note especially when using driver source that V_{GSS_surge} must be in the range of absolute maximum rating.

- *6 Please be advised not to use SiC-MOSFETs with V_{GS} below 10V as doing so may cause thermal runaway.
- *7 Tested after applying V_{GS} = 21V for 100ms.

*8 Pulsed

*9 Measured conformable to JESD51-14.

See the application note "rthjc_measurement_and_usage_an-e.pdf". Link

URL: https://fscdn.rohm.com/en/products/databook/applinote/discrete/common/rthjc_measurement_and_usage_an-e.pdf



 P_{W}

<100ns*

1µs*

10µs*

100µs

1ms

10ms

1000 10000

Electrical characteristic curves

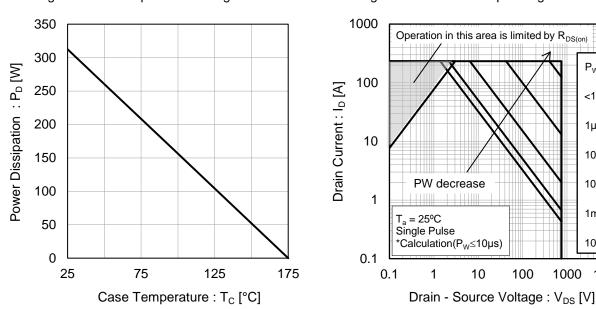
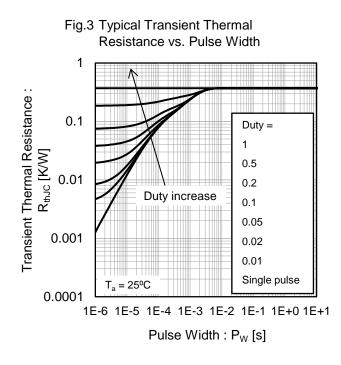


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area





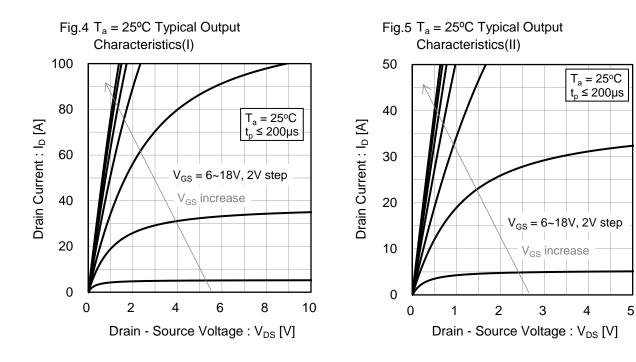
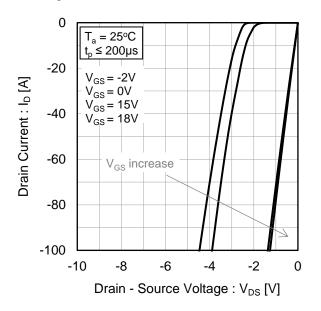
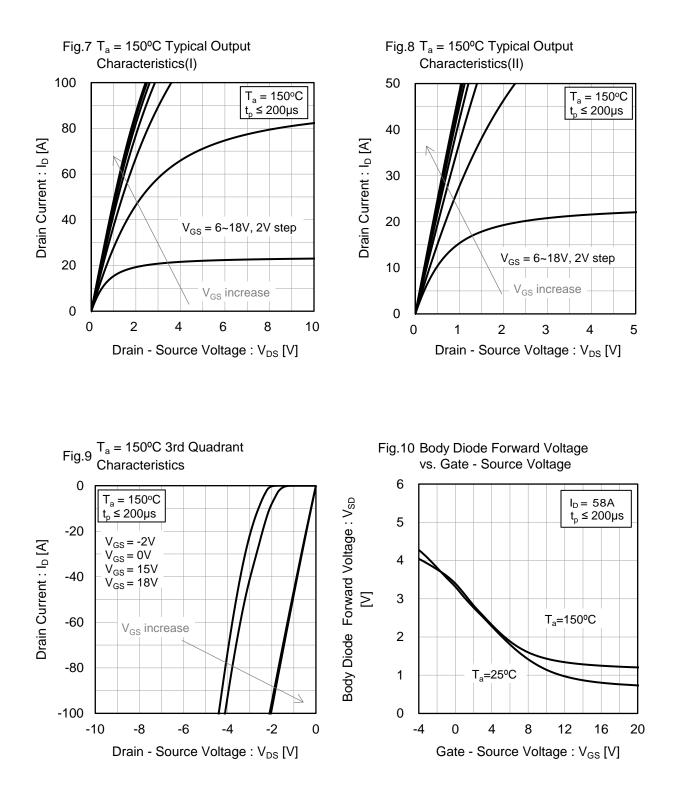


Fig.6 $T_a = 25^{\circ}C$ 3rd Quadrant Characteristics









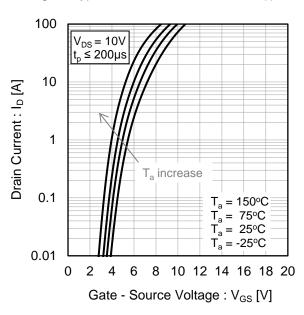
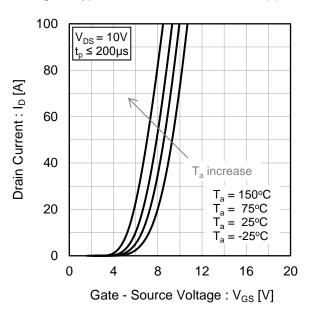


Fig.11 Typical Transfer Characteristics (I)

Fig.12 Typical Transfer Characteristics (II)



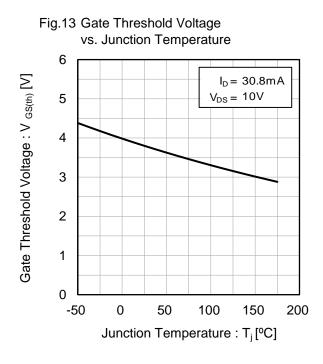
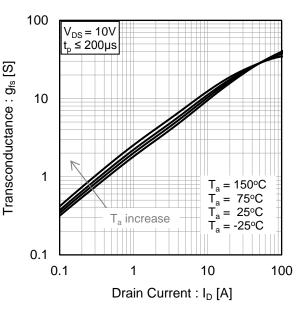
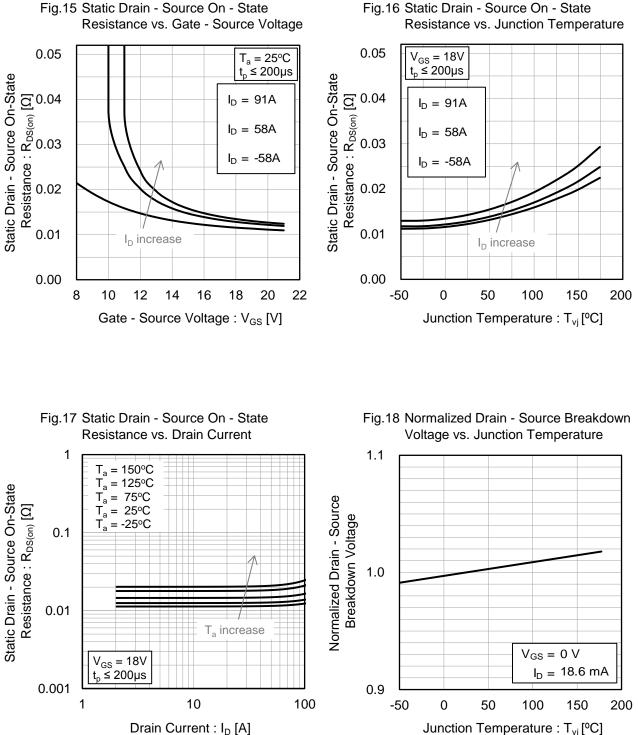


Fig.14 Transconductance vs. Drain Current









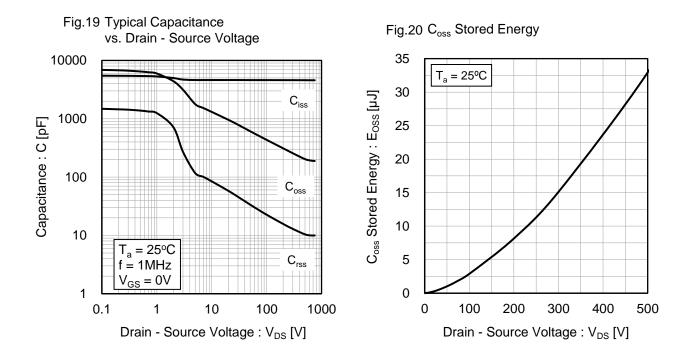
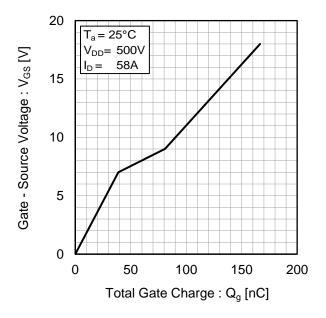
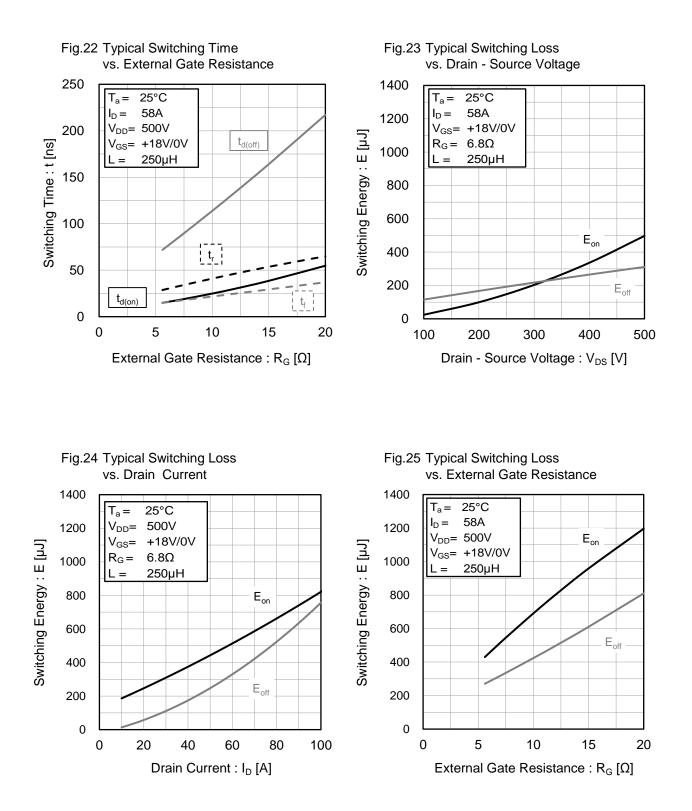


Fig.21 Dynamic Input Characteristics









Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

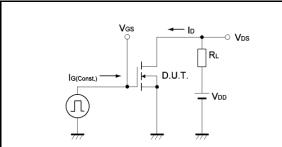


Fig.2-1 Switching Characteristics Measurement Circuit

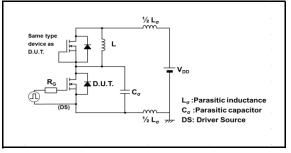


Fig.2-3 Waveforms for Switching Energy Loss

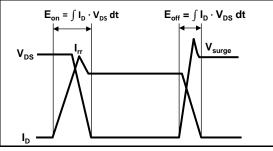


Fig.3-1 Reverse Recovery Time Measurement Circuit

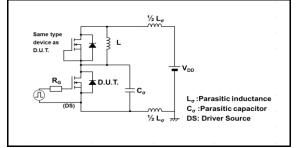


Fig.1-2 Gate Charge Waveform

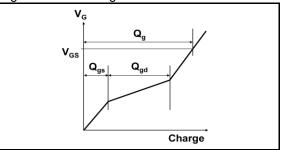


Fig.2-2 Waveforms for Switching Time

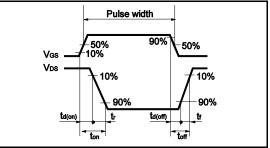
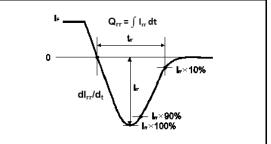
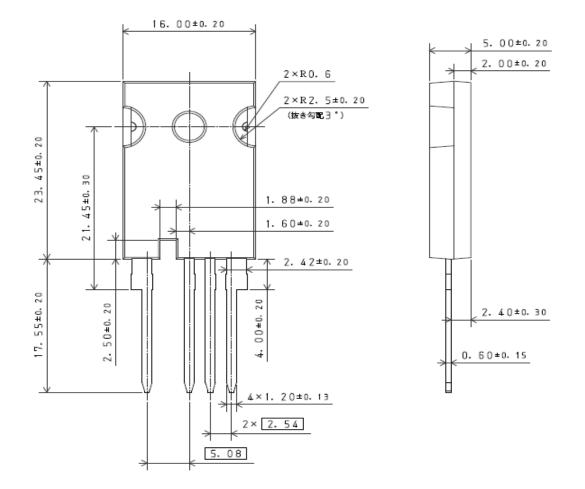


Fig.3-2 Reverse Recovery Waveform





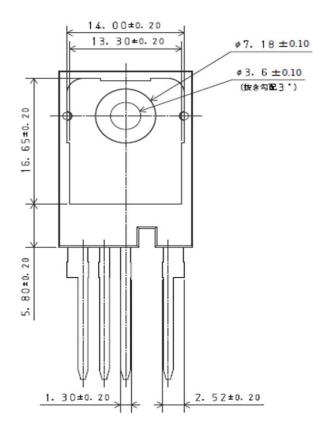
• Package Dimensions



Unit: mm





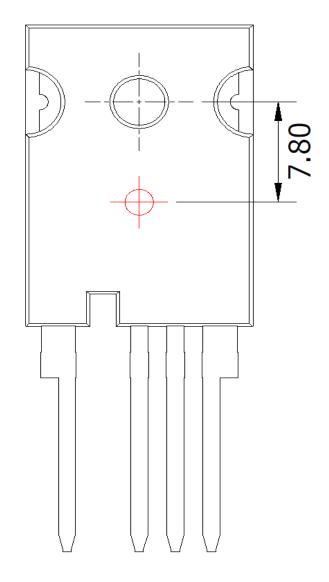


Unit: mm





•Die Bonding Layout



 $\boldsymbol{\cdot}$ Front view of the packaging.

·Dimensions are design values.

·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm



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