1. Information Note Basic Data					
1.1 Company	<b>ALLEGRO</b> microsystems	Allegro MicroSystems 955 Perimeter Rd, Manchester, New Hampshire 03103 U.S.A.			
1.2 Information Note No.		20220412TSMC62711			
1.3 Title of IN		A6271KLPTR-T-1 Dual Fab Source: TSMC and UMC			
1.4 Customer		Allegro Authorized Distributor			
1.5 Issue Date		12-Apr-22			

2. Contact							
2.1 Contact Supplier							
2.1.1 Name	Mark Caggiano	Mark Caggiano					
2.1.2 Phone	1-603-626-2300						
2.1.3 Email	mcaggiano@allegromicro.com						
2.2 Team Supplier (optional)							
2.2.1 Name (optional)	2.2.2 Phone (optional)	2.2.3 Email (optional)					

3. Description						
	Current	New				
Description #1		Allegro will be adding Taiwan Semiconductor Manufacturing Compnay (TSMC) as a dual source. TSMC will be the primary wafer fab site.				
Description #2						
Description #3						
Description #4						
Description #5						
Description #6						
Description #7						
Description #8						
Description #9						
Description #10						

4. Reason / Motivation					
4.1 Motivation Dual Fab source for supply security.					
	No change in form, fit, function, or reliability of the product(s) is expected, however validation of equivalence within a specific application is at the discretion of the customer.				

5. Marking of Parts / Traceability				
5.1 Description	No change in marking and traceability (AML Internal Part No. contains fab location)			

6. Timing / Schedule							
6.1 Intended Start of Delivery	1-Oct-2022	Timing may vary					

7. Attachments (e.g. additional documentation, pictures, part list)					

			1.	Information No	te Basic Data					
1.1 Company	<b></b>	EGRO microsystems	Allegro MicroSystems 955 Perimeter Rd, Manchester, New Hampshire 03103 U.S.A.							
1.2 Information Note No.			20220412TSMC	262711						
1.3 Title of IN					e: TSMC and UM	С				
1.4 Customer			Allegro Authoriz							
1.5 Issue Date			12-Apr-22							
1.5 155ac Bate			12 /\p. 22	8. Affected	Darts					
		8.1 Curre	- m t	8. Affecteu	raits		9.2 No/:	8.2 New (if applicable)		
	8.1.2 Supplier Catalog Part No.	8.1.3	8.1.4 Package Name	8.1.5 Part Description (optional)	<b>Additional Part</b>		8.1.3	8.2.4 Package Name	8.2.6 Additional Part Information (optional)	
A6271KLPTR-T-1	A6271KLPTR-T-1		LP-16							

1. Information Note Basic Data								
1.1 Company	<b>ALLEGRO</b> microsystems	Allegro MicroSystems 955 Perimeter Rd, Manchester, New Hampshire 03103 U.S.A.						
1.2 Information No	te No.	20220412TSMC62711						
1.3 Title of IN		A6271KLPTR-T-1 Dual Fab Source: TSMC and UMC						
1.4 Customer		Allegro Authorized Distributor						
1.5 Issue Date		12-Apr-22						

Form adopted from ZVEI Revision 5.0

Please note: It is our intention to inform our customer of changes as early as possible. Under Allegro's procedure for product/process change notification, Allegro strives, based on its technical judgment, to provide notification of significant changes that may affect form, fit, function, reliability/durability and processability/manufacturability. However, as Allegro cannot ensure evaluation of product/process changes for each and every application, the customer retains responsibility to validate the impact of a change on its application suitability. If samples are needed for validation of a change, requests may be made via the contact information provided herein. Please contact your Account Manager or Local Sales Representative for any questions.