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# PXI-5922

# Specifications

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2022-07-06



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# PXI-5922 Specifications

## Definitions

**Warranted** specifications describe the performance of a model under stated operating conditions and are covered by the model warranty. **Warranted** specifications account for measurement uncertainties, temperature drift, and aging. **Warranted** specifications are ensured by design, or verified during production and calibration.

The following characteristic specifications describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- **Typical** specifications describe the performance met by a majority of models.
- **Nominal** specifications describe an attribute that is based on design, conformance testing, or supplemental testing.
- **Measured (meas)** specifications describe the measured performance of a representative model.

Specifications are **Typical** unless otherwise noted.

## Conditions

Specifications are valid under the following conditions unless otherwise noted.

- Full operating temperature range
- All impedance selections
- All sample rates
- Source impedance  $\leq 50 \Omega$

Specifications are valid under the following conditions unless otherwise noted:

- Ambient temperatures of 15 °C to 35 °C

## Vertical

### Analog Input

|                    |  |
|--------------------|--|
| Number of channels | Software-selectable: two simultaneously sampling, single-ended or unbalanced differential channels or one differential channel |
| Connector          | BNC  |

### Impedance and Coupling

|                 |  |
|-----------------|--|
| Input impedance | Software-selectable: 50 $\Omega$ $\pm$ 2.0% or 1 M $\Omega$ $\pm$ 2.0% in parallel with a nominal capacitance of 60 pF |
| Input coupling  | AC, DC, GND  |

### Voltage Levels

|                               |   |
|-------------------------------|---|
| Full-scale (FS) input range   | $\pm$ 1 V (2 V <sub>pk-pk</sub> )<br>$\pm$ 5 V (10 V <sub>pk-pk</sub> ) |
| <b>Maximum input overload</b> |   |
| 50 $\Omega$                   | 7 V RMS with  Peaks  $\leq$ 10 V  |
| 1 M $\Omega$                  | Peaks  $\leq$ 42 V  |

## Accuracy

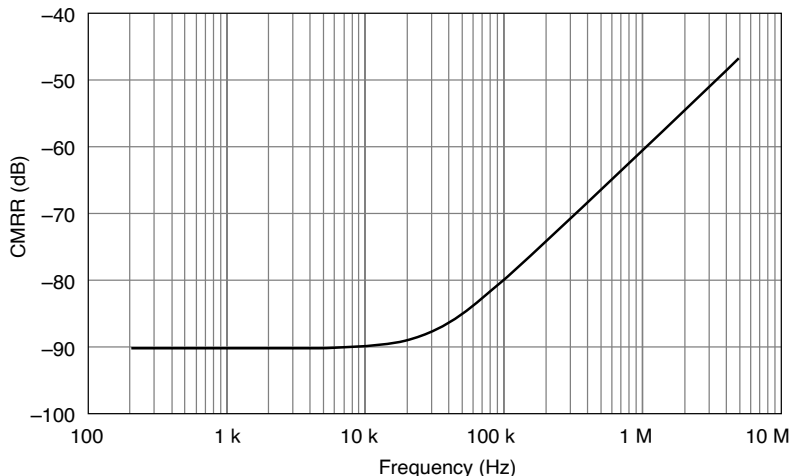
| Sample Rate | Resolution |
|-------------|------------|
| 50 kS/s     | 24 bits    |
| 500 kS/s    | 24 bits    |
| 1 MS/s      | 22 bits    |
| 5 MS/s      | 20 bits    |
| 10 MS/s     | 18 bits    |
| 15 MS/s     | 16 bits    |

Table 1. PXI-5922 Resolution

|                                  |   |
|----------------------------------|---|
| <b>DC accuracy<sup>[1]</sup></b> |   |
| 2 V <sub>pk-pk</sub> range       | $\pm(0.05\%$ of input + 50 $\mu\text{V}$ ), warranted                       |
| 10 V <sub>pk-pk</sub> range      | $\pm(0.05\%$ of input + 100 $\mu\text{V}$ ), warranted                      |
| <b>DC drift<sup>[2]</sup></b>    |   |
| 2 V <sub>pk-pk</sub> range       | $\pm(0.002\%$ of input + 5 $\mu\text{V}$ per $^{\circ}\text{C}$ ), nominal  |
| 10 V <sub>pk-pk</sub> range      | $\pm(0.002\%$ of input + 10 $\mu\text{V}$ per $^{\circ}\text{C}$ ), nominal |
| AC amplitude accuracy            | 0.06% at 1 kHz <sup>[3]</sup>   |
| <b>Crosstalk<sup>[4]</sup></b>   |   |
| At 100 kHz                       | $\leq -110$ dB  |
| At 1 MHz                         | $\leq -100$ dB  |
| At 6 MHz                         | $\leq -80$ dB   |

|                                    |                                  |
|------------------------------------|----------------------------------|
| Common-mode rejection ratio (CMRR) | 50 dB up to 1 kHz <sup>[5]</sup> |
|------------------------------------|----------------------------------|

Figure 1. PXI-5922 CMRR with Differential Terminal Configuration, Measured



## Bandwidth and Transient Response

|                      |                                 |
|----------------------|---------------------------------|
| Alias-free bandwidth | $0.4 \times \text{Sample Rate}$ |
|----------------------|---------------------------------|

| Sample Rate         | Attenuation |
|---------------------|-------------|
| <5 MS/s             | 100 dB      |
| 5 MS/s              | 96 dB       |
| (5 MS/s, 7.5 MS/s)  | 90 dB       |
| [7.5 MS/s, 15 MS/s] | 80 dB       |

Table 2. Alias Protection<sup>[6]</sup>

|                            |       |
|----------------------------|-------|
| AC coupling cutoff (-3 dB) | 90 Hz |
|----------------------------|-------|

| Sample Rate | 50 Ω and 1 MΩ |
|-------------|---------------|
| 1 MS/s      | 0.03 dB       |
| 5 MS/s      | 0.06 dB       |
| 10 MS/s     | 0.15 dB       |

|             |               |  |
|-------------|---------------|--|
| Sample Rate | 50 Ω and 1 MΩ |  |
| 15 MS/s     | 0.3 dB        |  |

Table 3. Passband Flatness<sup>[7]</sup>

Figure 2. 100 kS/s Frequency Response, Measured

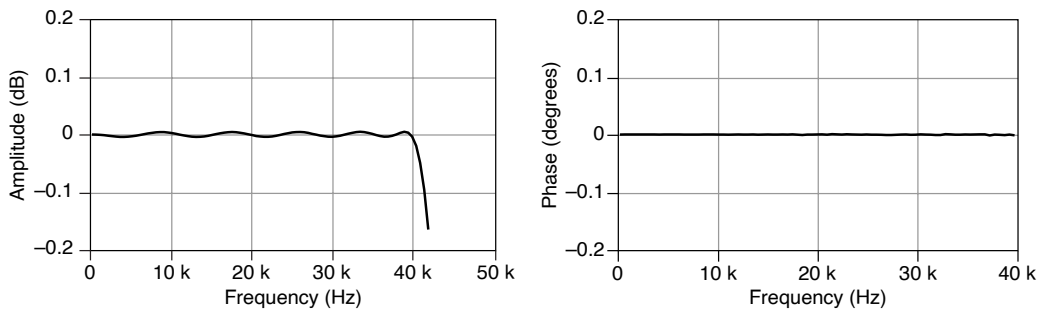


Figure 3. 1 MS/s Frequency Response, Measured

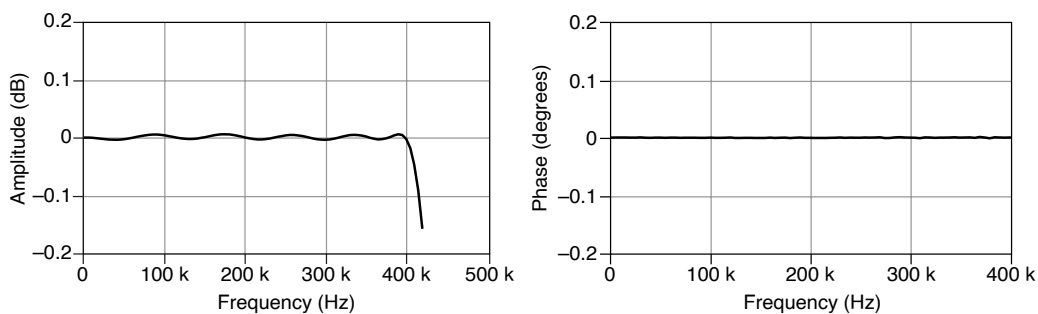
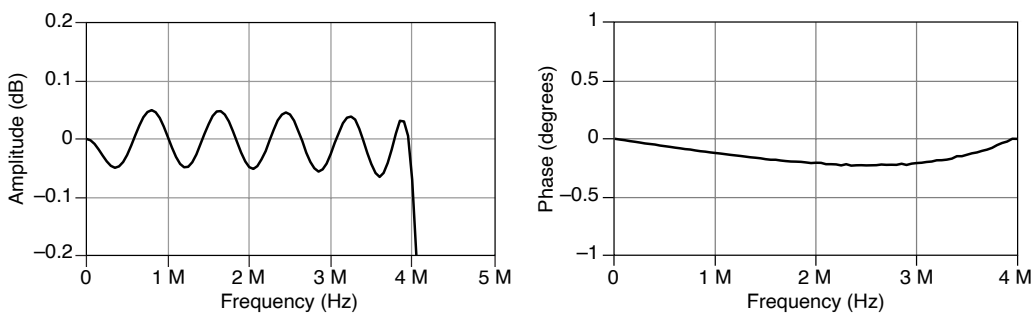


Figure 4. 10 MS/s Frequency Response, Measured



## Spectral Characteristics

|                 |                       |                      |
|-----------------|-----------------------|----------------------|
| Input Frequency | Range                 |                      |
|                 | 10 V <sub>pk-pk</sub> | 2 V <sub>pk-pk</sub> |
| 10 kHz          | 114 dBc               | 109 dBc              |

| Input Frequency | Range                 |                      |
|-----------------|-----------------------|----------------------|
|                 | 10 V <sub>pk-pk</sub> | 2 V <sub>pk-pk</sub> |
| 100 kHz         | 110 dBc               | 103 dBc              |
| 1 MHz           | 96 dBc                | 92 dBc               |

Table 4. Spurious-Free Dynamic Range (SFDR)<sup>[8]</sup>

Figure 5. PXI-5922 Dynamic Performance with 10 kHz Input Signal, Measured, 1 MΩ, 10 V<sub>pk-pk</sub> Range, 500 kS/s, Unbalanced Differential, 10,000-Point FFT with 10 Averages

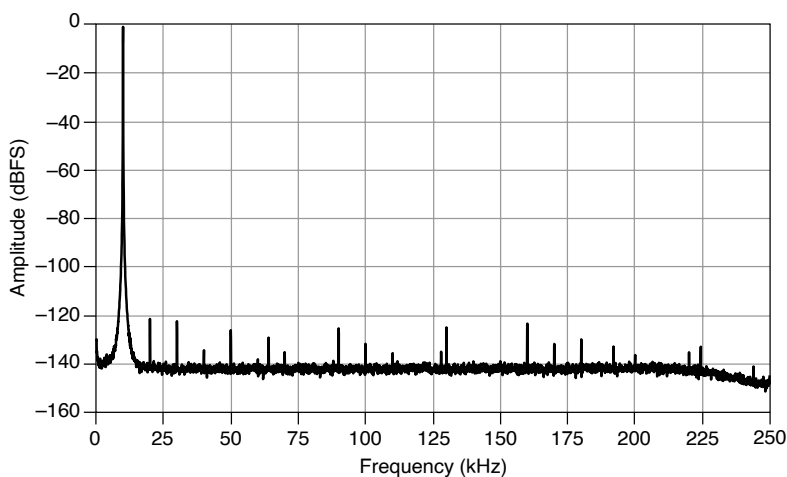
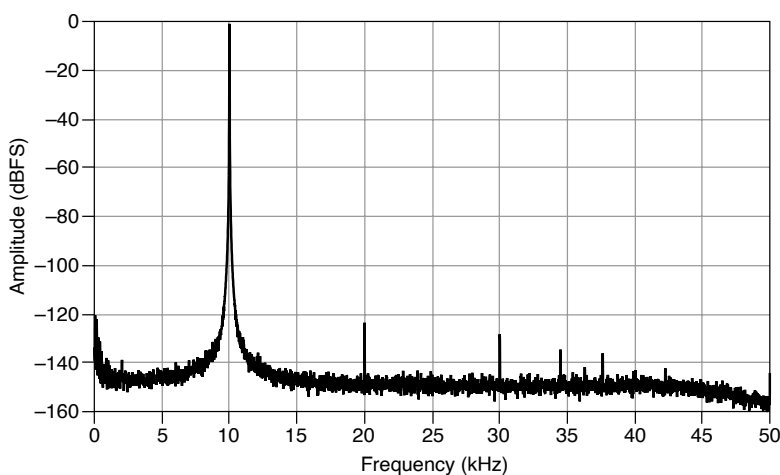


Figure 1. PXI-5922 Dynamic Performance with 10 kHz Input Signal, Measured, 1 MΩ, 2 V<sub>pk-pk</sub> Range, 100 kS/s, Unbalanced Differential, 10,000-Point FFT with 10 Averages



| Input Frequency | Range                 |                      |
|-----------------|-----------------------|----------------------|
|                 | 10 V <sub>pk-pk</sub> | 2 V <sub>pk-pk</sub> |
| 10 kHz          | -112 dBc              | -107 dBc             |



| Input Frequency | Range                 |                      |
|-----------------|-----------------------|----------------------|
|                 | 10 V <sub>pk-pk</sub> | 2 V <sub>pk-pk</sub> |
| 100 kHz         | -108 dBc              | -101 dBc             |
| 1 MHz           | -94 dBc               | -90 dBc              |

Table 5. Total Harmonic Distortion (THD)<sup>[9]</sup>

| Sample Rate | Range                 |                      |
|-------------|-----------------------|----------------------|
|             | 10 V <sub>pk-pk</sub> | 2 V <sub>pk-pk</sub> |
| 1 MS/s      | 105 dB                | 99 dB                |
| 10 MS/s     | 89 dB                 | 87 dB                |

Table 6. Signal-to-Noise and Distortion (SINAD)<sup>[10]</sup>

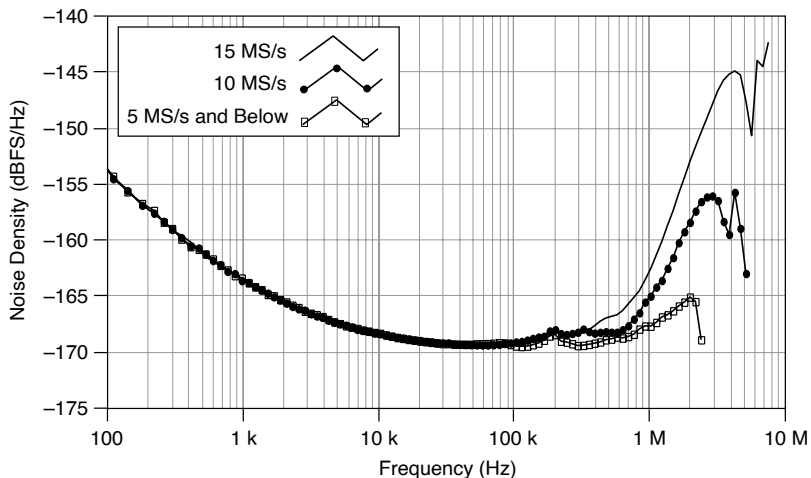
| Sample Rate | Range                 |                      |
|-------------|-----------------------|----------------------|
|             | 10 V <sub>pk-pk</sub> | 2 V <sub>pk-pk</sub> |
| 1 MS/s      | 108 dB                | 104 dB               |
| 10 MS/s     | 91 dB                 | 90 dB                |

Table 7. Signal-to-Noise Ratio (SNR) without Harmonics<sup>[11]</sup>

| Sample Rate | Range                 |                   |                      |                   |
|-------------|-----------------------|-------------------|----------------------|-------------------|
|             | 10 V <sub>pk-pk</sub> |                   | 2 V <sub>pk-pk</sub> |                   |
|             | dBFS                  | μV <sub>rms</sub> | dBFS                 | μV <sub>rms</sub> |
| 50 kS/s     | -120                  | 3.4               | -117                 | 1.0               |
| 100 kS/s    | -118                  | 4.3               | -115                 | 1.2               |
| 1 MS/s      | -108                  | 13                | -104                 | 4.2               |
| 5 MS/s      | -101                  | 31                | -98                  | 8.7               |
| 10 MS/s     | -91                   | 92                | -91                  | 20                |
| 15 MS/s     | -79                   | 401               | -79                  | 80                |

Table 8. RMS Noise, Warranted<sup>[12]</sup>

Figure 7. PXI-5922 Noise Density, Measured



## Skew, Input Bias Current

|   |                    |
|---|--------------------|
| Channel-to-channel skew <sup>[13]</sup> | ≤500 ps            |
| Input bias current <sup>[14]</sup>      | ≤500 nA, warranted |

## Settling Time

| Filter Type <sup>[16]</sup> | 1%     | 0.01%  |
|-----------------------------|--------|--------|
| 48 Tap Standard             | 800 ns | 2.5 μs |
| 48 Tap Hanning              | 700 ns | 1.5 μs |
| 16 Tap Hanning              | 300 ns | 1.4 μs |
| 8 Tap Hanning               | 200 ns | 1.3 μs |

Table 9. Settling Time<sup>[15]</sup>

Figure 8. PXI-5922 Step Response Using Different Filter Types, Measured<sup>[17]</sup>

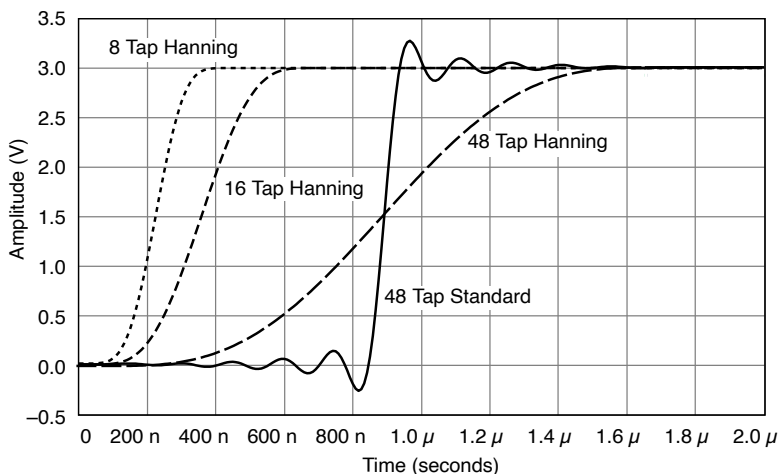
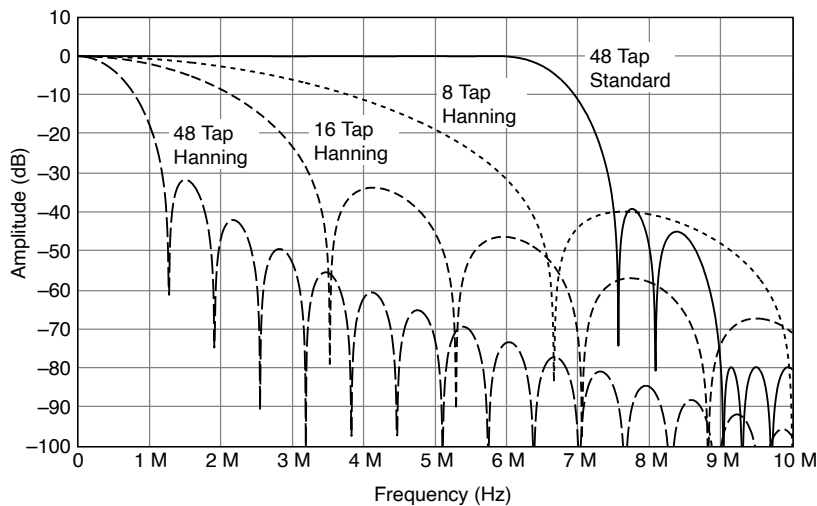


Figure 9. PXI-5922 Frequency Response Using Different Filter Types, Measured



## Horizontal Sample Clock

|         |  |
|---------|--|
| Sources | Internal onboard clock (internal VCXO) <sup>[18]</sup> |
|---------|--|

## Onboard Clock (Internal VCXO)

|   |                             |
|---|-----------------------------|
| Sample rate range, real-time sampling (single shot) <sup>[19]</sup>   | 50 kS/s to 15 MS/s          |
| <b>Phase noise density (5 MHz input signal)</b><br>At 10 kHz <-133 dBc/Hz<br>At 100 kHz <-145 dBc/Hz  |                             |
| Sample clock jitter <sup>[20]</sup>   | ≤3 ps RMS (100 Hz to 1 MHz) |
| Timebase frequency  | 120 MHz                     |
| <b>Timebase accuracy</b><br>Not phase-locked to Reference clock ±50 ppm, warranted<br>Phase-locked to Reference clock Equal to the Reference clock accuracy |                             |
| Sample clock delay range  | ±1 Sample clock period      |
| Sample clock delay resolution   | 400 ps                      |

## Phase-Locked Loop (PLL) Reference Clock

|                         |   |
|-------------------------|---|
| Reference clock sources | PXI_CLK 10 (backplane connector)<br>CLK IN (front panel SMB connector)            |
| Frequency range         | 1 MHz to 20 MHz in 1 MHz increments <sup>[21]</sup> ; must be accurate to ±50 ppm |

|                                       |   |
|---------------------------------------|---|
| Duty cycle tolerance                  | 45% to 55%  |
| Exported Reference clock destinations | CLK OUT (front panel SMB connector)<br>PFI <0..1> (front panel 9-pinmini-circular DIN connector)<br>PXI_TRIG <0..6> (backplane connector) |

### CLK IN (Reference Clock Input, Front Panel Connector)

|                        |   |
|------------------------|---|
| Input voltage range    | Square wave: $0.2 V_{pk-pk}$ to $1 V_{pk-pk}$ |
| Maximum input overload | 7 V RMS with $ Peaks  \leq 10 V$              |
| Impedance              | 50 $\Omega$                                   |
| Coupling               | AC  |

### CLK OUT (Reference Clock Output, Front Panel Connector)

|                       |             |
|-----------------------|-------------|
| Output impedance      | 50 $\Omega$ |
| Logic type            | 5 V CMOS    |
| Maximum drive current | $\pm 50$ mA |

## Trigger

### Reference (Stop) Trigger

|               |      |
|---------------|------|
| Trigger types | Edge |
|---------------|------|

|                 |  |
|-----------------|--|
|                 | Window<br>Hysteresis<br>Digital<br>Immediate<br>Software   |
| Trigger sources | CH 0<br>CH 1<br>TRIG<br>PXI_Trig <0..6><br>PFI <0..1><br>PXI Star Trigger<br>RTSI <0..6><br>Software |
| Time resolution | Sample clock period  |
| Rearm time      | $144 \times \text{Sample clock period}$ <sup>[22]</sup>  |
| Holdoff         | Up to $(2^{32} - 1) \times \text{Sample clock period}$   |

#### Related information

- [Refer to the NI High-Speed Digitizers Help for more information about the sources available for each trigger type.](#)

## Analog Trigger

|  |  |
|--|--|
| Trigger types  | Edge<br><br>Window<br><br>Hysteresis   |
| Sources <sup>[23]</sup>  | CH 0 (front panel BNC connector)<br><br>CH 1 (front panel BNC connector)<br><br>TRIG (front panel BNC connector) |
| Trigger level range  | 100% FS  |
| <b>Edge trigger sensitivity</b><br>CH 0, CH 1 <span style="float: right;">2% FS</span><br><br>TRIG (external trigger) <span style="float: right;">0.3 V<sub>pk-pk</sub> up to 1 MHz</span> |  |
| Jitter   | Sample clock period  |

## Digital Trigger

|              |   |
|--------------|---|
| Trigger type | Digital   |
| Sources      | PXI_TRIG <0..6> (backplane connector)<br><br>PFI <0..1> (front panel 9-pin DIN connector)<br><br>PXI Star Trigger (backplane connector) |

## External Trigger

|                        |  |
|------------------------|--|
| Source                 | TRIG (front panel BNC connector)               |
| Impedance              | 100 k $\Omega$ in parallel with 52 pF, nominal |
| Input voltage range    | $\pm 2.5$ V                                    |
| Coupling               | DC   |
| Level accuracy         | $\pm 0.3$ V up to 100 kHz                      |
| Maximum input overload | Peaks  $\leq 42$ V                             |

## PFI 0 and PFI 1 (Programmable Function Interface, AUX Front Panel Connectors)

|           |                         |
|-----------|-------------------------|
| Connector | 9-pin mini-circular DIN |
| Direction | Bidirectional           |

### As an Input (Trigger)

|                 |   |
|-----------------|---|
| Destinations    | Start trigger (acquisition arm)<br>Reference (stop) trigger<br>Arm Reference trigger<br>Advance trigger |
| Input impedance | 150 k $\Omega$ , nominal  |



|                        |               |
|------------------------|---------------|
| $V_{IH}$               | 2.0 V         |
| $V_{IL}$               | 0.8 V         |
| Maximum input overload | -0.5 V, 5.5 V |
| Maximum frequency      | 25 MHz        |

### As an Output (Event)

|                       |   |
|-----------------------|---|
| Sources               | Start trigger (acquisition arm)<br>Reference (stop) trigger<br>End of Record<br>Done (end of acquisition) |
| Output impedance      | 50 $\Omega$   |
| Logic type            | 3.3 V CMOS  |
| Maximum drive current | $\pm 24$ mA   |
| Maximum frequency     | 20 MHz  |

### Waveform Specifications

|                            |              |
|----------------------------|--------------|
| <b>Onboard memory size</b> |              |
| 8 MB/channel               | 2 MS/channel |
| 32 MB/channel              | 8 MS/channel |

|   |  |
|---|--|
| 256 MB/channel  | 64 MS/channel  |
| Minimum record length   | 1 Sample   |
| Number of pretrigger samples                                      | 0 up to full Record Length for both single-record mode and multiple-record mode  |
| Number of posttrigger samples                                     | 0 up to full Record Length for both single-record mode and multiple-record mode  |
| <b>Maximum number of records in onboard memory<sup>[24]</sup></b> |  |
| 8 MB/channel  | 13,107   |
| 32 MB/channel   | 52,428   |
| 256 MB/channel  | 100,000  |
| Allocated onboard memory per record                               | <b>(Record Length × 4 bytes/S) + 400 bytes</b> , rounded up to next multiple of 128 bytes or 640 bytes, whichever is greater |

## Calibration

|  |  |
|--|--|
| Self-calibration                           | Self-calibration is done on software command. The calibration corrects for gain and offset for all input ranges, input bias current, and nonlinearities in the ADCs. |
| External calibration (factory calibration) | The external calibration calibrates the VCXO and the voltage reference. Appropriate constants are stored in nonvolatile memory.                                      |
| Interval for external calibration          | 2 years  |

|              |            |
|--------------|------------|
| Warm-up time | 15 minutes |
|--------------|------------|

## Software

### Driver Software

Driver support for this device was first available in NI-SCOPE2.8.

NI-SCOPE is an IVI-compliant driver that allows you to configure, control, and calibrate the PXI-5922. NI-SCOPE provides application programming interfaces for many development environments.

### Application Software

NI-SCOPE provides programming interfaces, documentation, and examples for the following application development environments:

- LabVIEW
- LabWindows™/CVI™
- Measurement Studio
- Microsoft Visual C/C++
- .NET (C# and VB.NET)

### Interactive Soft Front Panel and Configuration

When you install NI-SCOPE on a 64-bit system, you can monitor, control, and record measurements from the PXI-5922 using InstrumentStudio.

InstrumentStudio is a software-based front panel application that allows you to perform interactive measurements on several different device types in a single program.



**Note** InstrumentStudio is supported only on 64-bit systems. If you are using a 32-bit system, use the NI-SCOPE-specific soft front panel instead of InstrumentStudio.

Interactive control of the PXI-5922 was first available via InstrumentStudio in NI-SCOPE18.0 and via the NI-SCOPE SFP in NI-SCOPE2.2. InstrumentStudio and the NI-SCOPE SFP are included on the NI-SCOPE media.

NI Measurement & Automation Explorer (MAX) also provides interactive configuration and test tools for the PXI-5922. MAX is included on the driver media.

## TClk Specifications

You can use the NI TClk synchronization method and the NI-TClk driver to align the Sample clocks on any number of supported devices, in one or more chassis. For more information about TClk synchronization, refer to the **NI-TClk Synchronization Help**, which is located within the **NI High-Speed Digitizers Help**. For other configurations, including multichassis systems, contact NI Technical Support at [ni.com/support](http://ni.com/support).

### Intermodule SMC Synchronization Using NI-TClk for Identical Modules

Specifications are valid under the following conditions:

- Any number of PXI modules installed in one NI PXI-1042 chassis.
- All parameters set to identical values for each SMC-based module.
- Sample clock set to 15 MS/s and all filters disabled.

|  |        |
|--|--------|
| Skew <sup>[25]</sup>                     | 500 ps |
| Average skew after manual adjustment     | <10 ps |
| Sample clock delay/adjustment resolution | ≤5 ps  |

#### Related information

- [For information about manual adjustment, refer to the Synchronization Repeatability Optimization topic in the NI-TClk Synchronization Help within the NI High-Speed Digitizers Help.](#)

- For additional help with the adjustment process, contact NI Technical support at [ni.com/support](http://ni.com/support).

## Power

| <b>Current draw</b> |        |
|---------------------|--------|
| +3.3 V DC           | 2.0 A  |
| +5 V DC             | 1.4 A  |
| +12 V DC            | 330 mA |
| -12 V DC            | 280 mA |
| Total power         | 20.9 W |

## Physical

|            |   |
|------------|---|
| Dimensions | 3U, one-slot, PXI/cPCI module<br><br>21.6 cm × 2.0 cm × 13.0 cm<br><br>(8.5 in × 0.8 in × 5.1 in) |
| Weight     | 336 g (11.8 oz)   |

## Environment

|                  |  |
|------------------|--|
| Maximum altitude | 2,000 m (at 25 °C ambient temperature) |
| Pollution Degree | 2                                      |

Indoor use only.

## Operating Environment

|                           |   |
|---------------------------|---|
| Ambient temperature range | 0 °C to 55 °C in all NI PXI chassis except the following: 0 °C to +45 °C when installed in an NI PXI-1000/B or PXI-101x chassis. (Tested in accordance with IEC 60068-2-1 and IEC 60068-2-2.) |
| Relative humidity range   | 10% to 90%, noncondensing (Tested in accordance with IEC 60068-2-56.)   |

## Storage Environment

|                           |  |
|---------------------------|--|
| Ambient temperature range | -40 °C to 71 °C (Tested in accordance with IEC 60068-2-1 and IEC 60068-2-2.) |
| Relative humidity range   | 5% to 95%, noncondensing (Tested in accordance with IEC 60068-2-56.)         |

## Shock and Vibration

|                   |   |
|-------------------|---|
| Operational shock | 30 g peak, half-sine, 11 ms pulse (Tested in accordance with IEC 60068-2-27. Test profile developed in accordance with MIL-PRF-28800F.) |
| Storage Shock     | 50 g, half-sine, 11 ms pulse (Tested in accordance with IEC 60068-2-27. Test profile developed in accordance with MIL-PRF-28800F.)      |

### Random vibration

Operating 5 Hz to 500 Hz, 0.31 g<sub>rms</sub> (Tested in accordance with IEC 60068-2-64.)

Nonoperating 5 Hz to 500 Hz, 2.46 g<sub>rms</sub> (Tested in accordance with IEC 60068-2-64. Test profile exceeds the requirements of MIL-PRF-28800F, Class 3.)

## Compliance and Certifications

### Safety Compliance Standards

This product is designed to meet the requirements of the following electrical equipment safety standards for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA C22.2 No. 61010-1



**Note** For safety certifications, refer to the product label or the [Product Certifications and Declarations](#) section.

### Electromagnetic Compatibility

This product meets the requirements of the following EMC standards for electrical equipment for measurement, control, and laboratory use:

- EN 61326-1 (IEC 61326-1): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions
- EN 55022 (CISPR 22): Class A emissions
- EN 55024 (CISPR 24): Immunity
- AS/NZS CISPR 11: Group 1, Class A emissions
- AS/NZS CISPR 22: Class A emissions
- FCC 47 CFR Part 15B: Class A emissions
- ICES-001: Class A emissions



**Note** In the United States (per FCC 47 CFR), Class A equipment is intended for use in commercial, light-industrial, and heavy-industrial locations.

In Europe, Canada, Australia, and New Zealand (per CISPR 11), Class A equipment is intended for use only in heavy-industrial locations.



**Note** Group 1 equipment (per CISPR 11) is any industrial, scientific, or medical equipment that does not intentionally generate radio frequency energy for the treatment of material or inspection/analysis purposes.



**Note** For EMC declarations, certifications, and additional information, refer to the [Product Certifications and Declarations](#) section.

## Product Certifications and Declarations


Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for NI products, visit [ni.com/product-certifications](https://ni.com/product-certifications), search by model number, and click the appropriate link.

## Environmental Management

NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.


For additional environmental information, refer to the **Engineering a Healthy Planet** web page at [ni.com/environment](https://ni.com/environment). This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

## EU and UK Customers

-  Waste Electrical and Electronic Equipment (WEEE)—At the end of the product life cycle, all NI products must be disposed of according to local laws and regulations. For more information about how to recycle NI products in your region, visit [ni.com/environment/weee](https://ni.com/environment/weee).



## 电子信息产品污染控制管理办法 ( 中国 RoHS )

-  中国 RoHS— NI 符合中国电子信息产品中限制使用某些有害物质指令(RoHS)。关于 NI 中国 RoHS 合规性信息，请登录 [ni.com/environment/rohs\\_china](http://ni.com/environment/rohs_china)。(For information about China RoHS compliance, go to [ni.com/environment/rohs\\_china](http://ni.com/environment/rohs_china).)

1 1 M $\Omega$  input impedance; within  $\pm 5$  °C of self-calibration temperature.

2 1 M $\Omega$  input impedance.

3 1 M $\Omega$  input impedance; within  $\pm 5$  °C of self-calibration temperature.

4 CH 0 to/from CH 1, External Trigger to CH 0 or CH 1.

5 Unbalanced differential input terminal configuration.

6 Input frequencies  $\geq 0.6 \times$  **Sample Rate**.

7 Referenced to DC; input frequencies up to  $0.4 \times$  **Sample Rate**.

8 -1 dBFS input signal; **Sample Rate** is  $10 \times$  input frequency; within  $\pm 2$  °C of self-calibration temperature.

9 -1 dBFS input signal; includes the second through the fifth harmonics; within  $\pm 2$  °C of self-calibration temperature .

10 -1 dBFS input signal; input frequency is  $0.1 \times$  **Sample rate**; within  $\pm 2$  °C of self-calibration temperature; calculated from THD and RMS noise.

11 -1 dBFS input signal; input frequency is  $0.1 \times$  **Sample rate**; within  $\pm 2$  °C of self-calibration temperature; calculated from SINAD and THD.

12 100 Hz to  $0.4 \times$  **Sample rate**; DC coupling; input 50  $\Omega$  terminated.

13 1 MHz input, 5 MS/s sample rate.

14 Within  $\pm 5$  °C of self-calibration temperature.

15 For a 3 V step from 0 V DC, excluding noise; time referenced to 1.5 V (50%) trigger; applies to 15 MS/s sample rate only.

16 To set or change the filter type, use the Flex FIR Antialias Filter Type property or the NISCOPE\_ATTR\_FLEX\_FIR\_ANTIALIAS\_FILTER\_TYPE attribute.

17 Time ( $t=0$ ) represents the actual time the edge arrived at the BNC connector on the NI 5922.

18 Internal Sample clock is locked to the Reference clock or derived from the onboard VCXO.

19 Available rates are (60 MS/s) /  $n$  where  $n$  is an integer value from 4 to 1200. The Sample clock period is  $n/(60\text{MS/s})$ .

20 Includes the effects of the converter aperture uncertainty and the clock circuitry jitter; excludes trigger jitter.

21 The default value is 10 MHz.

22 Holdoff set to 0.

23 TRIG is an analog edge trigger only.

24 It is possible to exceed these numbers if you fetch records while acquiring data. For more information, refer to the **NI High-Speed Digitizers Help**.

25 Caused by clock and analog path delay differences. No manual adjustment performed.