

Customer Information Notification

202207007I: i.MX 8M Nano Reference Manual Update to Rev 2

Note: This notice is NXP Company Proprietary.

Issue Date: Nov 25, 2022 Effective date: Nov 26, 2022

Here is your personalized notification about a NXP general announcement. For detailed information we invite you to view this notification online

Management summary

Reference manual update for i.MX 8M Nano to revision 2.

Change Category

[]Wafer Fab Process	[]Assembly Process	[]Product Marking	[]Test Process	[]Design
[]Wafer Fab Materials	[]Assembly Materials	[]Mechanical Specification	[]Test Equipment	[]Errata
[]Wafer Fab Location	[]Assembly Location	[]Packing/Shipping/Labeling	[]Test Location	[]Electrica spec./Test coverage
[]Firmware	[X]Other: Re	eference Manual		

PCN Overview

Description

NXP Semiconductors announces reference manual update for i.MX 8M Nano to revision 2. The revision history included in the updated document provides a detailed description of the changes. Reference manual Changes Summary:

- 1. Chapter 1, Introduction
- 2. Chapter 2, Memory Map
- 3. Chapter 6, SNVS, Reset, Fuse, and Boot
- 4. Chapter 11, Connectivity
- 5. Chapter 13, Multimedia
- 6. Thermal Monitoring Unit (TMU)

Above chapters have been updated, details refer to RM revision history.

This evaluation has been successfully completed according to NXP specifications. Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02

The i.MX 8M Nano reference manual Rev.2 are attached to this notice, and can be found at: https://www.nxp.com/webapp/Download?colCode=IMX8MNRM

Reason

The Reference Manuals have been updated for chapter contents and provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: view online

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name jack mao

Position System Engineer

e-mail

address jack.mao@nxp.com

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards. Customer Focus, Passion to Win.

NXP Quality Management Team.

About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ: NXPI) provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise. These innovations are used in a wide range of automotive, identification, wireless infrastructure, lighting, industrial, mobile, consumer and computing applications.

You have received this email because you are a designated contact or subscribed to NXP Quality Notifications. NXP shall not be held liable if this Notification is not correctly distributed within your organization.

This message has been automatically distributed. Please do not reply.

NXP Semiconductors

High Tech Campus, 5656 AG Eindhoven, The Netherlands

© 2006- 2022 NXP Semiconductors. All rights reserved.