



## Customer Information Notification **Update**

202209018IU01 : MPC5644A Mask 0M14X, MPC5642A Mask 2N60C  
Errata Update SEP2022

**Note:** This notice is NXP Company  
Proprietary.

**Issue Date:** Dec 13, 2022 **Effective date:** Dec 14, 2022

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### Change Category

Wafer Fab Process     Assembly Process     Product Marking     Test Process     Design

Wafer Fab Materials     Assembly Materials     Mechanical Specification     Test Equipment     Errata

Wafer Fab Location     Assembly Location     Packing/Shipping/Labeling     Test Location     Electrical spec./Test coverage

Firmware     Other

### PCN Overview Description

NXP Semiconductors announces an errata update for below devices to Revision September 2022:

1. MPC5644A mask 0M14X
2. MPC5642A mask 2N60C

The revision history included in the updated document provides a detailed description of the change.  
Please see document revision history for more information.

Errata revision September 2022 are attached to this notice and can be found at:

1. MPC5644A mask 0M14X: [https://www.nxp.com/products/processors-and-microcontrollers/power-architecture/mpc5xxx-microcontrollers/ultra-reliable-mpc56xx-mcus/ultra-reliable-mpc564xa-mcu-for-automotive-industrial-engine-management:MPC564xA?tab=Documentation\\_Tab](https://www.nxp.com/products/processors-and-microcontrollers/power-architecture/mpc5xxx-microcontrollers/ultra-reliable-mpc56xx-mcus/ultra-reliable-mpc564xa-mcu-for-automotive-industrial-engine-management:MPC564xA?tab=Documentation_Tab)
2. MPC5642A mask 2N60C: [https://www.nxp.com/products/processors-and-microcontrollers/power-architecture/mpc5xxx-microcontrollers/ultra-reliable-mpc56xx-mcus/ultra-reliable-mpc564xa-mcu-for-automotive-industrial-engine-management:MPC564xA?tab=Documentation\\_Tab](https://www.nxp.com/products/processors-and-microcontrollers/power-architecture/mpc5xxx-microcontrollers/ultra-reliable-mpc56xx-mcus/ultra-reliable-mpc564xa-mcu-for-automotive-industrial-engine-management:MPC564xA?tab=Documentation_Tab)

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02

**Reason**

Errata has been updated to provide additional technical clarification.

**Identification of Affected Products**

Product identification does not change

**Anticipated Impact on Form, Fit, Function, Reliability or Quality**

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No Impact on form, fit, function, reliability or quality

No changes were made to the current production device. The errata describe existing conditions identified on current production devices.

**Additional information**

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Additional documents: [view online](#)

**Update Information**

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Due to an issue with our sales history database you may have received incomplete data. For this reason, NXP is issuing an update to the original notification to ensure sales history integrity. We apologize for any inconvenience this may have caused.

**Contact and Support**

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For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

<b>Name</b>	NXP Technical Support
<b>Position</b>	NXP Technical Support
<b>e-mail address</b>	<a href="mailto:tech.support@nxp.com">tech.support@nxp.com</a>

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NXP Quality Management Team.

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