



2301111396 Errata Update v0.2 for EFM32PG22

Bulletin Issue Date: Jan 11, 2023

Effective Date: Jan 11, 2023

Description of Change

Silicon Labs is pleased to announce the release of errata version 0.2 for EFM32PG22 devices.

Reason for Change

The following errata and workarounds have been added or updated:

- Updated the workaround in I2C_E303 (I2C Fails to Indicate New Incoming Data)
- Added CUR_E302 (Extra EM1 Current if FPU is Disabled)
- Added DCDC_E302 (DCDC Interrupts Block EM2/3 Entry or Cause Unexpected Wake-up)
- Added EUART_E301 (EUART receiver enters lockup state when using low frequency IrDA mode)
- Added EUART_E302 (Incorrect stop bits lock rx)
- Added IADC_E306 (Changing Gain During a Scan Sequence Causes an Erroneous IADC Result)
- Added USART_E304 (PRS Transmit Unavailable in Synchronous Secondary Mode)

Link to errata:

<https://www.silabs.com/documents/public/errata/efm32pg22-errata.pdf>

Product Identification

This notification includes both standard and customer-specific part numbers. An asterisk * represents a number or letter (one or more) in a customer-specific part number.

Existing Part #
EFM32PG22C200F64IM32-C
EFM32PG22C200F64IM32-CR
EFM32PG22C200F64IM40-C
EFM32PG22C200F64IM40-CR
EFM32PG22C200F128IM32-C
EFM32PG22C200F128IM32-CR
EFM32PG22C200F128IM40-C
EFM32PG22C200F128IM40-CR
EFM32PG22C200F256IM32-C
EFM32PG22C200F256IM32-CR
EFM32PG22C200F256IM40-C
EFM32PG22C200F256IM40-CR
EFM32PG22C200F512IM32-C
EFM32PG22C200F512IM32-CR
EFM32PG22C200F512IM40-C
EFM32PG22C200F512IM40-CR
EFM32PG22C200X*IM40-C
EFM32PG22C200X*IM40-CR

Kit Identification

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

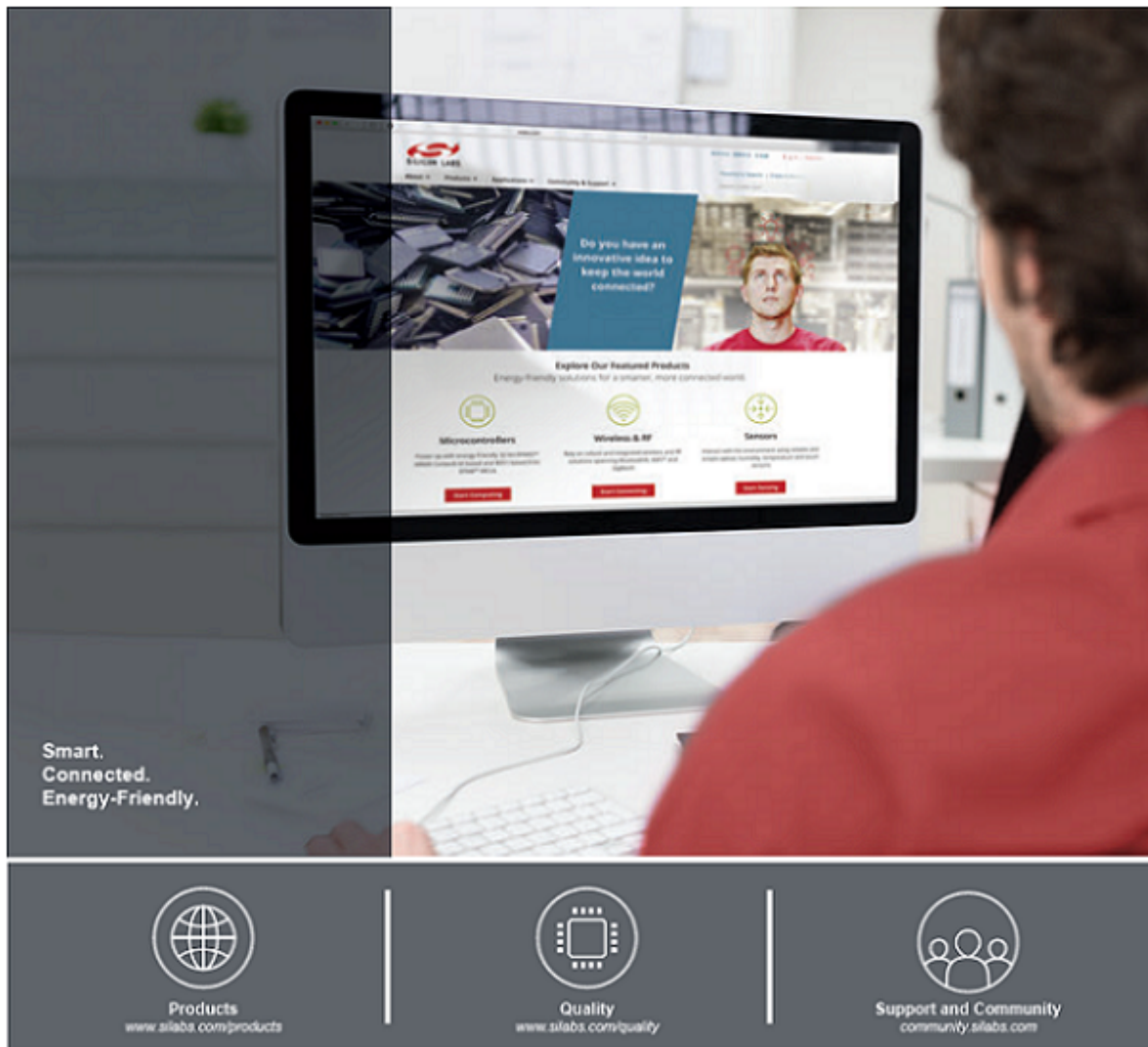
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Customer Actions Needed:

Review updated errata.

User Registration

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