Customer Information Notification

202301010I : i.MX 8M Dual / 8M QuadLite / 8M Quad (IMX8MDQLQ_2N14W) Errata update to Rev 2

Note: This notice is NXP Company Proprietary.

Issue Date: Feb 01, 2023 **Effective date:** Feb 02, 2023

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Management summary
Errata update for i.MX 8M Dual / 8M QuadLite / 8M Quad to revision 2.

Change Category

[ ]Wafer Fab Process	[ ]Assembly Process	[ ]Product Marking	[ ]Test Process	[ ]Design
[ ]Wafer Fab Materials	[ ]Assembly Materials	[ ]Mechanical Specification	[ ]Test Equipment	[X]Errata
[ ]Wafer Fab Location	[ ]Assembly Location	[ ]Packing/Shipping/Labeling	[ ]Test Location	[ ]Electrical spec./Test coverage
[ ]Firmware	[ ]Other			

PCN Overview
Description

NXP Semiconductors announces Errata update for i.MX 8M Dual / 8M QuadLite / 8M Quad to revision 2 . The revision history included in the updated document provides a detailed description of the changes.

Errata changes Summary for i.MX 8M Dual / 8M QuadLite / 8M Quad.
2 new items are added:
ERR051249: TZASC: ID SWAP function not supported.
ERR051273: DDRC: Periodic hardware-based DQS2DQ calibration is not supported.

Please refer to the Errata doc for detail information.

The i.MX 8M Dual / 8M QuadLite / 8M Quad Errata Rev 2 can be found at:
https://www.nxp.com/docs/en/errata/IMX8MDQLQ_2N14W.pdf

Reason
Errata has been updated with 2 additional items, and provided related workarounds.

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: view online

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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