

Film Capacitors

EMI Suppression Capacitors (MKP)

Series/Type: B32924*4 ... B32928*4

Date: February 2022

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X2 / 350 V AC

Typical applications

- X2 class for interference suppression
- Severe ambient conditions
- "E-meters". "In-series" with mains
- "Across the line" applications

Climatic

- Max. operating temperature: 110 °C
- Climatic category (IEC 60068-1:2013): 40/110/56

Construction

- Dielectric: polypropylene (MKP)
- Plastic case (UL 94 V-0)
- Epoxy resin sealing (UL 94 V-0)

Features

- Internal series construction
- Good self-healing properties
- High current handling
- RoHS-compatible
- Stable capacitance in severe ambient conditions 85 °C, 85% RH, 330 V AC, 1000 h
- AEC-Q200D compliant

Terminals

- Parallel wire leads. lead-free tinned
- Special lead lengths available on request

Marking

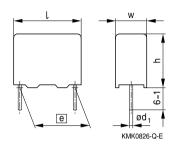
Manufacturer's logo, lot number, date code, rated capacitance (coded), capacitance tolerance (code letter), rated AC voltage (IEC), series number, sub-class (X2), dielectric code (MKP), climatic category, passive flammability category, approvals

Delivery mode

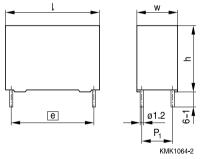
Bulk (untaped)
Taped (Ammo pack or reel)
For taping details, refer to chapter
"Taping and packing".

Dimensional drawing

Drawing 1



Drawing 2



P1 = 20.3 mm

Dimensions in mm

Pins	Lead spacing <u>e</u> ±0.4	Lead diameter d ₁ ±0.05	Туре	Drawing
2	27.5	0.8	B32924*4	1
2	37.5	1.0	B32926*4	1
4	37.5	1.2	B32926*4	2
4	52.5	1.2	B32928*4	2



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Marking example (position of marks may vary):



KMK1872-T

Approvals

Approval marks	Standards	Certificate
2 315	UL 60384-14.2014/A1:2016 CSA E60384-14:2013/AMD1:2016	ENEC-01393 (approved by UL)
c 91 us	UL 60384-14.2014 CSA E60384-14:2013	E97863 (approved by UL)

Overview of available types

Lead spacing	27.5 mm	37.5 mm	52.5 mm
Туре	B32924*4	B32926*4	B32928*4
C _R (μF)			
0.47			
0.56			
0.68			
0.82			
1.0			
1.2			
1.5			
1.8			
2.2			
2.7			
3.3			
4.7			
5.6			
6.8			
8.2			
10			
15			
20			





B32924*4

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Ordering codes and packing units (lead spacing 27.5 mm)

C _R	Max. dimensions	Ordering code	Reel	Untaped	Pins
	$w \times h \times l$	(composition see below)			
μF	mm		pcs./MOQ	pcs./MOQ	
0.47	11.0 × 19.0 × 31.5	B32924A4474+***	1400	1280	2
0.56	$11.0 \times 19.0 \times 31.5$	B32924A4564+***	1400	1280	2
0.68	$11.0 \times 21.0 \times 31.5$	B32924A4684+***	1400	1280	2
0.82	$12.5 \times 21.5 \times 31.5$	B32924A4824M***	1200	1120	2
0.82	$13.5 \times 23.0 \times 31.5$	B32924B4824K***	1000	1040	2
1.0	$13.5 \times 23.0 \times 31.5$	B32924A4105M***	1000	1040	2
1.0	$14.0 \times 24.5 \times 31.5$	B32924B4105K***	_	1040	2
1.2	$14.0 \times 24.5 \times 31.5$	B32924A4125M***	_	1040	2
1.5	$16.0 \times 32.0 \times 31.5$	B32924B4155+***	_	880	2
1.5	$18.0 \times 27.5 \times 31.5$	B32924A4155+***	_	800	2
1.8	$16.0 \times 32.0 \times 31.5$	B32924B4185+***	_	880	2
1.8	$18.0 \times 27.5 \times 31.5$	B32924A4185M***	_	800	2
2.2	$18.0 \times 33.0 \times 31.5$	B32924S4225+***	_	800	2
2.2	$19.0 \times 30.0 \times 31.5$	B32924A4225M***	_	720	2
2.2	$21.0 \times 31.0 \times 31.5$	B32924B4225K***	_	720	2
2.7	$22.0 \times 33.0 \times 31.5$	B32924A4275+***	_	640	2
3.3	$22.0 \times 36.5 \times 31.5$	B32924A4335M***	_	640	2
3.3	$22.0 \times 48.0 \times 31.5$	B32924B4335K***	_	320	2

MOQ = Minimum Order Quantity, consisting of 4 packing units.

Further intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

 $M = \pm 20\%$

 $K = \pm 10\%$

*** = Packaging code:

000 = Straight terminals, untaped (lead length 6 -1 mm)

003 = Straight terminals, untaped

(lead length 3.2 \pm 0.3 mm) 189 = Straight terminals, Reel



B32926*4

X2 / 350 V AC



Ordering codes and packing units (lead spacing 37.5 mm)

C _R	Max. dimensions	Ordering code	Reel	Untaped	Pins
	$w \times h \times l$	(composition see below)			
μF	mm		pcs./MOQ	pcs./MOQ	
1.0	12.0 × 22.0 × 42.0	B32926A4105+***	_	1620	2
1.2	$12.0 \times 22.0 \times 42.0$	B32926A4125M***	_	1620	2
1.2	$14.0 \times 25.0 \times 42.0$	B32926B4125K***	_	1380	2
1.5	$14.0 \times 25.0 \times 42.0$	B32926A4155+***	_	1380	2
1.8	$14.0 \times 25.0 \times 42.0$	B32926A4185M***	_	1380	2
1.8	$16.0 \times 28.5 \times 42.0$	B32926B4185K***	_	800	2
2.2	$16.0 \times 28.5 \times 42.0$	B32926A4225+***	_	800	2
2.7	$17.0 \times 32.0 \times 42.0$	B32926A4275M***	_	760	2
2.7	$18.0 \times 32.5 \times 42.0$	B32926B4275K***	_	720	2
3.3	$18.0 \times 32.5 \times 42.0$	B32926A4335M***	_	720	2
3.3	$20.0 \times 39.5 \times 42.0$	B32926B4335K***	_	640	2
4.7	$20.0 \times 39.5 \times 42.0$	B32926B4475M***	_	640	2
4.7	$28.0 \times 37.0 \times 42.0$	B32926A4475K***	_	440	2
5.6	$28.0 \times 37.0 \times 42.0$	B32926A4565M***	_	440	2
5.6	$28.0 \times 42.5 \times 42.0$	B32926B4565K***	_	440	2
6.8	$28.0 \times 42.5 \times 42.0$	B32926A4685+***	_	440	2
8.2	$30.0 \times 45.0 \times 42.0$	B32926A4825M***	_	400	2
8.2	$33.0 \times 48.0 \times 42.0$	B32926B4825K***	_	180	4
10.0	$33.0\times48.0\times42.0$	B32926A4106M***	_	180	4

MOQ = Minimum Order Quantity, consisting of 4 packing units.

Further intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

 $M = \pm 20\%$ $K = \pm 10\%$ *** = Packaging code:

000 = Straight terminals, untaped (lead length 6 - 1 mm)

003 = Straight terminals, untaped (lead length 3.2 ±0.3 mm) 189 = Straight terminals. Reel





B32928*4

X2 / 350 V AC

Ordering codes and packing units (lead spacing 52.5 mm)

C _R	Max. dimensions	Ordering code	Reel	Untaped	Pins
	$w \times h \times l$	(composition see below)			
μF	mm		pcs./MOQ	pcs./MOQ	
8.2	$30.0 \times 45.0 \times 57.5$	B32928A4825K***	_	280	4
10.0	$30.0 \times 45.0 \times 57.5$	B32928A4106K***	_	280	4
15.0	$35.0 \times 50.0 \times 57.5$	B32928A4156K***	_	108	4
20.0	$45.0 \times 57.0 \times 57.5$	B32928A4206K***	_	140	4

MOQ = Minimum Order Quantity, consisting of 4 packing units.

Further intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

 $M = \pm 20\%$ $K = \pm 10\%$ *** = Packaging code:

000 =Straight terminals, untaped (lead length 6 -1 mm)

003 = Straight terminals, untaped (lead length 3.2 ±0.3 mm)

189 = Straight terminals, Reel



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Technical data

Reference standard: IEC 60384-14:2013/AMD1:2016 / UL 60384-14:2014/A1:2016. All data given at T = 20 $^{\circ}$ C, unless otherwise specified.

Rated AC voltage (IEC 60384-14:2013)	350 V (50/60 Hz)
Maximum continuous DC voltage	650 V at $T_{op} \le$ 85 °C 1.5% / °C derating when 85 °C < $T_{op} \le$ 110 °C
Max. operating temperature T _{op,max}	+110 °C
DC test voltage	4.3 · 350 = 1505 V DC, 2 s

The repetition of this DC voltage test may damage the capacitor. Special care must be taken in case of use several capacitors in a parallel configuration.

Dissipation factor tan δ (in 10 ⁻³)		$C_R \le 4.7 \ \mu F$	C _R > 4.7 μF	
at 20 °C (upper limit values)	at 1 kHz	0.9	1.2	
Insulation resistance R_{ins} or time constant $\tau = C_R \cdot R_{ins}$ at 100 V DC, 20 °C, rel. humidity \leq 65% and for 60 s (minimum as-delivered values)	30 000 s			
Passive flammability category	В			
Capacitance tolerances (measured at 1 kHz)	±10% (K), ±20	% (M)		
Damp heat test	Test conditions			
Limit relices of the dame hands at	Temperature: Relative humid Voltage value: Test duration:	•	+85 °C ±2 °C 85% ±2% 330 V AC, 50 Hz 1000 hours	
Limit values after damp heat test	Capacitance c Dissipation fac Insulation resis	tor change l∆tan δl	$\leq 7.5\%$ $\leq 3 \cdot 10^{-3}$ (at 1 kHz) 50% of initial limit	





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Pulse handling capability

"dV/dt" represents the maximum permissible voltage change per unit of time for non-sinusoidal voltages, expressed in V/us.

" k_0 " represents the maximum permissible pulse characteristic of the waveform applied to the capacitor, expressed in $V^2/\mu s$.

Note:

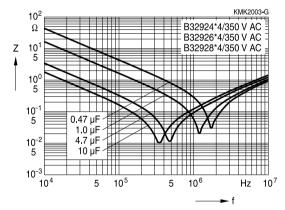
The values of dV/dt and k_0 provided below must not be exceeded in order to avoid damaging the capacitor.

dV/dt and ko values

Lead spacing	27.5 mm	37.5 mm	52.5 mm
dV/dt in V/μs	80	40	30
k ₀ in V ² /μs	27 400	10 400	8 600

Impedance Z versus frequency f

(typical values)



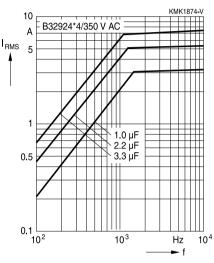


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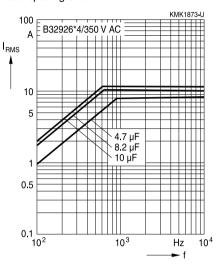


Permissible AC current I_{RMS} versus frequency f (for sinusoidal waveform, TA \leq 90 °C and Δ ESR <100% from receipt condition)

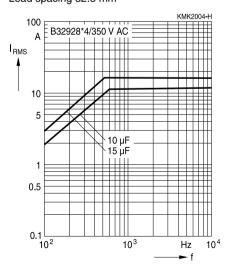
Lead spacing 27.5 mm



Lead spacing 37.5 mm



Lead spacing 52.5 mm







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Testing and Standards

Test	Reference	Conditions of test		Performance requirements
Voltage	IEC	Voltage proof betwe	en terminals,	Within specified limits
proof	60384-14:2013/	4.3 V _R , 2 s		
	AMD1:2016	Terminals and enclo	sure:	
		2 V _R + 1500 V AC		
		Insulation resistance	e, R _{ins}	
		Capacitance, C		
		Dissipation factor, ta	ın δ	
Robust	IEC	Tensile strength (tes	t Ua1)	Capacitance and $tan \delta$
ness of	60068-2-21:2006	Wire diameter	Tensile	within specified limits
termina-		VVIII O GIGITIOTO	force	
tions		$0.5 < d_1 \le 0.8 \text{ mm}$	10 N	
		$0.8 < d_1 \le 1.25 \text{ mm}$	20 N	
Resistance	IFC	Solder bath tempera		$\Delta C/C_0 \le 5\%$
to solder-	60068-2-20:2008,	260 ±5 °C, immersion		tan δ within specified limits
ing heat	test Tb.	10 seconds		tarre warm openined in the
	method 1A			
Vibration	IEC	Test Fc: vibration sir	nusoidal	No visible damage
	60384-14:2013/	Displacement: 0.75	mm	
	AMD1:2016	Accleration: 98 m/s ²		
		Frequency: 10 Hz	500 Hz	
		Test duration: 3 orth	ogonal axes,	
		2 hours each axe		
Bump	IEC	Test Eb: Total 4000 bumps with		No visible damage
	60384-14:2013/	400 m/s ² mounted on PCB		$ \Delta C/C_0 \le 5\%$
	AMD1:2016	6 ms duration		tan δ within specified limits
Damp	IEC	Test Ca		No visible damage
heat,	60384-14:2013/	40 °C / 93% RH / 56	days	$ \Delta C/C_0 \le 5\%$
steady	AMD1:2016			$ \Delta \tan \delta \le 0.008$ for C $\le 1 \mu$ F
state				$ \Delta \tan \delta \le 0.005$ for C > 1 μ F
				Voltage proof
				R _{ins} ≥ 50% of initial limit
Special	_	85 °C / 85% RH / 1000 h /		$ \Delta C/C_0 \le 7.5\%$
biased		330 V AC, 50 Hz		$ \Delta \tan \delta \le 0.003$
damp heat				$R_{ins} \ge 50\%$ of initial limit
test				
Rapid	IEC	T _A = lower category		No visible damage
change of	60384-14:2013/	T_B = upper category	•	$ \Delta C/C_0 \le 5\%$
tempera-	AMD1:2016	5 cycles, duration t =	= 30 min.	tan δ within specified limits
ture				



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Test	Reference	Conditions of test	Performance requirements
Climatic sequence	IEC 60384-14:2013/ AMD1:2016	Dry heat Tb / 16 h Damp heat cyclic, 1st cycle +55 °C / 24 h / 95% 100% RH Cold Ta / 2 h Damp heat cyclic, 5 cycles +55 °C / 24 h / 95% 100% RH	No visible damage $\begin{split} \Delta C/C_0 &\leq 5\% \\ \Delta \ tan \ \delta &\leq 0.008 \ for \ C \leq 1 \ \mu F \\ \Delta \ tan \ \delta &\leq 0.005 \ for \ C > 1 \ \mu F \\ Voltage \ proof \\ R_{ins} &\geq 50\% \ of \ initial \ limit \end{split}$
Impulse test Endurance	IEC 60384-14:2013/ AMD1:2016	3 impulses Tb / 1.25 $V_{\rm R}$ / 1000 hours, 1000 $V_{\rm RMS}$ for 0.1 s every hour	No visible damage $\begin{split} \Delta C/C_0 &\leq 10\% \\ \Delta \tan \delta &\leq 0.008 \text{ for } C \leq 1 \mu\text{F} \\ \Delta \tan \delta &\leq 0.005 \text{ for } C > 1 \mu\text{F} \\ \text{Voltage proof} \\ R_{\text{ins}} &\geq 50\% \text{ of initial limit} \end{split}$
Passive flamma- bility	IEC 60384-14:2013/ AMD1:2016	Flame applied for a period of time depending on capacitor volume	В
Active flamma- bility	IEC 60384-14:2013/ AMD1:2016	20 discharges at 2.5 kV + V _R	The cheesecloth shall not burn with a flame

Mounting guidelines

1 Soldering

1.1 Solderability of leads

The solderability of terminal leads is tested to IEC 60068-2-20, test Ta, method 1.

Before a solderability test is carried out, terminals are subjected to accelerated ageing (to IEC 60068-2-2, test Ba: 4 h exposure to dry heat at 155 °C). Since the ageing temperature is far higher than the upper category temperature of the capacitors, the terminal wires should be cut off from the capacitor before the ageing procedure to prevent the solderability being impaired by the products of any capacitor decomposition that might occur.

Solder bath temperature	235 ±5 °C
Soldering time	2.0 ±0.5 s
Immersion depth	2.0 +0/-0.5 mm from capacitor body or seating plane
Evaluation criteria:	
Visual inspection	Wetting of wire surface by new solder ≥90%, free-flowing solder



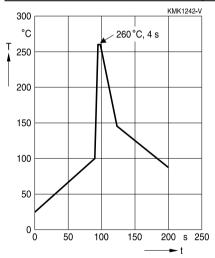


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1.2 Resistance to soldering heat

Resistance to soldering heat is tested to IEC 60068-2-20, test Tb, method 1. Conditions:

Serie	s	Solder bath temperature	Soldering time
MKT	boxed (except 2.5 × 6.5 × 7.2 mm) coated uncoated (lead spacing >10 mm)	260 ±5 °C	10 ±1 s
MFP			
MKP	(lead spacing >7.5 mm)		
MKT	boxed (case $2.5 \times 6.5 \times 7.2$ mm)		5 ±1 s
MKP MKT	(lead spacing ≤7.5 mm) uncoated (lead spacing ≤10 mm) insulated (B32559)		<4 s recommended soldering profile for MKT uncoated (lead spacing ≤ 10 mm) and insulated (B32559)



Immersion depth	2.0 +0/-0.5 mm from capacitor body or seating plane
Shield	Heat-absorbing board, (1.5 \pm 0.5) mm thick, between capacitor body and liquid solder
Evaluation criteria:	
Visual inspection	No visible damage
$\Delta C/C_0$	2% for MKT/MKP/MFP 5% for EMI suppression capacitors
$tan \ \delta$	As specified in sectional specification



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1.3 General notes on soldering

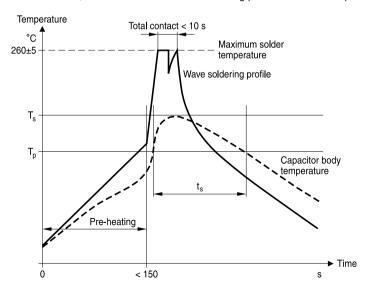
Permissible heat exposure loads on film capacitors are primarily characterized by the upper category temperature T_{max} . Long exposure to temperatures above this type-related temperature limit can lead to changes in the plastic dielectric and thus change irreversibly a capacitor's electrical characteristics. For short exposures (as in practical soldering processes) the heat load (and thus the possible effects on a capacitor) will also depend on other factors like:

- Pre-heating temperature and time
- Forced cooling immediately after soldering
- Terminal characteristics: diameter, length, thermal resistance, special configurations (e.g. crimping)
- Height of capacitor above solder bath
- Shadowing by neighboring components
- Additional heating due to heat dissipation by neighboring components
- Use of solder-resist coatings

The overheating associated with some of these factors can usually be reduced by suitable countermeasures. For example, if a pre-heating step cannot be avoided, an additional or reinforced cooling process may possibly have to be included.

Recommendations

As a reference, the recommended wave soldering profile for our film capacitors is as follows:



T_s: Capacitor body maximum temperature at wave soldering

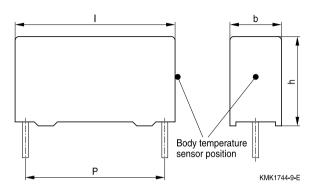
T_n: Capacitor body maximum temperature at pre-heating

KMK1745-A-E





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Body temperature should follow the description below:

■ MKP capacitor

During pre-heating: $T_p \le 110 \, ^{\circ}\text{C}$ During soldering: $T_s \le 120 \, ^{\circ}\text{C}$, $t_s \le 45 \, \text{s}$

■ MKT capacitor

During pre-heating: $T_p \le 125 \, ^{\circ}\text{C}$ During soldering: $T_s \le 160 \, ^{\circ}\text{C}$, $t_s \le 45 \, \text{s}$

When SMD components are used together with leaded ones, the film capacitors should not pass into the SMD adhesive curing oven. The leaded components should be assembled after the SMD curing step.

Leaded film capacitors are not suitable for reflow soldering.

In order to ensure proper conditions for manual or selective soldering, the body temperature of the capacitor (T_s) must be \leq 120 °C.

One recommended condition for manual soldering is that the tip of the soldering iron should be <360 °C and the soldering contact time should be no longer than 3 seconds.

For uncoated MKT capacitors with lead spacings ≤10 mm (B32560/B32561) the following measures are recommended:

- pre-heating to not more than 110 °C in the preheater phase
- rapid cooling after soldering

Please refer to our Film Capacitors Data Book in case more details are needed.



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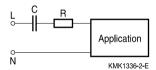
Application note for the different possible X1 / X2 positions

In series with the powerline (i.e. capacitive power supply)

Typical Applications:

- Power meters
- ECUs for white goods and household appliances
- Different sensor applications
- Severe ambient conditions

Basic circuit



Required features

- High capacitance stability over the lifetime
- Narrow tolerances for a controlled current supply

Recommended product series

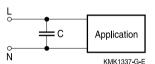
- B3293* (305 V AC) heavy duty with EN approval for X2 (UL Q1/2010)
- B3265* MKP series standard MKP capacitor without safety approvals
- B3267*L MKP series standard MKP capacitor without safety approvals
- B3292*H/J (305 V AC), severe ambient condition, approved as X2

In parallel with the powerline

Typical Applications:

Standard X2 are used parallel over the mains for reducing electromagnetic interferences coming from the grid. For such purposes they must meet the applicable EMC directives and standards.

Basic circuit



Required features

- Standard safety approvals (ENEC. UL. CSA, CQC)
- High pulse load capability
- Withstand surge voltages

Recommended product series

- B3292*C/D (305 V AC) standard series, approved as X2
- B3291* (330 V AC), approved as X1
- B3291* (530 V AC), approved as X1
- B3291* (550 V AC), approved as X1
- B3292*H/J (305 V AC), severe ambient condition, approved as X2





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Cautions and warnings

- Do not exceed the upper category temperature (UCT).
- Do not apply any mechanical stress to the capacitor terminals.
- Avoid any compressive, tensile or flexural stress.
- Do not move the capacitor after it has been soldered to the PC board.
- Do not pick up the PC board by the soldered capacitor.
- Do not place the capacitor on a PC board whose PTH hole spacing differs from the specified lead spacing.
- Do not exceed the specified time or temperature limits during soldering.
- Avoid external energy inputs, such as fire or electricity.
- Avoid overload of the capacitors.
- Consult us if application is with severe temperature and humidity condition.
- There are no serviceable or repairable parts inside the capacitor. Opening the capacitor or any attempts to open or repair the capacitor will void the warranty and liability of TDK Electronics.
- Please note that the standards referred to in this publication may have been revised in the meantime.

The table below summarizes the safety instructions that must always be observed. A detailed description can be found in the relevant sections of the chapters "General technical information" and "Mounting guidelines".

Topic	Safety information	Reference chapter "General technical information"
Storage conditions	Make sure that capacitors are stored within the specified range of time, temperature and humidity conditions.	4.5 "Storage conditions"
Flammability	Avoid external energy, such as fire or electricity (passive flammability), avoid overload of the capacitors (active flammability) and consider the flammability of materials.	5.3 "Flammability"
Resistance to vibration	Do not exceed the tested ability to withstand vibration. The capacitors are tested to IEC 60068-2-6:2007. TDK Electronics offers film capacitors specially designed for operation under more severe vibration regimes such as those found in automotive applications. Consult our catalog "Film Capacitors for Automotive Electronics".	5.2 "Resistance to vibration"



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Topic	Safety information	Reference chapter "Mounting guidelines"
Soldering	Do not exceed the specified time or temperature limits during soldering.	1 "Soldering"
Cleaning	Use only suitable solvents for cleaning capacitors.	2 "Cleaning"
Embedding of capacitors in finished assemblies	When embedding finished circuit assemblies in plastic resins, chemical and thermal influences must be taken into account. Caution: Consult us first, if you also wish to embed other uncoated component types!	3 "Embedding of capacitors in finished assemblies"

Design of our capacitors

Our EMI capacitors use polypropylene (PP) film metalized with a thin layer of Zinc (Zn). The following key points have made this design suitable to IEC/UL testing, holding a minimum size.

- Overvoltage AC capability with very high temperature Endurance test of IEC 60384-14:2013 (4th edition) / UL 60384-14:2014 (2nd edition) must be performed at 1.25 × V_R at maximum temperature, during 1000 hours, with a capacitance drift less than 10%.
- Higher breakdown voltage withstanding if compared to other film metallizations, like Aluminum. IEC 60384-14:2013 (4th edition) / UL 60384-14:2014 (2nd edition) establishes high voltage tests performed at $4.3 \times V_R 1$ minute, impulse testing at 2500 V for C = 1 μ F and active flammability tests
- Damp heat steady state: 40 °C/ 93% RH / 56 days. (without voltage or current load)

Effect of humidity on capacitance stability

Long contact of a film capacitor with humidity can produce irreversible effects. Direct contact with liquid water or excess exposure to high ambient humidity or dew will eventually remove the film metallization and thus destroy the capacitor. Plastic boxed capacitors must be properly tested in the final application at the worst expected conditions of temperature and humidity in order to check if any parameter drift may provoke a circuit malfunction.

In case of penetration of humidity through the film, the layer of Zinc can be degraded, specially under AC operation (change of polarity), accelerated by the temperature, provoking an increment of the serial resistance of the electrode and eventually a reduction of the capacitance value. For DC operation, the parameter drift is much less.

Plastic boxes and resins can not protect 100% against humidity. Metal enclosures, resin potting or coatings or similar measures by customers in their applications will offer additional protection against humidity penetration.

Display of ordering codes for TDK Electronics products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications, on the company website, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the order-





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ing codes are due to different processes employed and do not affect the specifications of the respective products.

Detailed information can be found on the Internet under www.tdk-electronics.tdk.com/orderingcodes.



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Symbols and terms

Symbol	English	German
α	Heat transfer coefficient	Wärmeübergangszahl
$lpha_{ extsf{C}}$	Temperature coefficient of capacitance	Temperaturkoeffizient der Kapazität
Α	Capacitor surface area	Kondensatoroberfläche
β_{C}	Humidity coefficient of capacitance	Feuchtekoeffizient der Kapazität
С	Capacitance	Kapazität
C_R	Rated capacitance	Nennkapazität
ΔC	Absolute capacitance change	Absolute Kapazitätsänderung
Δ C/C	Relative capacitance change (relative	Relative Kapazitätsänderung (relative
	deviation of actual value)	Abweichung vom Ist-Wert)
$\Delta C/C_R$	Capacitance tolerance (relative deviation	Kapazitätstoleranz (relative Abweichung
	from rated capacitance)	vom Nennwert)
dt	Time differential	Differentielle Zeit
Δt	Time interval	Zeitintervall
ΔT	Absolute temperature change	Absolute Temperaturänderung
	(self-heating)	(Selbsterwärmung)
$\Delta tan \delta$	Absolute change of dissipation factor	Absolute Änderung des Verlustfaktors
ΔV	Absolute voltage change	Absolute Spannungsänderung
dV/dt	Time differential of voltage function (rate	Differentielle Spannungsänderung
	of voltage rise)	(Spannungsflankensteilheit)
$\Delta V/\Delta t$	Voltage change per time interval	Spannungsänderung pro Zeitintervall
E	Activation energy for diffusion	Aktivierungsenergie zur Diffusion
ESL	Self-inductance	Eigeninduktivität
ESR	Equivalent series resistance	Ersatz-Serienwiderstand
f	Frequency	Frequenz
f ₁	Frequency limit for reducing permissible	Grenzfrequenz für thermisch bedingte
	AC voltage due to thermal limits	Reduzierung der zulässigen
		Wechselspannung
f_2	Frequency limit for reducing permissible	Grenzfrequenz für strombedingte
	AC voltage due to current limit	Reduzierung der zulässigen
,	December 1 for some 1	Wechselspannung
f _r	Resonant frequency	Resonanzfrequenz
F_D	Thermal acceleration factor for diffusion	Therm. Beschleunigungsfaktor zur Diffusion
_	Dereting factor	
F _⊤	Derating factor	Deratingfaktor
1	Current (peak)	Stromspitze
I _C	Category current (max. continuous	Kategoriestrom (max. Dauerstrom)
	current)	





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Symbol	English	German
I _{RMS}	(Sinusoidal) alternating current,	(Sinusförmiger) Wechselstrom
	root-mean-square value	
i _z	Capacitance drift	Inkonstanz der Kapazität
k_0	Pulse characteristic	Impulskennwert
Ls	Series inductance	Serieninduktivität
λ	Failure rate	Ausfallrate
λ_0	Constant failure rate during useful	Konstante Ausfallrate in der
	service life	Nutzungsphase
λ_{test}	Failure rate, determined by tests	Experimentell ermittelte Ausfallrate
P_{diss}	Dissipated power	Abgegebene Verlustleistung
P_{gen}	Generated power	Erzeugte Verlustleistung
Q	Heat energy	Wärmeenergie
ρ	Density of water vapor in air	Dichte von Wasserdampf in Luft
R	Universal molar constant for gases	Allg. Molarkonstante für Gas
R	Ohmic resistance of discharge circuit	Ohmscher Widerstand des
		Entladekreises
R_{i}	Internal resistance	Innenwiderstand
R_{ins}	Insulation resistance	Isolationswiderstand
R_P	Parallel resistance	Parallelwiderstand
R_s	Series resistance	Serienwiderstand
S	severity (humidity test)	Schärfegrad (Feuchtetest)
t	Time	Zeit
Т	Temperature	Temperatur
τ	Time constant	Zeitkonstante
$tan \ \delta$	Dissipation factor	Verlustfaktor
$tan \; \delta_{\scriptscriptstyle D}$	Dielectric component of dissipation factor	Dielektrischer Anteil des Verlustfaktors
$tan \; \delta_{\scriptscriptstyle P}$	Parallel component of dissipation factor	Parallelanteil des Verlfustfaktors
$tan \; \delta_{\text{S}}$	Series component of dissipation factor	Serienanteil des Verlustfaktors
T_A	Temperature of the air surrounding the component	Temperatur der Luft, die das Bauteil umgibt
T_{max}	Upper category temperature	Obere Kategorietemperatur
T _{min}	Lower category temperature	Untere Kategorietemperatur
t _{OL}	Operating life at operating temperature	Betriebszeit bei Betriebstemperatur und
•	and voltage	-spannung
T_{op}	Operating temperature, $T_A + \Delta T$	Beriebstemperatur, $T_A + \Delta T$
T _R	Rated temperature	Nenntemperatur
T_{ref}	Reference temperature	Referenztemperatur
t _{SL}	Reference service life	Referenz-Lebensdauer



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Symbol	English	German
V_{AC}	AC voltage	Wechselspannung
V_{c}	Category voltage	Kategoriespannung
$V_{\text{C,RMS}}$	Category AC voltage	(Sinusförmige)
		Kategorie-Wechselspannung
V_{CD}	Corona-discharge onset voltage	Teilentlade-Einsatzspannung
V_{ch}	Charging voltage	Ladespannung
V_{DC}	DC voltage	Gleichspannung
V_{FB}	Fly-back capacitor voltage	Spannung (Flyback)
V_{i}	Input voltage	Eingangsspannung
V_{\circ}	Output voltage	Ausgangssspannung
V_{op}	Operating voltage	Betriebsspannung
V_p	Peak pulse voltage	Impuls-Spitzenspannung
V_{pp}	Peak-to-peak voltage Impedance	Spannungshub
V_R	Rated voltage	Nennspannung
ν̂ _R	Amplitude of rated AC voltage	Amplitude der Nenn-Wechselspannung
V_{RMS}	(Sinusoidal) alternating voltage,	(Sinusförmige) Wechselspannung
	root-mean-square value	
V_{SC}	S-correction voltage	Spannung bei Anwendung "S-correction"
V_{sn}	Snubber capacitor voltage	Spannung bei Anwendung
		"Beschaltung"
Z	Impedance	Scheinwiderstand
e	Lead spacing	Rastermaß



Important notes

The following applies to all products named in this publication:

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