



## Product Change Notification

CN-202303025F

**Issue date:** 30 Jun 2023

**Effective date:** 28 Sep 2023

Dear [etempest@farnell.com](mailto:etempest@farnell.com),

Here's your personalized quality information concerning products our customers and partners purchased from Nexperia.

For more details please contact your respective Nexperia CSR/AM.



### Expansion of wafer fab capacity by introduction GTA Semiconductor (former ASMC) for selected HEF types in PC900SMB, PC950SM and PC95SSMB process

#### Change Category

<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Wafer	Assembl			
Fab	y			
Process	Process	<input checked="" type="checkbox"/> Product Marking	<input type="checkbox"/> Test	<input type="checkbox"/> Design
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/> Mechanical	Location	<input type="checkbox"/> Errata
Wafer	Assembl	Specification	<input type="checkbox"/> Test	<input type="checkbox"/>
Fab	y	<input type="checkbox"/>	Process	Electrical
Material	Materials	Packing/Shipping/Labelin	<input type="checkbox"/> Test	spec./Tes
s	<input type="checkbox"/>	g	Equipmen	t
<input checked="" type="checkbox"/>	Assembl		t	coverage
Wafer	y			
Fab	Location			
Location				

#### Details of this change

Expansion of wafer fab capacity by introduction GTA Semiconductor (former ASMC) Shanghai China for selected Logic HEF types in PC950SM, PC95SSMB and PC900SMB process to meet customer demand

- Additional source for PC9xx process at GTA Semiconductor (Shanghai China) next to NXP ICN8 (Nijmegen The Netherlands)
- For selected Logic HEF product types
- Release PC9xx process cluster at GTA (PC950SM, PC95SSMB and PC900SMB process)
- No change in datasheet, form, fit, function, quality or reliability anticipated
- Matching wafer PCM (Process Control Modules) and product parameters by 1.5 delta/sigma and Cpk > 1.67
- No change in die layout, all masks are produced from the same GDS2 file
- No change in ordering code 12NC and product type name
- No change in wafer fab process
- No change in BOM

SQR\_CN-202303025F.pdf: [https://qcm.nexperia.com/Document/DOC-556891/SQR\\_CN-202303025F.pdf](https://qcm.nexperia.com/Document/DOC-556891/SQR_CN-202303025F.pdf)

### **Why do we implement this change?**

- Introduce second source wafer fab location
- To support increased customer demand
- Order Lead-Time Improvement
- Flexibility

### **Identification of affected products**

- The change can be identified by backward traceability
- Applied wafer fab is indicated on the reel and box label and product marking

Indicators:

"Y"= GTA (ASMC), Shanghai, China

"T"= NXP ICN8, Nijmegen, Netherlands

## **Management summary**

- Release 2nd source PC9xx process
- Introduction of GTA Semiconductor (former ASMC) wafer fab in Shanghai China for selected Logic HEF products in PC9xx process

## Product availability

### Production

Planned first shipment: 30 Sep 2023

### Sample information

Samples are available upon request

## Impact

No impact to the product's functionality anticipated

### Data sheet revision

No impact to existing datasheet

### Disposition of old products

Not applicable introduction of 2nd source

### Feedback

Your acknowledgement of this change, conform JEDEC J-STD-046, is expected till 30 Jul 2023. Lack of acknowledgement of the PCN constitutes acceptance of the change.

## Additional information

[View Change Notification Online](#)

### Remarks

- No change in ordering code 12NC - Samples are available upon request via Helpdesk+ from BG Analog & Logic ICs sample store in Nijmegen The Netherlands

## Contact and support

For all Quality Notification content inquiries, please contact your local Nexperia Sales Support Team.

For specific questions on this notice or the products affected please contact our specialist directly: [pcn@nexperia.com](mailto:pcn@nexperia.com)

In case of distribution, please contact your distribution partner.

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