PRODUCT / PROCESS CHANGE NOTIFICATION

1. PCN basic data			
1.1 Company		STMicroelectronics International N.V	
1.2 PCN No.		POWER AND DISCRETE PRODUCTS/24/14840	
1.3 Title of PCN		Mold Compound replacement on STS Plant for industrial devices	
1.4 Product Category		SiC	
1.5 Issue date		2024-06-27	

2. PCN Team			
2.1 Contact supplier	2.1 Contact supplier		
2.1.1 Name	Robert Goodman		
2.1.2 Phone	+1 6024856271		
2.1.3 Email	robert.goodman@st.com		
2.2 Change responsibility			
2.2.1 Product Manager	Riccardo NICOLOSO		
2.1.2 Marketing Manager	Antonino GAITO		
2.1.3 Quality Manager	Vincenzo MILITANO		

3. Change			
3.1 Category 3.2 Type of change		3.3 Manufacturing Location	
Materials	Direct Material: Mold compound - Chemistry (raw material)	Shenzhen - China	

4. Description of change			
	Old	New	
4.1 Description	Resin EMC: Kyocera KEG-300S-1	Resin SUMITOMO	
4.2 Anticipated Impact on form,fit, function, quality, reliability or processability?	PROCESSABILITY		

5. Reason / motivation for change		
5.1 Motivation KYOCERA KEG-300S-1 out of production		
5.2 Customer Benefit	SERVICE CONTINUITY	

6. Marking of parts / traceability of change		
6.1 Description	by QA number	

7. Timing / schedule		
7.1 Date of qualification results	2024-06-25	
7.2 Intended start of delivery	2024-09-25	
7.3 Qualification sample available?	Upon Request	

8. Qualification / Validation			
.1 Description 14840 qualification.pdf			
3.2 Qualification report and Available (see attachment) Issue Date 2024-06-27		2024-06-27	

9. Attachments (additional documentations)

14840 Public product.pdf 14840 14840.pdf 14840 qualification.pdf

10. Affected parts		
10. 1 Current10.2 New (if applicable)		
10.1.1 Customer Part No	10.1.2 Supplier Part No	10.1.2 Supplier Part No
	SCT10N120	
	SCT20N120	
	SCT30N120	
	SCT50N120	

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Public Products List

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PCN Title : Mold Compound replacement on STS Plant for industrial devices **PCN Reference :** POWER AND DISCRETE PRODUCTS/24/14840

Subject : Public Products List

Dear Customer,

Please find below the Standard Public Products List impacted by the change.

SCT10N120	SCT1000N170	SCT30N120
SCT50N120	SCT20N120	

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MOSFET Silicon Carbide SiC Technology Product family diffused in ST CT6 Catania (Italy) and assembled in Package HIP247 ST Shenzhen STS (China) with Sumitomo Moulding Compound Reliability Evaluation Plan

general Information on selected test vehicles			
	TV1: SCT015W120G3AG		
Commercial Product	TV2: SCT1000N170AG		
	TV3: SCT070W120G3AG		
	TV1: BRJO		
Product Line	TV2: K17F		
	TV3: BRJ7		
Silicon process Technology	SIC MOSFET GEN3		
Package	HIP247		

Note: this report is a summary of the reliability trials performed in good faith by STMicroelectronics in order to evaluate the electronic device conformance to its specific mission profile for Automotive Application. This report and its contents shall not be disclosed to a third party without previous written agreement from STMicroelectronics or under the approval of the author (see below). Revision history

Rev.	Changes description	Author	Date	
1.0	New release	G. Allegra	May 30 th 2024	
Approved by				

Function	Location	Name	Date		
Division Reliability Manager	ST Catania (Italy)	M. De Tomasi	May 30 th 2024		



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1. Reliability Evaluation Overview

1.1. Objective and reliability strategy

Aim of this document is to present the reliability evaluation plan to release in mass production the products designed in Power MOSFET Silicon Carbide SiC Gen3 Technology, both for Automotive and Standard application domains, diffused in ST CT6 Catania (Italy) 6" Wafer Fab and assembled in HIP247 package in ST Shenzhen STS (China) Assembly Plant with use of Sumitomo Molding Compound.

The list of SiC Gen3 products assembled in HIP247 package that will have this improvement is long including die size in the range 2.6–26 mm² and Vds values in the range 1200–1700V.

The family of products designed in SiC Gen3 Technology in combination with HIP247 package in Shenzhen Assembly Plant was positively evaluated in agreement with AEC-Q101 rev.E and ST 0061692 guidelines in 2022 as per RERPTD21068.

Basing on above mentioned data, in order to assess the release of Resin Sumitomo G720EH for products designed in SiC Gen3 Technology in combination with HIP247 package, assembled in ST Shenzhen STS (China) a reliability evaluation on 3 lots of a selected test vehicles is needed.

Here below listed the chosen test vehicles selected to cover the features in terms of maximum and minimum die size and maximum Vds value inside the product family:

Commercial Product	Silicon line	Die size (mm²)	Vds (V)	Reliability items
SCT1000N170AG	K17F	2.6	1700	PV, TC, AC, HTRB, HTGB, HV- H3TRB, IOL, DPA on 1 lot
SCT070W120G3AG	BRJ7	6.9	1200	PV, TC, AC, HTRB, HTGB, HV– H3TRB, IOL, DPA on 1 lot
SCT015W120G3AG	BRJO	26	1200	PV, TC, AC, HTRB, HTGB, HV– H3TRB, IOL, DPA on 1 lot (with process implementation)

The reliability evaluations will be done in agreement with AEC-Q101 rev.E and ST 0061692 guidelines performing the stress test on 1 lot of each indicated product.

This strategy will allow to cover all the needed item to address the resin change on the entire SiC product family in HIP247 package.

Dedicated test, in addition to the ones already mentioned will be performed if needed to cover specific customer's requirement and any feature gap not known.

Details of each stress test and relevant conditions are reported in the table at section 2.



Test Plan

life.augmented

AEC-Q101 Test Plan Table

TEST GROUP	#	Data Type	TEST NAME	DESCRIPTION / COMMENTS	TEST FLAG
Α	A1	1	PC	Preconditioning	Not Applicable
ACCELERATED	A2	1	HAST	Highly Accelerated Stress Test	No
ENVIRONMENT STRESS	A2 alt	1	H3TRB	High Humidity High Temp. Reverse Bias	Yes
TESTS	A3	1	UHAST	Unbiased Highly Accelerated Stress Test	No
	A3 alt	1	AC	Autoclave	Yes
	A4	1	TC	Temperature Cycling	Yes
	A4a	1	TCHT	Temperature Cycling Hot Test	Yes
	A4a alt	1	TCDT	Temperature Cycling Delamination Test	Yes
	A5	1	IOL	Intermittent Operational Life	Yes
	A5alt	1	PTC	Power Temperature Cycling	No
В	B1	1	HTRB	High Temperature Reverse Bias	Yes
ACCELERATED LIFETIME	Bla	1	ACBV	AC blocking voltage	Not Applicable
SIMULATION TESTS	B1b	1	SSOP	Steady State Operational	Not Applicable
	B2	1	HTGB	High Temperature Gate Bias	Yes
С	C1	1	DPA	Destructive Physical Analysis	Yes
PACKAGE ASSEMBLY	C2	2	PD	Physical Dimension	Yes
INTEGRITY TESTS	C3	3	WBP	Wire Bond Pull Strength	Yes
	C4	3	WBS	Wire Bond Shear Strength	Yes
	C5	3	DS	Die Shear	Yes
	C6	2	TS	Terminal Strength	No
	C7	2	RTS	Resistance to Solvents	No
	C8	2	RSH	Resistance to Solder Heat	No
	C9	3	TR	Thermal Resistance	No
	C10	2	SD	Solderability	No
	C11	3	WG	Whisker Growth Evaluation	Not Applicable
	C12	2	CA	Constant Acceleration	Not Applicable
	C13	2	VVF	Vibration Variable Frequency	Not Applicable
	C14	2	MS	Mechanical Shock	Not Applicable
	C15	2	HER	Hermeticity	Not Applicable
D DIE FABRICATION RELIABILITY TESTS	DI	3	DI	Dielectric Integrity	Yes
E	EO	1	EV	Eternal Visual	Yes
ELECTRICAL	E1	1	TEST	Pre- and Post-Stress Electrical Test	Yes
VERIFICATION TESTS	E2	1	PV	Parametric Verification	Yes
	E3	1	ESDH	ESD HBM Characterization	Yes
	E4	2	ESDC	ESD CDM Characterization	Yes
	E5	3	UIS	Unclamped Inductive Switching	Not Applicable
	E6	3	SC	Short Circuit Characterization	No

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2. Test summary details

2.1. Test Summary table

Test method revision reference is the one active at the date of reliability trial execution.

Test	#	Reference	AEC-Q101 (Group A - ACCELERATED ENVIRONMENT STRESS TESTS) STM Test Conditions	Lots	S.S.	Total	Comments
РС	A1	-	-	-	-	-	Not Applicable
HAST	A2	-	-	-	-	-	NO, covered by H3TRB
H3TRB	A2 alt	JESD22A- 101	1000h @ Ta=85°C, RH=85% Vds= 960V (BRJO & BRJ7) 1000h @ Ta=85°C, RH=85% Vds= 1360V (K17F)	3	77	231	
UHAST	A3	-	-	-	-	-	NO, covered by AC
AC	A3 alt	JESD22 A- 102	Autoclave (Ta=121°C, Pa=2atm for 96 hours)	3	77	231	
тс	A4	JESD22A-	Ta=-55°C /+150°C Duration= 1000cy				
ТСНТ	A4a	104 Appendix 6	125°C TEST after TC	3	77	7 231	231
TCDT	A4a alt	J–STD–035	100% AM inspection after TC				
IOL	A5	MIL-STD- 750 Method 1037	15Kcy / ΔTj ≥ 100°C	3	77	231	
РТС	A5alt	-	-	-	-	-	NO, covered by IOL

Test	#	Reference	AEC-Q101 (Group B - ACCELERATED LIFETIME SIMULATION TESTS) STM Test Conditions	Lots	S.S.	Total	Comments
HTRB	B1	JESD22 A- 108	Tj=200°C, Vds=1360V (K17F), 1200V (BRJO and BRJ7) 1000h	3	77	231	
ACBV	Bla	-	-	-	-	-	Not Applicable Thyristors only
SSOP	B1b	_	-	-	-	-	Not Applicable Voltage Regulator only
HTCR	B 2	JESD22 A- HTGB + 108 Tj=200°C, Vgs= 22V, 1000h HTGB - Tj=200°C, Vgs= -10V, 1000h	HTGB + Tj=200°C, Vgs= 22V, 1000h	3	77	231	
mub	02		HTGB – Tj=200°C, Vgs= –10V, 1000h	3	77	231	

Test	#	Reference	AEC-Q101 (Group C – PACKAGE ASSEMBLY INTEGRITY TESTS) STM Test Conditions	Lots	S.S.	Total	Comments
DPA	C1	AECQ101-004 Section 4	-	3	4	12	Devices after TC and H3TRB where done (1 lot / Test Vehicle)

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Automotive and Discrete Group

Q&R Catania Team

PD	C2	JEDEC JESD22-B-100	-	-	-	-	
WBP	C3	MIL-STD-750-2 Method 2037	MIL-STD-750-2 Method 2037		-	-	
WBS	C4	AEC Q101-003 JESD22 B116	AEC Q101-003 JESD22 B116		-	-	
DS	C5			-	-	-	
TS	C6	Not applicable: only	Not applicable: only for new package.				Data type 2
RTS	C7						
RSH	C8	JEDEC JESD22-A- 111 (SMD)		-	-	-	
TR	С9	JESD24-3, 24-4, 24-6	JESD24-3, 24-4, 24-6		-	-	
SD	C10	JEDEC J–STD–002	-	-	-	-	
WG	C11	Not applicable: only	for new package.	-	-	-	
CA	C12						
VVF	C13	Not applicable: only	-	-	-		
MS	C14	for hermetic packages.		-	-	-	
HER	C15]	-	-	-]	

Test	#	Reference	AEC-Q101 (Group D - DIE FABRICATION RELIABILITY TESTS) STM Test Conditions	Lots	S.S.	Total	Comments
DI	D1	AEC Q101-004 Section 3	-	3	5	15	

Test	#	Reference	AEC-Q101 (Group E – ELECTRICAL VERIFICATION TESTS) STM Test Conditions	Lots	S.S.	Total	Comments
EV	EO	JEDEC JESD22-B101	II qualification parts submitted for testing		539	1617	
TEST EI			User specification or supplier's standard specification	3	539	1617	All qualification parts
PV	E2		All parameters according to user specification	3	25	75	
ESDH	E3	AEC-Q101-001	ESD HBM Characterization	-	-	-	No impact on active area or on
ESDC	ESDC E4 AEC-Q101-005		ESD CDM Characterization	-	-	-	gate oxide
UIS	E5	AEC-Q101-004 Section 2		-	-	-	Not applicable
SC	E6	-	-	-	-	-	No



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MOSFET Silicon Carbide SiC Technology Product family diffused in ST CT6 Catania (Italy) and assembled in Package HIP247 ST Shenzhen STS (China) Sumitomo Moulding Compound Interim Reliability Evaluation Report

general Information on selected test vehicles						
	TV1: SCT015W120G3AG					
Commercial Product	TV2: SCT1000N170AG					
	TV3: SCT070W120G3AG					
	TV1: BRJO					
Product Line	TV2: K17F					
	TV3: BRJ7					
Silicon process Technology	SIC MOSFET GEN3					
Package	HIP247					

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	(evision history										
	Rev.	Changes descriptio	n	Author	Date						
1 First release		First release	G.Allegra		May 28 th 2024						
	Approved	by									
	Function		Location	Name	Date						
Division Reliability Manager			ST Agrate (Italy)	M. De Tomasi	May 28 th 2024						

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1. Reliability Evaluation Overview

1.1. Objective

Aim of this document is to present the reliability evaluations performed on selected test vehicles to release in mass production the products designed in Power MOSFET Silicon Carbide SiC Technology, both for Automotive and Standard application domains, diffused in ST CT6 Catania (Italy) 6" Wafer Fab and assembled in package HIP247 in ST Shenzhen STS (China) Assembly Plant, with use of Sumitomo molding compound.

The SiC MOSFET GEN3 Technology products diffused in ST CT6 Catania (Italy) in combination with HIP247 package families in STS (China) were positively evaluated in 2022–2023 period in agreement with ST0061692 and AEC Q101 Rev.E guidelines (RERPTD21068).

Here below listed the chosen test vehicles selected to cover the features in terms of maximum and minimum die size and maximum and minimum Vds values inside the mentioned product family:

- SCT015W120G3AG (BRJO as ST internal line), 26 mm² (max die size) Vds breakdown voltage 1200V, with ROUND TEOS and Double Passivation
- SCT1000N170AG (K17F as ST internal line), 2.6 mm² (min die size), Vds breakdown voltage 1700V (max Vds), with Single Passivation
- SCT070W120G3AG (BRJ7 as internal line), 6.9 mm², Vds breakdown voltage 1200V, with Single Passivation

These are Power MOSFET Silicon Carbide based intended for Automotive application. Performed reliability verifications is running with positive interim results (HTRB, TC, AC).

1.2. Reliability Strategy and Test Plan

1.2.1. Reliability strategy

Reliability trials performed as part of this reliability evaluation are in agreement with ST 0061692 and **AEC-Q101 rev E** specification and are listed in below Test Plan by using 1 lot per each Test Vehicle product. For details on test conditions, generic data used and specifications references refer to test results summary in section 3.

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1.2.2. Test Plan

AEC-Q101 Test Plan Table

TEST GROUP	#	Data Type	TEST NAME	DESCRIPTION / COMMENTS	TEST FLAG
Α	A1 1 PC Preconditioning		No		
ACCELERATED	A2	1	HAST	Highly Accelerated Stress Test	No
ENVIRONMENT STRESS	A2 alt	1	H3TRB	High Humidity High Temp. Reverse Bias	Yes
TESTS	A3	1	UHAST	Unbiased Highly Accelerated Stress Test	No
	A3 alt	1	AC	Autoclave	Yes
	A4	1	тс	Temperature Cycling	Yes
	A4a	1	ТСНТ	Temperature Cycling Hot Test	Yes
	A4a alt	1	TCDT	Temperature Cycling Delamination Test	Yes
	A5	1	IOL	Intermittent Operational Life	Yes
	A5alt	1	РТС	Power Temperature Cycling	No
В	B1	1	HTRB	High Temperature Reverse Bias	Yes
ACCELERATED LIFETIME	B1a	1	ACBV	AC blocking voltage	Not Applicable
SIMULATION TESTS	B1b	1	SSOP	Steady State Operational	Not Applicable
	B2	1	HTGB	High Temperature Gate Bias	Yes
С	C1	1	DPA	Destructive Physical Analysis	Yes
PACKAGE ASSEMBLY	C2	2	PD	Physical Dimension	Yes
INTEGRITY TESTS	C3	3	WBP	Wire Bond Pull Strength	Yes
	C4	3	WBS	Wire Bond Shear Strength	Yes
	C5	3	DS	Die Shear	Yes
	C6	2	TS	Terminal Strength	No
	C7	2	RTS	Resistance to Solvents	No
	C8	2	RSH	Resistance to Solder Heat	No
	C9	3	TR	Thermal Resistance	Yes
	C10	2	SD	Solderability	No
	C11	3	WG	Whisker Growth Evaluation	Not Applicable
	C12	2	CA	Constant Acceleration	Not Applicable
	C13	2	VVF	Vibration Variable Frequency	Not Applicable
	C14	2	MS	Mechanical Shock	Not Applicable
	C15	2	HER	Hermeticity	Not Applicable
D DIE FABRICATION RELIABILITY TESTS	DI	3	DI	Dielectric Integrity	Yes
E	EO	1	EV	External Visual	Yes
ELECTRICAL	TRICAL E1 1 TEST Pre- and Post-Stress Electrical Test		Pre- and Post-Stress Electrical Test	Yes	
VERIFICATION TESTS	E2	1	PV	Parametric Verification	Yes
	E3	1	ESDH	ESD HBM Characterization	Yes
	E4	2	ESDC	ESD CDM Characterization	Yes
	E5	3	UIS	Unclamped Inductive Switching	Not Applicable
	E6	3	SC	Short Circuit Characterization	No

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1.3. Conclusion

Performed reliability verifications is running with positive interim results, Neither electrical nor parametric rejects were detected at final electrical testing.

Based on the overall achieved positive interim results obtained on the evaluations performed on the selected Test Vehicles in agreement to with ST 0061692 and AEC-Q101 Rev.E specification, we can release the risk production for the products designed in Power MOSFET Silicon Carbide SiC Gen3 Technology, both for Automotive and Standard application domains, diffused in ST CT6 Catania (Italy) 6" Wafer Fab and assembled in package HIP247 in ST Shenzhen STS (China) Assembly Plant, with SUMITOMO molding compound.

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2. Product Characteristics

2.1. Generalities

2.1.1. Test vehicle BRJ7, K17F, BRJO



Automotive-grade silicon carbide Power MOSFET 1200 V, 63 mΩ typ., 30 A in an HiP247 package

HIP247

Order code Vos Rostem typ. Jo SCT070W120G3AG 1200 V 63 mD 30 A AEC-Q101 qualified Very low R_{QS(on)} over the entire temperature range . . . High speed switching performances . . .

- Very fast and robust intrinsic body diode
- Very high operating junction temperature capability (T_J = 200 $^{\circ}$ C)

Applications

Features

- Main inverter (electric traction) DC/DC converter for EV/HEV
- DC/DC converter for EV/HE On board charger (OBC)
- Un board charger (UBC)

Description

This silicon carbide Power MOSFET device has been developed using ST's advanced and innovative 3rd generation SiC MOSFET technology. The device features a very low R_{DS(on)} over the entire temperature range combined with low capacitances and very high switching operations, which improve application performance in frequency, energy efficiency, system size and weight reduction.

Product status link

Product summary		
Order code	SCT070W120G3AG	
Marking	70W120G3AG	
Package	HiP247	
Packing	Tube	

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SCT1000N170

Datasheet

Silicon carbide Power MOSFET 1700 V, 1.0 Ω typ., 7 A in an HiP247 package



HiP247



	1
1.3 Ω	7 A
	1.3 Ω

- Very fast and robust intrinsic body diode
- Low capacitances
- Very high operating junction temperature capability (T_J = 200 °C)

Applications

Features

- Auxiliary power supply for server
- Switch mode power supply

Description

This silicon carbide Power MOSFET is produced exploiting the advanced, innovative properties of wide bandgap materials. This results in unsurpassed on-resistance per unit area and very good switching performance almost independent of temperature. The outstanding thermal properties of the SiC material, combined with the device's housing in the proprietary HiP247 package, allows designers to use an industry standard outline with significantly improved thermal capability. These features render the device perfectly suitable for high-efficiency and high power density applications.



SCT1000N170					
Order code	SCT1000N170				
Marking	SCT1000N170				
Package	HIP247				
Packing	Tube				

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SCT015W120G3AG

Target - Datasheet

Automotive silicon carbide Power MOSFET 1200 V, 128 A, 15 m Ω (typ., T_J = 25 °C) in a HiP247 package.

Order code	Vos	Rosion	16
SCT015W120G3AG	1200V	15mΩ	128A

Features

- :
- Very low RDS(on) over the entire temperature range High speed switching performances Very fast and robust intrinsic body diode Very high operating temperature capability (Tj=200°C)

Applications





- Switching mode power supply
 Power supply for renewable energy systems
 High Frequency DC-DC converter

Description

This silicon carbide Power MOSFET has been developed using ST's advanced and innovative 3rd generation SIC MOSFET technology. The device features remarkably low on-resistance per unit area and very good switching performance.

Maturi	ty status link				
SCT015W120G3AG					
Devic	e summary				
Order code	SCT015W120G3AG				
Marking	TBD				
Package	HiP247				
Packing	Tube				

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2.2. Traceability

2.2.1. Wafer Fab information

Wafer fab name / location	CTWF 6" (Catania – Italy)
Wafer diameter (inches)	6"
Silicon process technology	SiC MOSFET Gen3
Die finishing front side	SiN/Polymide (SCT015W120G3AG) Polymide (SCT1000N170AG) Polymide (SCT070W120G3AG)
Die finishing back side	Ti-Ni-Au
Die size (micron)	5100 x 5100 (SCT015W120G3AG) 1600 x 1600 (SCT1000N170AG) 2800 x 2450 (SCT070W120G3AG)
Metal levels/ materials/ thicknesses	1 / AlSiCu /4.5um

2.2.2. Assembly information

3. Assembly plant name / location	ST Shenzhen STS (China)				
Package descrition	HIP247				
Lead frame/Substrate	HIP247 3L Mon Th2 Ve2 OpA SelNi/NiP				
Die attach material	Preform Pb/Ag/Sn 95.5/2.5/2				
Wire bonding material/diameter	WIRE AI 7 and 20 MILS (SCT015W120G3AG) WIRE AI 5 and 7 MILS (SCT1000N170AG) WIRE AI 5 and 10 MILS (SCT070W120G3AG)				
Molding compound material	Sumitomo G720EH				
Package Moisture Sensitivity Level (JEDEC J–STD020D)	Not applicable				

3.1.1. Reliability Testing information

Reliability laboratory location	STM Catania (Italy)
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4. Tests Results Summary

4.1. Lot Information

Lot #	Commercial Product	Product Line	Diffusion Lot	Tracecode
Lot1	SCT015W120G3AG	BRJO	VV349739	GK4192UT02
Lot2	SCT1000N170AG	K17F	VV301279	GK402603
Lot3	SCT070W120G3AG	BRJ7	VV334088	G348TMFRQ

4.2. Test results summary (table)

Test method revision reference is the one active at the date of reliability trial execution.

Test	#	Reference	AEC-Q101 (Group A – ACCELERATED ENVIRONMENT STRESS TESTS) STM Test Conditions	Lots	S.S.	Total	Results Fail/SS/lots	Comments
РС	A1	JESD22-A113 J-STD-020	Not applicable	-	-	-	-	
HAST	A2	-	-	-	-	-	-	NO, covered by H3TRB
			1000h @ Ta=85°C, RH=85% Vds= 100V	3	77	231	/77/3 To be start	
H3TRB	A2 alt	JESD22A-101	1000h @ Ta=85°C, RH=85% Vds= 960V (BRJO & BRJ7) 1000h @ Ta=85°C, RH=85% Vds= 1360V (K17F)	3	77	231	/77/3 To be start	
UHAST	A3	-	-	-	-	_	-	NO, covered by AC
AC	A3 alt	JESD22 A-102	Autoclave (Ta=121°C, Pa=2atm for 96 hours)	3	77	231	0/77/3	
тс	A4		Ta=-55°C /+150°C Duration= 1000cy					
тснт	A4a	JESD22A-104 Appendix 6 I-STD-035	125°C TEST after TC	3	77	231	0/77/2 BRJO to be start	
TCDT	A4a alt		100% AM inspection after TC					
IOL	A5	MIL-STD-750 Method 1037	15Kcy / ΔTj ≥ 100°C	3	77	231	/77/3 To be start	

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								NO,
PTC	A5alt	-	-	-	-	-	-	covered by
								IOL

Test	#	Reference	AEC-Q101 (Group B – ACCELERATED LIFETIME SIMULATION TESTS) STM Test Conditions	Lots	S.S.	Total	Results Fail/SS/lots	Comments
HTRB	B1	JESD22 A-108	Tj=200°C, Vds=1360V (K17F), 1200V (BRJO and BRJ7) 1000h	3	77	231	0/77/3	
ACBV	Bla	-	-	-	-	_	-	Not Applicable Thyristors only
SSOP	B1b	-	-	-	-	_	-	Not Applicable Voltage Regulator only
HTCR	۶D	JESD22	HTGB + Tj=200°C, Vgs= 22V, 1000h	3	77	231	/77/3 To be start	
HTGB B2		2 A-108	HTGB – Tj=200°C, Vgs= –10V, 1000h	3	77	231	/77/3 To be start	

Test	#	Reference	AEC-Q101 (Group C - PACKAGE ASSEMBLY INTEGRITY TESTS) STM Test Conditions	Lots	S.S.	Total	Results Fail/SS/lots	Comments
DPA	C1	AECQ101-004 Section 4		3	4	12	-	Devices after TC and H3TRB
PD	C2	JEDEC JESD22-B-100		1	30	30	Done	From assembly data
WBP	C3	MIL-STD-750-2 Method 2037		1	5	5	Done	From assembly data
WBS	C4	AEC Q101-003 JESD22 B116		1	5	5	Done	From assembly data
DS	C5	MIL-STD-750-2 Method 2017		1	5	5	Done	From assembly data
TS	C6	MIL-STD-750-2 Method 2036	Not applicable: only for	-	-	-		
RTS	C7	JESD22-B-107	new package	-	-	-		
RSH	C8	JESD22-A-111		-	-	-		

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TR	C9	JESD24–3, 24–4, 24–6		1	10	10	Done	
SD	C10	JEDEC J-STD-002	Not applicable: only for new package	-	-	-		
WG	C11	Not applicable: only f	Not applicable: only for new package.			-	-	
CA	C12		Not applicable: only for new package. Items			-	-	
VVF	C13	Not applicable: only f				-	-	
MS	C14	hermetic packages.		-	-	-	-	
HER	C15		iermetic packages.			-	-	

Test	#	Reference	AEC-Q101 (Group D – DIE FABRICATION RELIABILITY TESTS) STM Test Conditions	Lots	S.S.	Total	Results Fail/SS/lots	Comments
DI	D1	AEC Q101-004 Section 3	-	3	5	15	running	

Test	#	Reference	AEC-Q101 (Group E – ELECTRICAL VERIFICATION TESTS) STM Test Conditions	Lots	S.S.	Total	Results Fail/SS/lots	Comments
EV	EO	JEDEC JESD22- B101	All qualification parts submitted for testing	3	616	1848	Running	All samples
TEST	E1		User specification or supplier's standard specification	3	616	1848	Running	
PV	E2		All parameters according to user specification	3	25	75	Running	1 lots per each TV
ESDH	E3	AEC-Q101- 001	ESD HBM Characterization	3	30	90		1 lot per each TV
ESDC	E4	AEC-Q101- 005	ESD CDM Characterization	3	30	90		1 lot per each TV
UIS	E5	AEC-Q101- 004 Section 2		-	_	-		Not applicable
SC	E6	-	-	-	-	-		No

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Product/process change notification: Mold Compound replacement on STS Plant for industrial devices

POWER AND DISCRETE PRODUCTS/24/14840

Product family	Technology	Package
SiC MOS	Gen 1 and Gen 3	TO247

Description of the change	
---------------------------	--

Mold Compound replacement on STS Plant for industrial devices with resin Resin SUMITOMO.

Reason

KYOCERA KEG-300S-1 out of production.

Date of implementation	
WK39'24	

Impact of the char	Impact of the change					
Form						
Fit						
Function						
Reliability						
Processibility	X					



Product/process change notification: Mold Compound replacement on STS Plant for industrial devices POWER AND DISCRETE PRODUCTS/24/14840

Qualification of the change

Qualification Report: see attached document.

APPENDIX 1: RISK ASSESSMENT

#	Risks identified	Resulting potential risk	Class (low/medium/high)	Possible action
1				
2				
3				
4				
5				

POWER AND DISCRETE PRODUCTS/24/14840

APPENDIX 2: QUALIFICATION PLAN

#	Test name	Conditions	Sample size	Criteria
1				
2				
3				
4				
5				

APPENDIX 3: QUALIFICATION EXECUTION AND RESULT

#	Test name	Conditions	Sample size	Criteria	When	Status	Result
1							
2							
3							
4							
5							



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