



## Customer Information Notification

202501032I : Datasheet Correction for Several NXP UART Bridge Devices

**Note:** This notice is NXP Company  
Proprietary.

**Issue Date:** Feb 12, 2025 **Effective Date:** Feb 13, 2025

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### Management summary

NXP announces a SC16IS741A, SC16IS740/750/760, SC16C752B and SC16IS752;  
SC16IS762 datasheet correction.

### Change Category

<input type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input type="checkbox"/> Test Process	<input type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Equipment	<input type="checkbox"/> Errata
<input type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Location	<input type="checkbox"/> Electrical spec./Test coverage

☐ Firmware ☒ Other: Datasheet

### Notification Overview

#### Description

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Corrected the value of the first item of column "Divisor used to generate 16x clock" from 2304 to 3840 in the table "Baud rates using a 3.072 MHz crystal"

## Reason

Correction to the 50 bit/s divisor value from 2304 to 3840 for a 3.072 MHz crystal (due to a typo).

## Anticipated Impact on Form, Fit, Function, Reliability or Quality

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No Impact on form, fit, function, reliability or quality

## Data Sheet Revision

A new data sheet will be issued

## Additional information

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Additional documents: [view online](#)

## Contact and Support

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For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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**Position** Product marketer

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NXP Quality Management Team.

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