



Customer Information Notification

202501018I : S32K3 Family and S32K3E Family Errata Update

Note: This notice is NXP Company Proprietary.

Issue Date: Mar 23, 2025 **Effective Date:** Mar 24, 2025

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Change Category

- | | | | | |
|--|--|--|--|---|
| <input type="checkbox"/> Wafer
Fab
Process | <input type="checkbox"/> Assembly
Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test
Process | <input type="checkbox"/> Design |
| <input type="checkbox"/> Wafer
Fab
Materials | <input type="checkbox"/> Assembly
Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test
Equipment | <input checked="" type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer
Fab
Location | <input type="checkbox"/> Assembly
Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test
Location | <input type="checkbox"/> Electrical
spec./Test
coverage |
| <input type="checkbox"/> Firmware <input type="checkbox"/> Other | | | | |

Notification Overview

Description

NXP Semiconductors announces an errata update for the S32K3 Family and S32K3E Family devices associated with this notification.

1. S32K344 (inclusive of S32K314, S32K324) mask set 0P55A/1P55A:
S32K344_0P55A/1P55A from Rev 12/Dec/2023 to Rev 21/Jan/2025

3. S32K342 (inclusive of S32K322, S32K341) mask set 0P97C: S32K342_0P97C from Rev 14/Dec/2023 to Rev 21/Jan/2025
2. S32K312 mask set 0P09C: S32K312_0P09C from Rev 14/Dec/2023 to Rev 22/Jan/2025
4. S32K311 (inclusive of S32K310) mask set 0P98C: S32K311_0P98C from Rev 24/May/2023 to Rev 21/Jan/2025
6. S32K388 mask set 0P39J/0P24N: S32K388_0P39J/0P24N from Rev 1, 7/2024 to Rev 2, 1/2025
5. S32K358 (inclusive of S32K328, S32K338, S32K348) mask set 1P14E: S32K358_1P14E from Rev 2, 1/2024 to Rev 3, 1/2025
7. S32K396 (inclusive of S32K39x, S32K37x, S32K36x) mask set 1P40E: S32K396_1P40E from Rev MAR/2024 to Rev 4, 1/2025

The revision history included in the updated document provides a detailed description of the change.

Please see document revision history for more information.

The updated errata documents referenced above are attached to this notification and can be found at the below link:

<https://www.nxp.com/products/S32K3>

<https://www.nxp.com/products/S32K39-37-36>

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02.

Reason

The Errata have been updated to provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

No changes were made to the current production device. The errata describe existing conditions identified on current production devices.

Data Sheet Revision

No impact to existing data sheet

Additional information

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name	Support Tickets
Position	NXP Support
e-mail address	http://www.nxp.com/support

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NXP Quality Management Team.

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