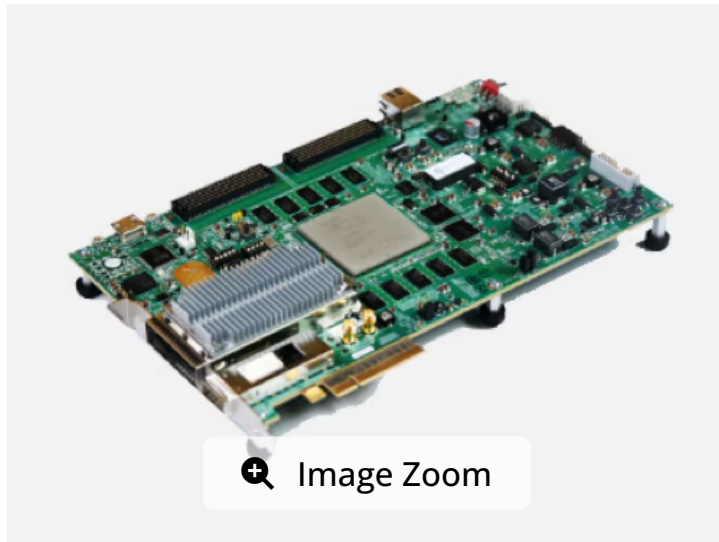




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AMD Virtex™ UltraScale™ FPGA VCU108 Evaluation Kit

by: [AMD](#)



The AMD Virtex™ UltraScale™ FPGA VCU108 Evaluation Kit is the perfect development environment for evaluating the unprecedented levels of performance, system integration and bandwidth provided by Virtex UltraScale devices. This kit provides an ideal platform for prototyping systems that

require massive data flow and packet processing such as 400+ Gbps systems, large-scale emulation and high performance computing.

Part Number:

EK-U1-VCU108-G

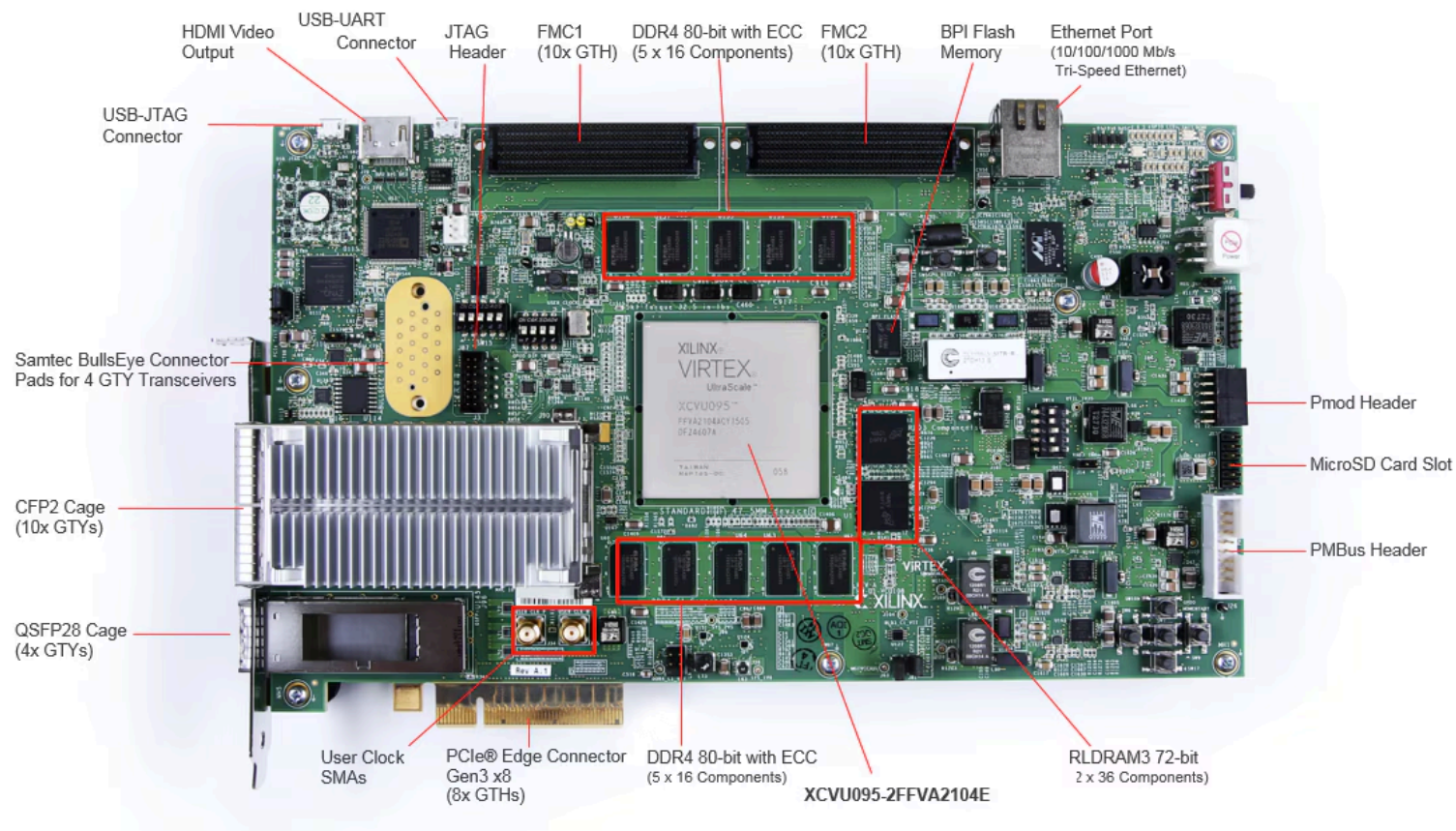
Lead Time: 2 Weeks**Device Support:** Virtex UltraScale[Add to Cart !\[\]\(de95854c7ee024cfadc48187bbb781b2_img.jpg\)](#)

Product Information

[Features & Devices](#)[Specifications](#)[What's Inside](#)

Board Features

Featuring the VCU108 Base Board



Communication & Networking

Clocking

- SI5335A Quad Clock Generator

Display

- HDMI video output

Expansion Connectors



- 4 x 28Gbps CFP2 cage
- 4 x 28Gbps QSPF28 cage
- GTY port(4 TX & RX) with pads for Samtec Bullseye Cable
- Dual USB-to-UART Bridge with micro-B USB connector
- PCI Express endpoint Gen3 x 8
- 10/100/1000 Mbps Ethernet (SGMII)
- Si570 IIC Programmable LVDS Clock Generator
- SI5328C Clock Multiplier and Jitter Attenuator
- Subminiature version A (SMA) connectors (differential)
- User & Status LEDs
- FMC HPC0 connector (ten GTH transceivers)
- FMC HPC1 connector (ten GTH transceivers)
- PMOD (1x6 0.1" Header)

Configuration

- Onboard JTAG configuration circuitry to enable configuration over USB
- JTAG header provided for use with AMD

Memory

- Two 4 GB DDR4 component memory interfaces (five [256 Mb x 16] devices each)
- 4 MB RLD3 component

Control & I/O

- User Push Buttons (x5)
- User DIP Switch (4-position)
- SMA User I/O (Diff Pair)

Power

- AC Power adapter (12V) or ATX

Analog

- XADC header Power management with 

download cables
such as the
Platform Cable
USB II

- 1 Gb Linear x16
BPI flash memory

memory
interfaces (five
[256 Mb x 16]
devices each)IIC
EEPROM: 8Kb

- Micro Secure
Digital (SD)
connector

PMBus voltage
monitoring
through Maxim
power controllers
and GUI, current
monitoring via
FPGA SYSMON
GUI

Resources

Documentation 

[VCU108 Documentation >](#)

