



Customer Information Notification

202601025I : S32E2/Z2 Reference Manual Update from Rev5 to Rev5.1

Note: This notice is NXP Company Proprietary.

Issue Date: Mar 26, 2026 **Effective Date:** Mar 27, 2026

Dear Emma Tempest,

Here is your personalized communication about an NXP notification.
For detailed information we invite you to [view this notification online](#)

Change Category

☐ Wafer Fab Process	☐ Assembly Process	☐ Product Marking	☐ Test Process	☐ Design
☐ Wafer Fab Materials	☐ Assembly Materials	☐ Mechanical Specification	☐ Test Equipment	☐ Errata
☐ Wafer Fab Location	☐ Assembly Location	☐ Packing/Shipping/Labeling	☐ Test Location	☐ Electrical spec./Test coverage

☐ Firmware ☒ Other: Reference Manual Update

Notification Overview

Description

NXP Semiconductors announces S32E2 and S32Z2 maskset P91J and N72W Reference Manual Update from Rev5 to Rev5.1.

Rev5.1 is an addendum update to the S32E2 and S32Z2 Rev5 Reference Manual.

The S32E2 and S32Z2 Reference Manual documents can be downloaded from:

<https://www.nxp.com/products/S32E2>

<https://www.nxp.com/products/S32Z2>

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02

Reason

The Reference Manual has been updated to provide additional technical clarification on some device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name NXP Support Tickets

**e-mail
address** <http://www.nxp.com/support>

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NXP Quality Management Team.

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