



Product Change Notification: SYST-18SGBM692

Date:

30-Mar-2026

Product Category:

8-Bit Microcontrollers

Notification Subject:

PIC18F26/46/56Q35 Family Silicon Errata and Data Sheet Clarifications

Affected CPNs:

[SYST-18SGBM692_Affected_CPN_03302026.pdf](#)

[SYST-18SGBM692_Affected_CPN_03302026.csv](#)

Notification Text:

SYST-18SGBM692

Microchip has released a new Document for the PIC18F26/46/56Q35 Family Silicon Errata and Data Sheet Clarifications of devices. If you are using one of these devices please read the document located at [PIC18F26/46/56Q35 Family Silicon Errata and Data Sheet Clarifications](#).

Notification Status: Final

Description of Change:

Initial release

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 30 Mar 2026

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

PIC18F26/46/56Q35 Family Silicon Errata and Data Sheet Clarifications

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Affected Catalog Part Numbers (CPN)

PIC18F26Q35-E/3LW

PIC18F56Q35-E/6MX

PIC18F26Q35-E/SP

PIC18F26Q35-E/SS

PIC18F46Q35-E/NHX

PIC18F46Q35-E/P

PIC18F46Q35-E/PT

PIC18F56Q35-E/PT

PIC18F26Q35-I/3LW

PIC18F56Q35-I/6MX

PIC18F26Q35-I/SP

PIC18F26Q35-I/SS

PIC18F46Q35-I/NHX

PIC18F46Q35-I/P

PIC18F46Q35-I/PT

PIC18F56Q35-I/PT

PIC18F26Q35T-I/3LW

PIC18F56Q35T-I/6MX

PIC18F26Q35T-I/SS

PIC18F46Q35T-I/NHX

PIC18F46Q35T-I/PT

PIC18F56Q35T-I/PT

Introduction

The PIC18F26/46/56Q35 devices that you have received conform functionally to the current device data sheet (DS40002671A), except for the anomalies described in this document.

The silicon issues discussed in the following pages are for silicon revisions with the Device and Revision IDs listed in the table below.

The errata described in this document will be addressed in future revisions of the PIC18F26/46/56Q35 silicon.

Note: This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current.

Table 1. Silicon Device Identification

Part Number	Device ID	Revision ID
		A0
PIC18F26Q35	0x7C60	0xA000
PIC18F46Q35	0x7CC0	0xA000
PIC18F56Q35	0x7D20	0xA000



Important: Refer to the **Device/Revision ID** section in the device data sheet for more detailed information on Device Identification and Revision IDs for your specific device.

Silicon Issue Summary

Table 2. Silicon Issue Summary

Module	Feature	Item No.	Issue Summary	Affected Revisions
				A0
Configurable Logic Block (CLB)	MFINTOSC (500 kHz)	1.1.1	MFINTOSC (500 kHz) Not Available as a CLB Input	X
	UARTx TX/RX	1.1.2	UARTx TX and RX Not Available as CLB Inputs	X
	SPI1 SDI	1.1.3	SDI1 Not Available as a CLB Input	X
Note: Only those issues indicated in the last column apply to the current silicon revision.				

1. Silicon Errata Issues

NOTICE

This document summarizes all silicon errata issues from all revisions of silicon, previous and current. Only the issues indicated by the bold font in the following tables apply to the current silicon revision.

1.1. Module: Configurable Logic Block (CLB)

1.1.1. MFINTOSC (500 kHz) Not Available as a CLB Input

The MFINTOSC (500 kHz) input is not available as an input to the CLB module. When the user selects the MFINTOSC (500 kHz), the MFINTOSC (32 kHz) signal is internally selected instead.

Work Around

If the MFINTOSC (500 kHz) signal is necessary, it can be routed through the CLC as an input to the CLB.

Affected Silicon Revisions

A0							
X							

1.1.2. UARTx TX and RX Not Available as CLB Inputs

The UART1 and UART2 TX and RX selections are not available as inputs to the CLB module. When the user selects the TX1, TX2, RX1, or RX2 input selections, the respective U1TXIF, U2TXIF, U1RXIF, and U2RXIF interrupt signals are internally selected instead.

Work Around

- If the TX1 and/or TX2 signals are necessary, they can be routed through a CLC as inputs to the CLB.
- If the RX1 and/or RX2 signals are necessary, they can be routed through CLBInxPPS as inputs to the CLB.

Affected Silicon Revisions

A0							
X							

1.1.3. SDI1 Not Available as a CLB Input

The SPI1 Serial Data Input (SDI1) selection is not available as an input to the CLB module. When the user selects the SDI1 input selection, the SPI1 Slave Select (SS1) signal is internally selected instead.

Work Around

If the SDI1 signal is necessary, it can be routed through CLBInxPPS as an input to the CLB.

Affected Silicon Revisions

A0							
X							

2. Data Sheet Clarifications

The following typographic corrections and clarifications are to be noted for the latest version of the device data sheet (DS40002671A):

Note:

Corrections are shown in **bold**. Where possible, the original bold text formatting has been removed for clarity.

2.1. None

There are no known data sheet clarifications as of this publication date.

3. Appendix A: Revision History

Doc. Rev.	Date	Comments
A	03/2026	Initial release of this document.

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Product Page Links

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