



Product Change Notification: SYST-15LAMW742

Date:

16-Jun-2026

Product Category:

Clock And Timing - Oscillators

Notification Subject:

Data Sheet - DSC60xxB - Ultra-Small, Ultra-Low Power MEMS Oscillator

Affected CPNs:

[SYST-15LAMW742_Affected_CPN_06162026.pdf](#)

[SYST-15LAMW742_Affected_CPN_06162026.csv](#)

Notification Text:

SYST-15LAMW742

Microchip has released a new Datasheet for the DSC60xxB - Ultra-Small, Ultra-Low Power MEMS Oscillator of devices. If you are using one of these devices please read the document located at [DSC60xxB - Ultra-Small, Ultra-Low Power MEMS Oscillator](#).

Notification Status: Final

Description of Change:

Changed typical values for all Period Jitter, RMS, and all Period Jitter, Peak-to-Peak, in the Electrical Characteristics table.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 16 Jun 2026

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

DSC60xxB - Ultra-Small, Ultra-Low Power MEMS Oscillator

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

DSC6003HA3B-016.0000

DSC6011HA2B-029.4912

DSC6001HA3B-PROG

DSC6003HA3B-PROG

DSC6003HA1B-020.0000

DSC6001HA2B-029.4912

DSC6021HA3B-01D0

DSC6003HA3B-032K768

DSC6011HA1B-032K768

DSC6011HA2B-032K768

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DSC6011HA2B-008.0000

DSC6013HA2B-048.0000

DSC6001MA2B-020.0000

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DSC6001HI1B-002K000T

DSC6001HI2B-048K000T

DSC6001HI1B-016.0000T

DSC6001HI3B-409K600T

DSC6001HI1B-014.7456T

DSC6013HI1B-019.2000T

DSC6011HI2B-020.0000T

DSC6003HI1B-027.1200T

DSC6013HI1B-027.1200T

DSC6003HI1B-032K768T

DSC6001HI2B-036.0000T

DSC6011HI2B-002.3000T

DSC6001HI1B-033.3333T

DSC6013HI1B-462K500T

DSC6011HI3B-016.0000T

DSC6041HI2B-008.1920T

DSC6001HI3B-009K000T

DSC6001HI1B-008.0000T

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DSC6011MI1B-012.2880T

DSC6001MI3B-026.0000T

DSC6001MI1B-024.5760T

DSC6011MI1B-050.0000T

DSC6041MI3B-039.3216T

DSC6011MI3B-039.3216T

DSC6003MI3B-026.0000T

DSC6003MI1B-350K000T

DSC6011MI2B-050.0000T

DSC6011MI2B-004.0000T

DSC6011MI2B-012.0000T

DSC6011MI2B-027.0000T

DSC6011MI2B-033.0000T
DSC6011MI2B-040.0000T
DSC6011MI2B-080.0000T
DSC6023MI2B-01TPT
DSC6001MI2B-016.6660T
DSC6001MI1B-025.0000T
DSC6021MI2B-01UAT
DSC6011MI2B-014.7456T
DSC6013MI1B-027.0000T
DSC6001MI2B-066.5600T
DSC6021MI2B-0207T
DSC6003MI1B-001.8432T
DSC6003MI1B-003.6864T
DSC6011MI1B-032K768T
DSC6001MI2B-024.0000T
DSC6001MI2B-016.0000T
DSC6001MI1B-002K000T
DSC6013MI3B-027.6000T
DSC6011MI1B-080.0000T
DSC6011MI1B-080.0000B
DSC6001MI2B-031.2500T
DSC6011MI3B-019.2000T
DSC6011MI2B-031.2500T
DSC6001MI2B-060.0000T
DSC6011JI2B-012.0000B
DSC6001JI3B-259K200T
DSC6001JI2B-001.0000T
DSC6003JI2B-250K000T
DSC6001JI2B-012.1732T
DSC6001JI3B-003K000T
DSC6051JI3B-024.0000T
DSC6051JI3B-050.0000T

DSC6011JI3B-327K680T

DSC6011JI2B-024.5760T

DSC6003JI1B-350K000T

DSC6011JI2B-064.0000T

DSC6001JI2B-026.0000T

DSC6051JI1B-008.0000T

DSC6023JI1B-01SQT

DSC6001JI1B-016.0000T

DSC6021JI2B-01STT

DSC6051JI2B-022.0000T

DSC6023JI2B-01TPT

DSC6003JI1B-012.0000T

DSC6001JI2B-016.7772T

DSC6011JI2B-020.0000T

DSC6013JI1B-063.2200T

DSC6021JI1B-01UJT

DSC6001JI1B-033.3333T

DSC6021JI2B-01VBT

DSC6021JI2B-01VET

DSC6023JI3B-01VHT

DSC6043JI3B-127K000T

DSC6003JI1B-004.0000T

DSC6011JI3B-016.0000T

DSC6001JI2B-058.9824T

DSC6011JI1B-006.3220T

DSC6001JI1B-035.0000T

DSC6003JI2B-004.5000T

DSC6003JI1B-004.5000T

DSC6001JI3B-003K200T

DSC6011JI2B-033.0000T

DSC6001JI2B-020.0000T

DSC6003JI3B-032K768T

DSC6001JI1B-050.0000T

DSC6011JI2B-512K000T

DSC6011JI1B-080.0000T

DSC6011JI1B-032K768T

DSC6001JI3B-115K200T

DSC6011JI1B-042.5000T

DSC6001JI2B-007.3728T

DSC6021JI2B-022UT

DSC6011JI1B-049K600T

DSC6011JI1B-040K960T

DSC6003JI1B-010K000T

DSC6001JI2B-033.3330T

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DSC6013JI3B-027.6000T

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DSC6001CI2B-032K768T

DSC6011CI1B-080.0000T

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DSC6003CI2B-307K000T

DSC6001CI2B-025.0000T

DSC6011CI2B-012.0000T

DSC6001CI3B-032K768T

DSC6001CI1B-025.0000T

DSC6011CI2B-024.0000T

DSC6001CI2B-004.0000T

DSC6021CI2B-01G3T

DSC6003CI2B-012.0000T

DSC6003CI2B-400K000T

DSC6003CI2B-024.0000T

DSC6001CI2B-012.0000T

DSC6011CI2B-025.0000T

DSC6011CI2B-040.0000T

DSC6023CI2B-023UT

DSC6003CI1B-010K000T

DSC6011CI2B-008.0000T

DSC6011CI1B-050.0000T

DSC6013CI1B-350K000T

DSC6011CI2B-032.7680T

DSC6011CI2B-016.0000T

DSC6001CI2B-100K000T

DSC6011CI1B-001.8432T

DSC6003CI1B-100K000T

DSC6001CI1B-012.0000T

DSC6001CI2B-457K000T

DSC6063CI3B-0298T

DSC6003CI1B-032K768T

DSC6021CI2B-02A8T

DSC6021CI2B-02A9T

DSC6061CI3B-02ABT

DSC6061CI3B-02ACT

DSC6001CI2B-024.0000T

DSC6001CI2B-064.0000T

DSC6011CI2B-012.2880T

DSC6001CA3B-025.0000T

DSC6011CI3B-750K000T

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DSC6021CI2B-435K000-430K000T

DSC6001BI3B-033.3333T

DSC6001BI3B-025.0000T

DSC6001BI3B-003.2768T

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DSC6001BI3B-002.4576T

DSC6001BI3B-002.7296T

DSC6001BI3B-003.0035T

DSC6021BI2B-028TT

DSC6023BI3B-029GT

DSC6011AI2B-025.0000T

DSC6011HA1B-016.0000T

DSC6001HA3B-029.4912T

DSC6021HA2B-020MT

DSC6021HA3B-021QT

DSC6003HA3B-100K000T

DSC6011HA3B-002.0000T

DSC6001HA3B-016.0000T

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DSC6053HA3B-307K200T

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DSC6003MA3B-080.0000T

DSC6003MA3B-100K000T

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DSC6011MA3B-024.0000T
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DSC6001JA3B-008.0000T
DSC6011JA2B-016K384T
DSC6011JA1B-004K000T
DSC6003JA2B-012.0000T
DSC6011JA2B-020.0000T
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DSC6001JA3B-001.0000T
DSC6011JA3B-008.0000T
DSC6011JA1B-018.4320T
DSC6001JA3B-025.0000T
DSC6011JA1B-032K768T
DSC6011JA1B-007.3728T
DSC6001JA3B-032K768T
DSC6001JA1B-270K000T
DSC6011JA1B-025.0000T
DSC6001CI2B-016.0000T
DSC6001CI1B-008.0000T
DSC6001CA3B-PROGT

DSC6003CA3B-PROGT

DSC6003CA1B-350K000T

DSC6001CA1B-025.0000T

DSC6003CA3B-040.0000T

DSC6003CA3B-052.0000T

DSC6003CA3B-460K800T

DSC6003BA1B-080.0000T

DSC6003BA3B-PROGT

DSC6001BA3B-PROGT

DSC6011BA3B-250K000T

DSC6001AA3B-PROGT

DSC6003AA3B-PROGT

DSC6011AA3B-400K000T

DSC6001AA1B-800K000T

DSC6021AA3B-026ET

Ultra-Small, Ultra-Low Power MEMS Oscillator

Features

- Wide Frequency Range: 2 kHz to 80 MHz
- Ultra-Low Power Consumption: 1.3 mA/1 μ A (Active/Standby)
- Ultra-Small Footprints
 - 1.6 mm $\times$ 1.2 mm VFLGA
 - 2.0 mm $\times$ 1.6 mm VFLGA
 - 2.5 mm $\times$ 2.0 mm VLGA
 - 3.2 mm $\times$ 2.5 mm VDFN
 - 5.0 mm $\times$ 3.2 mm VDFN
 - 7.0 mm $\times$ 5.0 mm VDFN
- Frequency Select Input Supports Two Pre-Defined Frequencies
- High Stability: ± 20 ppm, ± 25 ppm, ± 50 ppm
- Wide Temperature Range
 - Automotive: -40°C to $+125^{\circ}\text{C}$
 - For AEC-Q100 Qualified, Refer to DSA60xx Family
 - Ext. Industrial: -40°C to $+105^{\circ}\text{C}$
 - Industrial: -40°C to $+85^{\circ}\text{C}$
 - Ext. Commercial: -20° to $+70^{\circ}\text{C}$
- Excellent Shock and Vibration Immunity
 - Qualified to MIL-STD-883
- High Reliability
 - 20x Better MTF Than Quartz Oscillators
- Supply Range of 1.71V to 3.63V
- Short Sample Lead Time: <2 weeks
- Lead Free and RoHS Compliant

Applications

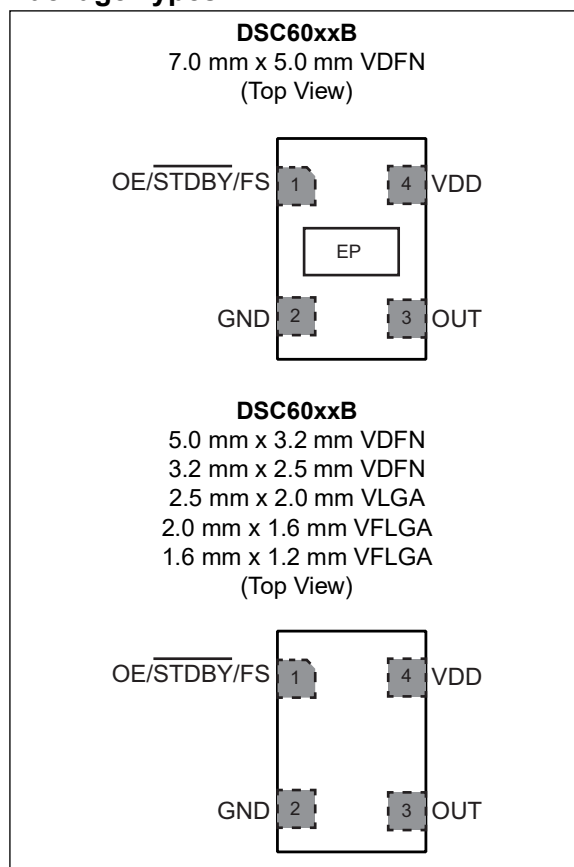
- Low Power/Portable Applications: IoT, Embedded/Smart Devices
- Consumer: Home Healthcare, Fitness Devices, Home Automation
- Automotive: Rear View/Surround View Cameras, Infotainment System (Please refer to DSA60xx Family)
- Industrial: Building/Factory Automation, Surveillance Camera

General Description

The DSC60xxB family of MEMS oscillators combines industry-leading low-power consumption, ultra-small packages with exceptional frequency stability, and jitter performance over temperature. The single-output DSC60xxB MEMS oscillators are excellent choices for use as clock references in small, battery-powered devices such as wearable and Internet of Things (IoT) devices in which small size, low power consumption, and long-term reliability are paramount. The Automotive Grade AEC-Q100 qualified option is available for this device.

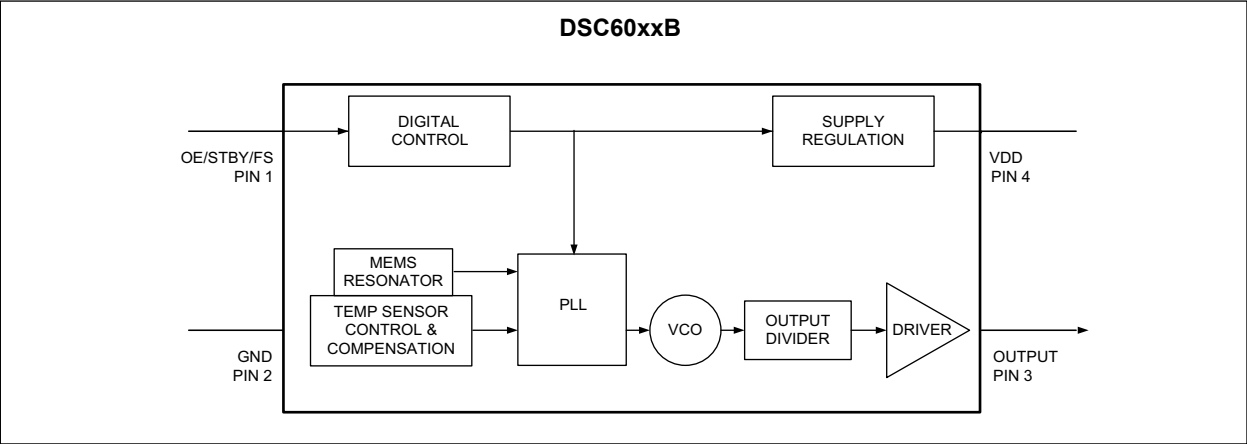
The DSC60xxB family is available in 1.6 mm $\times$ 1.2 mm and 2.0 mm $\times$ 1.6 mm VFLGA, 7.0 mm $\times$ 5.0 mm, 5.0 mm $\times$ 3.2 mm, & 3.2 mm $\times$ 2.5 mm VDFN, and 2.5 mm $\times$ 2.0 mm VLGA packages. These packages are “drop-in” replacements for standard 4-pin CMOS quartz crystal oscillators.

Package Types



DSC60XXB

Block Diagram



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings

Supply Voltage	–0.3V to +4.0V
Input Voltage (V_{IN})	–0.3V to $V_{DD}+0.3V$
ESD Protection	4 kV HBM, 400V MM, 2 kV CDM

ELECTRICAL CHARACTERISTICS

Electrical Characteristics: Unless otherwise indicated, $V_{DD} = 1.8V -5\%$ to $3.3V +10\%$, $T_A = -40^{\circ}C$ to $+125^{\circ}C$.						
Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions
Supply Voltage Note 1	V_{DD}	1.71	—	3.63	V	—
Active Supply Current	I_{DD}	—	1.3	—	mA	$F_{OUT} = 24\text{ MHz}$, $V_{DD} = 1.8V$, No Load
Power Supply Ramp	t_{PU}	0.1	—	100	ms	Note 9
Standby Supply Current Note 2	I_{STBY}	—	1.0	—	μA	$V_{DD} = 1.8/2.5V$
		—	1.5	—		$V_{DD} = 3.3V$
Frequency	f_0	0.002	—	80	MHz	—
Frequency Stability Note 3	Δf	—	—	± 20 ± 25 ± 50	ppm	All temp ranges
Aging	Δf	—	—	± 5	ppm	1st year @ $25^{\circ}C$
		—	—	± 1		Per year after first year
Startup Time	t_{SU}	—	—	1.5	ms	From 90% V_{DD} to valid clock output, $T = 25^{\circ}C$
Input Logic Levels Note 4	V_{IH}	$0.7 \times V_{DD}$	—	—	V	Input Logic High
	V_{IL}	—	—	$0.3 \times V_{DD}$	V	Input Logic Low
Output Disable Time Note 5	t_{DA}	—	—	200 + 2 Periods	ns	—
Output Enable Time Note 6	t_{EN}	—	—	1	μs	—
Enable Pull-Up Resistor Note 7	—	—	300	—	k Ω	If configured
Output Logic Levels, Low Drive	V_{OH}	$0.8 \times V_{DD}$	—	—	V	Output Logic High, $I = 1\text{ mA}$
	V_{OL}	—	—	$0.2 \times V_{DD}$	V	Output Logic Low, $I = -1\text{ mA}$

- Note 1:** Pin 4 V_{DD} should be filtered with 0.1 μF capacitor.
- 2:** Not including current through pull-up resistor on EN pin (if configured).
- 3:** Includes frequency variations due to initial tolerance, temp. and power supply voltage.
- 4:** Input waveform must be monotonic with rise/fall time < 10 ms
- 5:** Output Disable time takes up to two periods of the output waveform + 200 ns.
- 6:** For parts configured with OE, not Standby.
- 7:** Output is enabled if pad is floated or not connected.
- 8:** Output Duty Cycle will be 40% to 60% when output frequency is between 40 MHz to 60 MHz.
- 9:** Time to reach 90% of target V_{DD} . Power ramp rise must be monotonic.
- 10:** Peak-to-peak period jitter is measured over 10,000 cycles.

DSC60XXB

ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Characteristics: Unless otherwise indicated, $V_{DD} = 1.8V -5\%$ to $3.3V +10\%$, $T_A = -40^{\circ}C$ to $+125^{\circ}C$.							
Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions	
Output Transition Time Rise Time/Fall Time	t_{RX}/t_{FX}	—	2.5	3.5	ns	DSC60x3B Low Drive, 20% to 80% $C_L = 5\text{ pF}$	$V_{DD} = 1.8V$
		—	1.5	2.2			$V_{DD} = 2.5V/3.3V$
	t_{RY}/t_{FY}	—	1.2	2.0	ns	DSC60x1B Std. Drive, 20% to 80% $C_L = 10\text{ pF}$	$V_{DD} = 1.8V$
		—	0.6	1.2			$V_{DD} = 2.5V/3.3V$
Output Duty Cycle Note 8	SYM	45	—	55	%	—	
Period Jitter, RMS	J_{PER}	—	39	—	ps	DSC60x3B Low Drive, $F_{OUT} = 27\text{ MHz}$ $C_L = 5\text{ pF}$	$V_{DD} = 1.8V$
		—	39	—			$V_{DD} = 2.5V/3.3V$
		—	37	—		DSC60x1B Std. Drive, $F_{OUT} = 27\text{ MHz}$ $C_L = 10\text{ pF}$	$V_{DD} = 1.8V$
		—	37	—			$V_{DD} = 2.5V/3.3V$
Cycle-to-Cycle Jitter, Peak	J_{Cy-Cy}	—	190	—	ps	DSC60x3B Low Drive, $F_{OUT} = 27\text{ MHz}$ $C_L = 5\text{ pF}$	$V_{DD} = 1.8V$
		—	120	—			$V_{DD} = 2.5V/3.3V$
		—	110	—		DSC60x1B Std. Drive, $F_{OUT} = 27\text{ MHz}$ $C_L = 10\text{ pF}$	$V_{DD} = 1.8V$
		—	100	—			$V_{DD} = 2.5V/3.3V$

- Note 1:** Pin 4 V_{DD} should be filtered with 0.1 μF capacitor.
- 2:** Not including current through pull-up resistor on EN pin (if configured).
- 3:** Includes frequency variations due to initial tolerance, temp. and power supply voltage.
- 4:** Input waveform must be monotonic with rise/fall time $< 10\text{ ms}$
- 5:** Output Disable time takes up to two periods of the output waveform + 200 ns.
- 6:** For parts configured with OE, not Standby.
- 7:** Output is enabled if pad is floated or not connected.
- 8:** Output Duty Cycle will be 40% to 60% when output frequency is between 40 MHz to 60 MHz.
- 9:** Time to reach 90% of target V_{DD} . Power ramp rise must be monotonic.
- 10:** Peak-to-peak period jitter is measured over 10,000 cycles.

ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Characteristics: Unless otherwise indicated, $V_{DD} = 1.8V -5\%$ to $3.3V +10\%$, $T_A = -40^{\circ}C$ to $+125^{\circ}C$.							
Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions	
Period Jitter, Peak-to-Peak, Note 10	$J_{PERPK-PK}$	—	280	—	ps	DSC60x3B Low Drive, $F_{OUT} = 27\text{ MHz}$ $C_L = 5\text{ pF}$	$V_{DD} = 1.8V$
		—	280	—			$V_{DD} = 2.5V/3.3V$
		—	280	—		DSC60x1B Std. Drive, $F_{OUT} = 27\text{ MHz}$ $C_L = 10\text{ pF}$	$V_{DD} = 1.8V$
		—	280	—			$V_{DD} = 2.5V/3.3V$

- Note 1:** Pin 4 V_{DD} should be filtered with 0.1 μF capacitor.
- 2:** Not including current through pull-up resistor on EN pin (if configured).
- 3:** Includes frequency variations due to initial tolerance, temp. and power supply voltage.
- 4:** Input waveform must be monotonic with rise/fall time < 10 ms
- 5:** Output Disable time takes up to two periods of the output waveform + 200 ns.
- 6:** For parts configured with OE, not Standby.
- 7:** Output is enabled if pad is floated or not connected.
- 8:** Output Duty Cycle will be 40% to 60% when output frequency is between 40 MHz to 60 MHz.
- 9:** Time to reach 90% of target V_{DD} . Power ramp rise must be monotonic.
- 10:** Peak-to-peak period jitter is measured over 10,000 cycles.

DSC60XXB

TEMPERATURE SPECIFICATIONS (Note 1)

Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions
Temperature Ranges						
Maximum Junction Temperature	T_J	—	—	+150	°C	—
Storage Ambient Temperature Range	T_S	–55	—	+150	°C	—
Soldering Temperature	—	—	+260	—	°C	40 sec. max.

Note 1: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction to air (i.e., T_A , T_J , θ_{JA}). Exceeding the maximum allowable power dissipation will cause the device operating junction temperature to exceed the maximum +125°C rating. Sustained junction temperatures above +125°C can impact the device reliability.

2.0 PIN DESCRIPTIONS

The DSC60xxB is a highly configurable device and can be factory programmed in many different ways to meet the customer's needs. Microchip's ClockWorks® Configurator <http://clockworks.microchip.com/Timing/> must be used to choose the necessary options, create the final part number, data sheet, and order samples. The descriptions of the pins are listed in Table 2-1.

TABLE 2-1: DSC60XXB PIN FUNCTION TABLE

Pin Number	Pin Name	Description
1	OE	Output Enable: H = Active, L = Disabled (High Impedance).
	$\overline{\text{STDBY}}$	Standby: H = Device is active, L = Device is in standby (Low Power Mode).
	FS	Frequency Select: H = Output Frequency 1, L = Output Frequency 2.
2	GND	Ground.
3	OUTPUT	Oscillator clock output
4	VDD	Power Supply: 1.71V to 3.63V.

An explanation of the different options listed in Table 2-1 follows.

2.1 Pin 1

This is a control pin and may be configured to fulfill one of six different functions. If not actively driven, a 10 k Ω pull-up resistor is recommended.

2.1.1 OUTPUT ENABLE (OE)

Pin 1 may be configured as OE. Oscillator output may be turned on and off according to the state of this pin.

2.1.2 $\overline{\text{STDBY}}$

Pin 1 may be configured as Standby. When the pin is low, both output buffer and PLL will be off and the device will enter a low power mode.

2.1.3 FREQUENCY SELECT (FS)

Pin 1 may be configured as FS. The output may be set to one of two pre-programmed frequencies. The output clock frequencies can only be set to either kHz or MHz. A combination of kHz and MHz cannot be set.

2.2 Pins 2 through 4

Pins 2 and 4 are the supply terminals, GND and VDD respectively. Pin 3 is the clock output, programmable to Standard and Low Drive strength settings. Visit ClockWorks® Configurator to customize your device.

DSC60XXB

3.0 DIAGRAMS

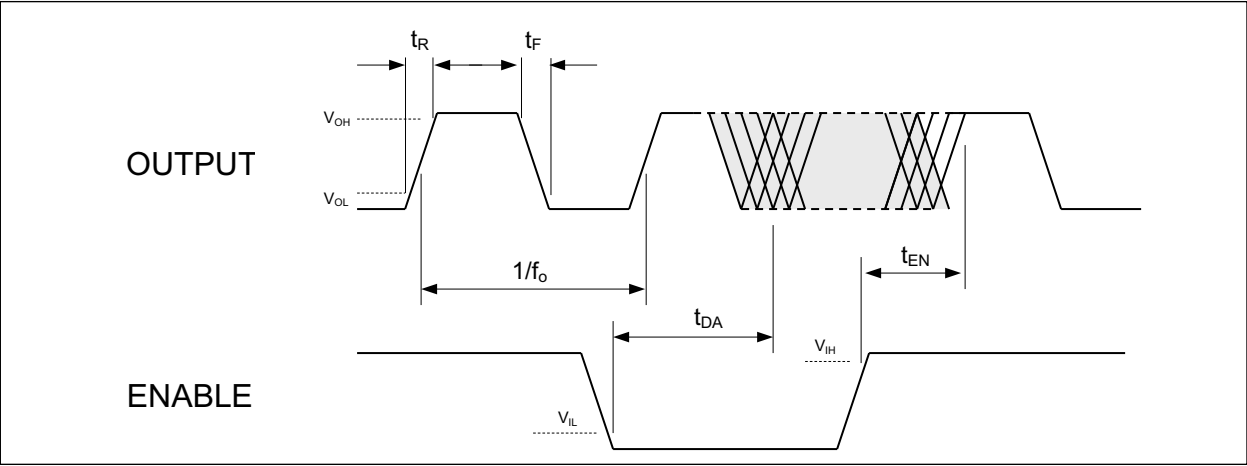


FIGURE 3-1: Output Waveform.

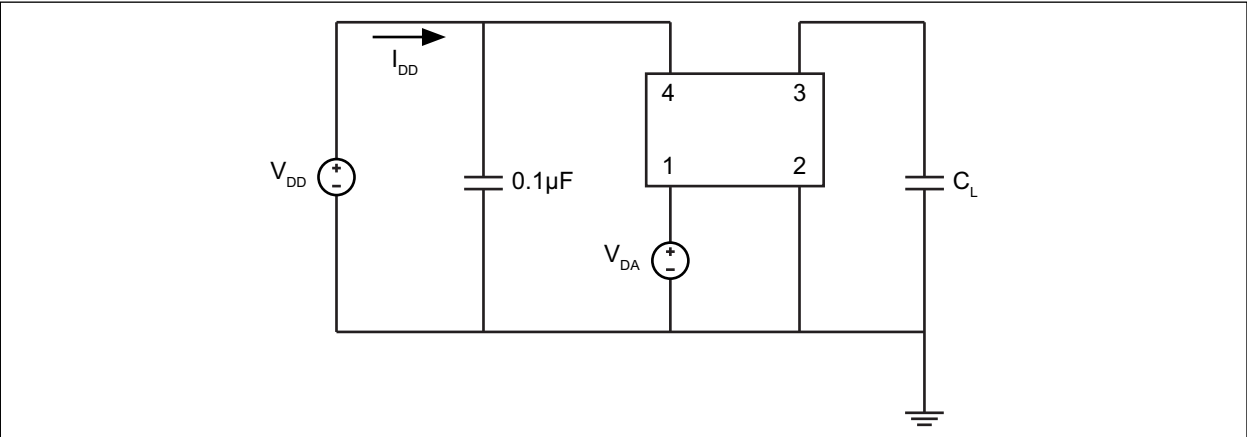


FIGURE 3-2: Test Circuit.

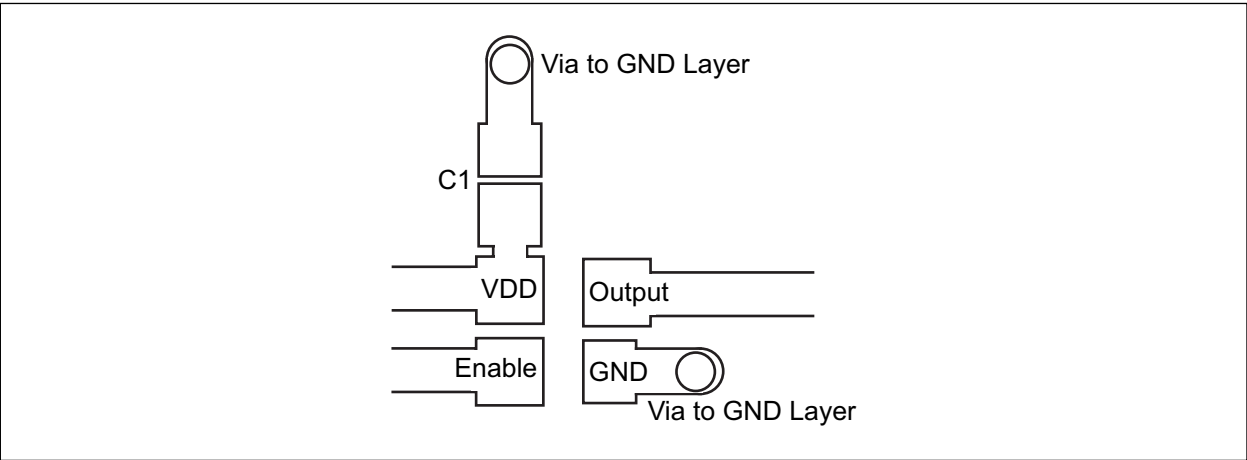


FIGURE 3-3: Recommended Board Layout.

4.0 SOLDER REFLOW PROFILE

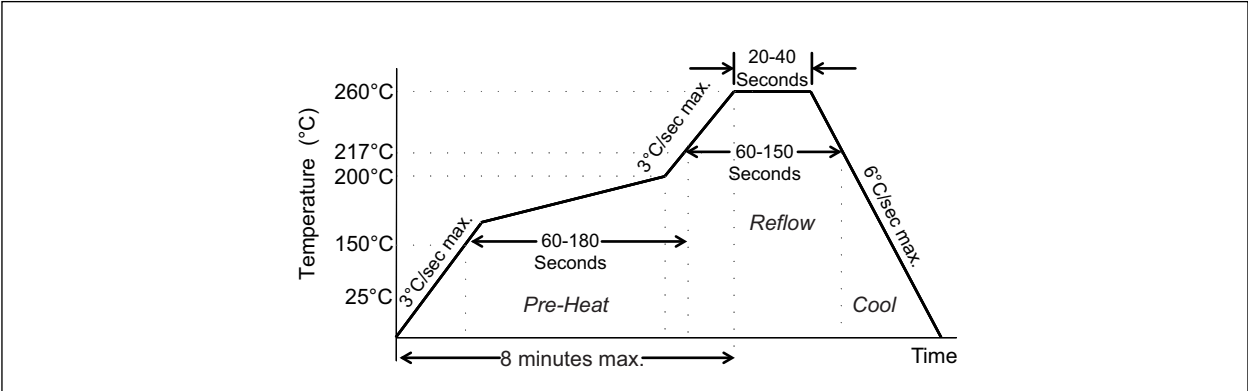


FIGURE 4-1: Solder Reflow Profile.

MSL 1 @ 260°C refer to JSTD-020C	
Ramp-Up Rate (200°C to Peak Temp)	3°C/sec. max.
Preheat Time 150°C to 200°C	60 to 180 sec.
Time maintained above 217°C	60 to 150 sec.
Peak Temperature	255°C to 260°C
Time within 5°C of actual Peak	20 to 40 sec.
Ramp-Down Rate	6°C/sec. max.
Time 25°C to Peak Temperature	8 minutes max.

5.0 PACKAGING INFORMATION

5.1 Package Marking Information

4-Lead

7.0mm x 5.0mm VDFN*

5.0mm x 3.2mm VDFN*

3.2mm x 2.5mm VDFN*

2.5mm x 2.0mm VLGA*

Example

XXXXXXX
XXXYYWW
0SSS

0400000
DAP1834
0287

4-Lead VFLGA*

2.0mm x 1.6mm

1.6mm x 1.2mm

Example

XXXX
SSS

011H
502

Legend:

XX...X

Y

YY

WW

SSS

(e3)

*

Product code or customer-specific information

Year code (last digit of calendar year)

Year code (last 2 digits of calendar year)

Week code (week of January 1 is week '01')

Alphanumeric traceability code

Pb-free JEDEC® designator for Matte Tin (Sn)

This package is Pb-free. The Pb-free JEDEC designator ((e3)) can be found on the outer packaging for this package.

•, ▲, ▼

Pin one index is identified by a dot, delta up, or delta down (triangle mark).

Note:

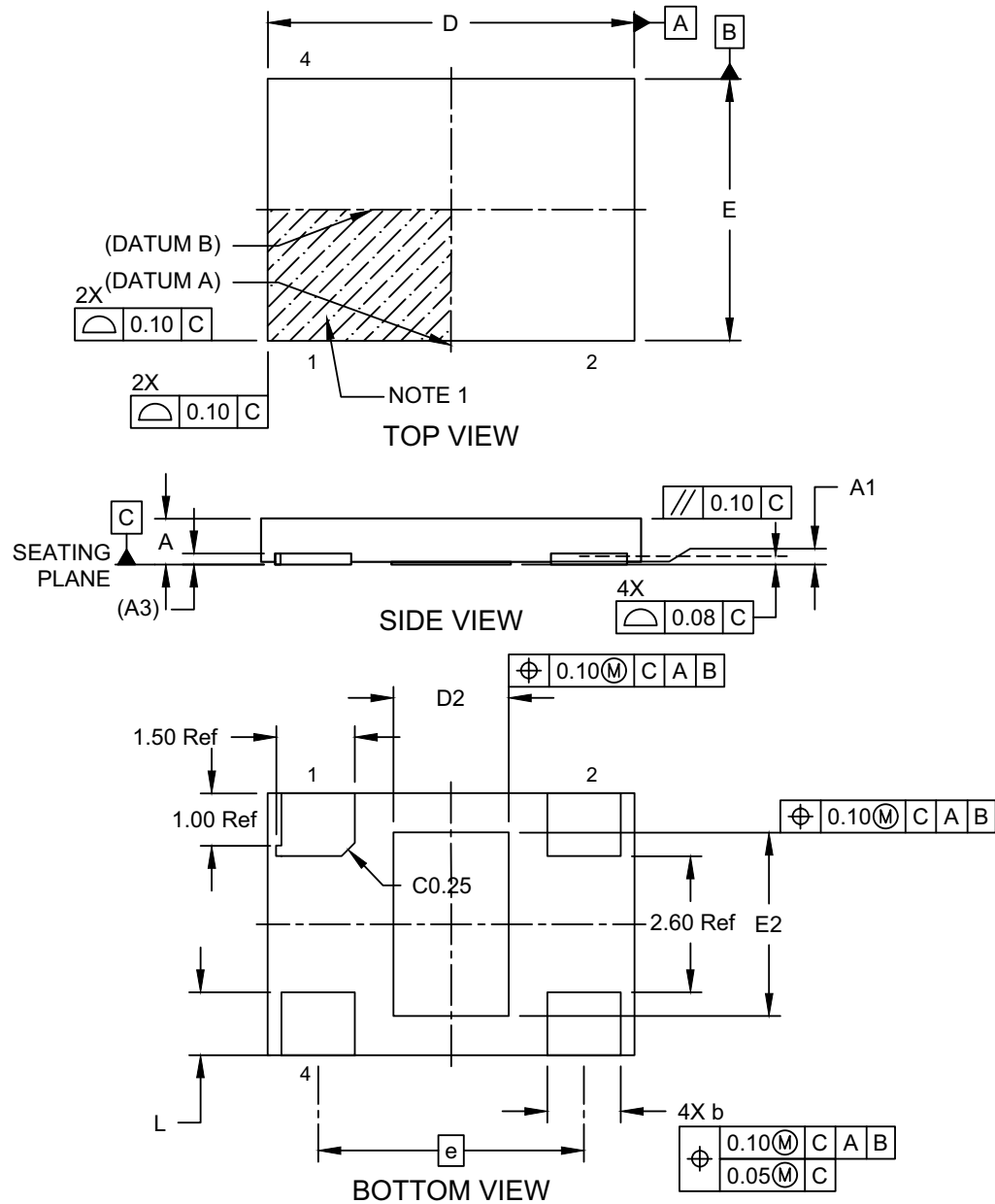
In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo.

Underbar (_) and/or Overbar (¯) symbol may not be to scale.

Note: Devices may not include a top-line frequency marking. Product identity and output frequency are defined by the ordering code and accompanying documentation. Devices without a frequency marking remain fully compliant with all applicable datasheet specifications.

4-Lead Very Thin Dual Flatpack, No Lead Package (JZA) - 7x5x0.9 mm Body [VDFN] With 2.2x3.5 mm Exposed Pad

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



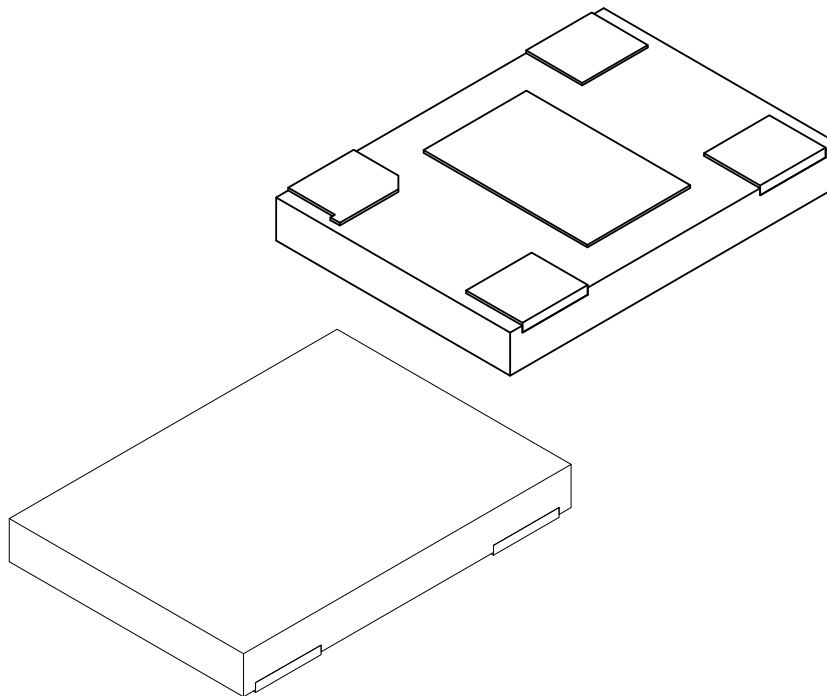
Microchip Technology Drawing C04-1025-JZA Rev B Sheet 1 of 2

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DSC60XXB

4-Lead Very Thin Dual Flatpack, No Lead Package (JZA) - 7x5x0.9 mm Body [VDFN] With 2.2x3.5 mm Exposed Pad

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



		Units	MILLIMETERS		
Dimension Limits			MIN	NOM	MAX
Number of Terminals	N		004		
Pitch	e		5.08 Ref		
Overall Height	A		0.80	0.85	0.90
Standoff	A1		0.00	-	0.05
Terminal Thickness	A3		0.203 Ref		
Overall Length	D		6.90	7.00	7.10
Exposed Pad Length	D2		2.10	2.20	2.30
Overall Width	E		4.90	5.00	5.10
Exposed Pad Width	E2		3.40	3.50	3.60
Terminal Width	b		1.35	1.40	1.45
Terminal Length	L		1.10	1.20	1.30

Notes:

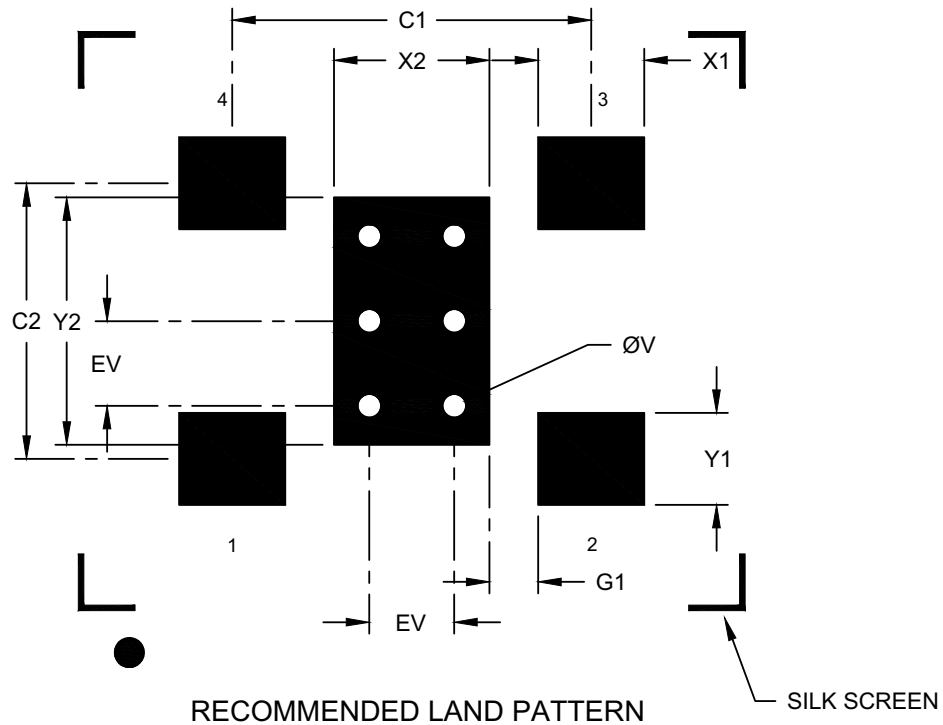
- Pin 1 visual index feature may vary, but must be located within the pin 1 area.
- Package is saw singulated
- Dimensioning and tolerancing per ASME Y14.5M
 - BSC: Basic Dimension. Theoretically exact value shown without tolerances.
 - REF: Reference Dimension, usually without tolerance, for information purposes only.

Microchip Technology Drawing C04-1025-JZA Rev B Sheet 2 of 2

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4-Lead Very Thin Dual Flatpack, No Lead Package [JZA] - 7x5x0.9 mm Body [VDFN] With 2.2x3.5 mm Exposed Pad

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Optional Center Pad Width	X2			2.30
Optional Center Pad Length	Y2			3.60
Contact Pad Spacing	C1		5.08	
Contact Pad Spacing	C2		3.90	
Contact Pad Width (Xnn)	X1			1.50
Contact Pad Length (Xnn)	Y1			1.30
Contact Pad to Center Pad (Xnn)	G1	0.69		
Thermal Via Diameter	V		0.33	
Thermal Via Pitch	EV		1.20	

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M
BSC: Basic Dimension. Theoretically exact value shown without tolerances.
2. For best soldering results, thermal vias, if used, should be filled or tented to avoid solder loss during reflow process

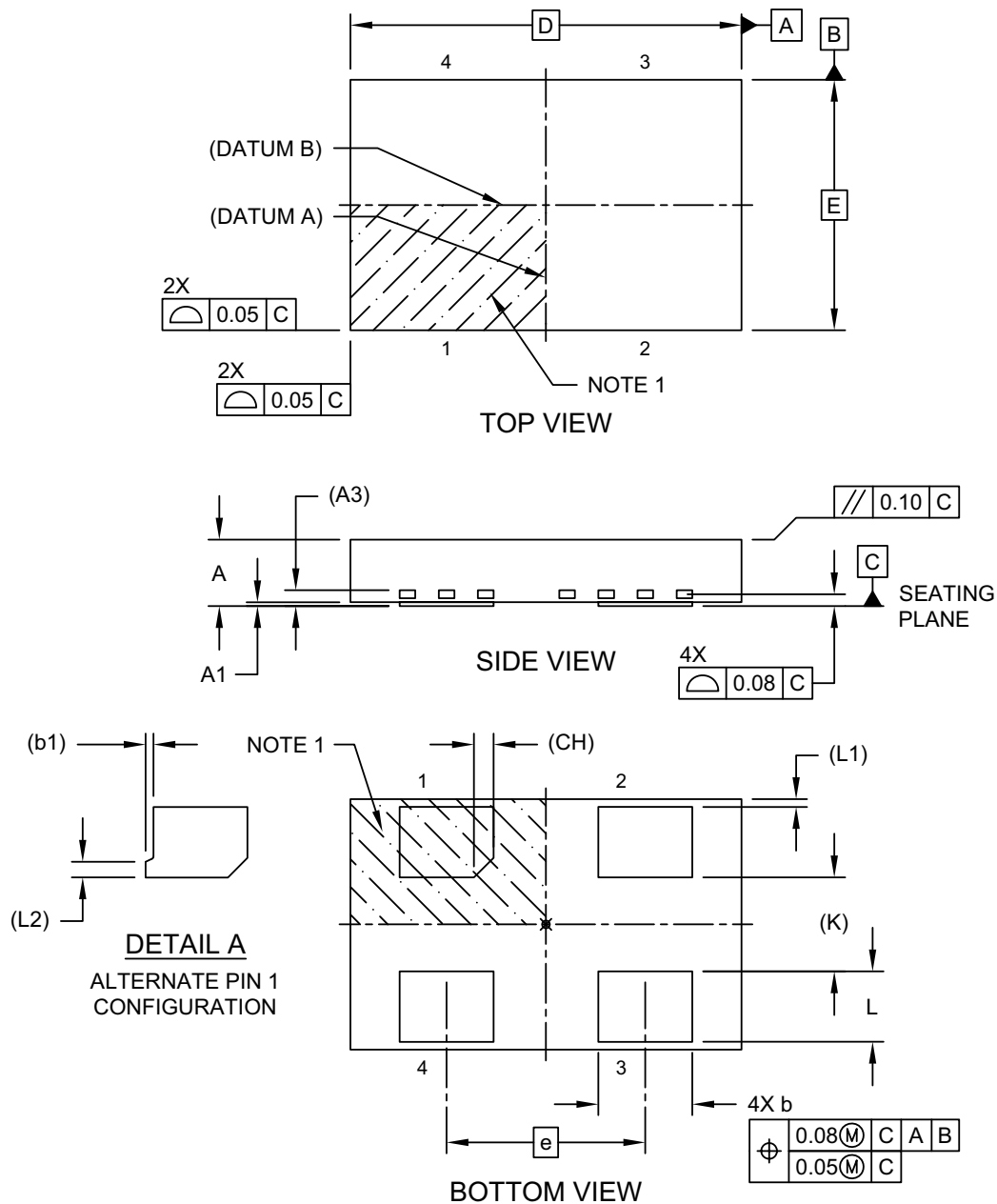
Microchip Technology Drawing C04-3025-JZA Rev B

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DSC60XXB

4-Lead Very Thin Plastic Dual Flat, No Lead Package (H6A) - 5x3.2 mm Body [VDFN]

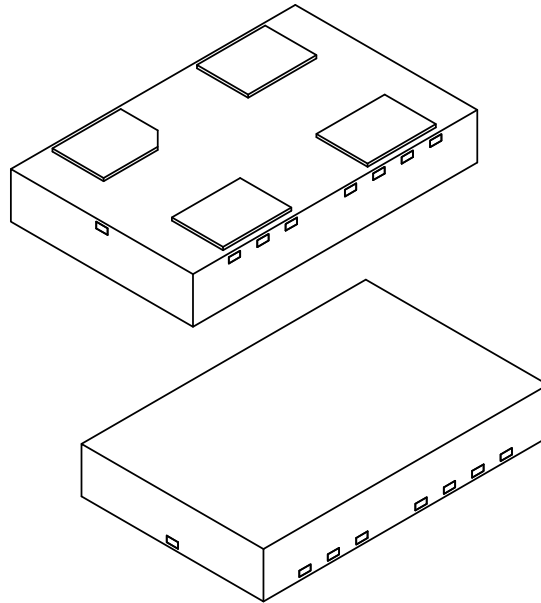
Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Microchip Technology Drawing C04-1008-H6A Rev C Sheet 1 of 2

4-Lead Very Thin Plastic Dual Flat, No Lead Package (H6A) - 5x3.2 mm Body [VDFN]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Number of Terminals	N	4		
Pitch	e	2.54 BSC		
Overall Height	A	0.80	0.85	0.90
Standoff	A1	0.00	0.02	0.05
Terminal Thickness	A3	0.20 REF		
Overall Length	D	5.00 BSC		
Overall Width	E	3.20 BSC		
Terminal Width	b	1.15	1.20	1.25
Terminal 1 Tab	b1	0.10 REF		
Terminal Length	L	0.80	0.90	1.00
Terminal Pull Back	L1	0.10 REF		
Terminal 1 Tab	L2	0.20 REF		
Terminal 1 Chamfer	CH	0.25 REF		
Terminal Spacing	K	1.20 REF		

Notes:

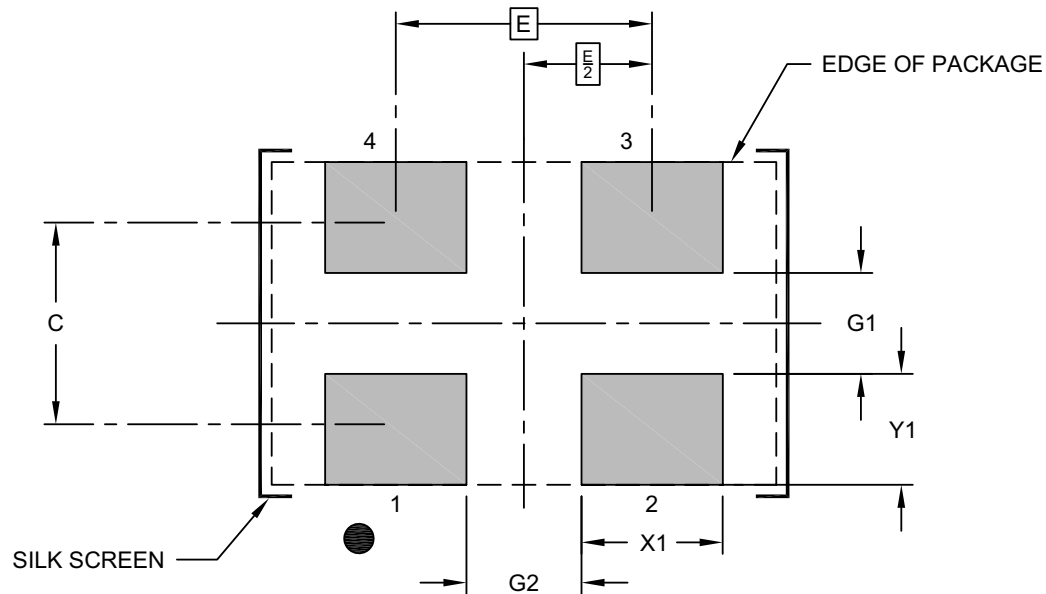
- Pin 1 visual index feature may vary, but must be located within the hatched area.
- Package is saw singulated
- Dimensioning and tolerancing per ASME Y14.5M
BSC: Basic Dimension. Theoretically exact value shown without tolerances.
REF: Reference Dimension, usually without tolerance, for information purposes only.

Microchip Technology Drawing C04-1008-H6A Rev C Sheet 2 of 2

DSC60XXB

4-Lead Very Thin Plastic Dual Flat, No Lead Package (H6A) - 5x3.2 mm Body [VDFN]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



RECOMMENDED LAND PATTERN

Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Contact Pitch	E		2.54	
Contact Pad Spacing	C		2.00	
Contact Pad Width (X4)	X1			1.40
Contact Pad Length (X4)	Y1			
Contact Pad to Center Pad (X2)	G1	1.00		1.10
Contact Pad to Contact Pad (X2)	G2	1.14		
Terminal 1 Contact Pad Chamfer	CH		0.30	

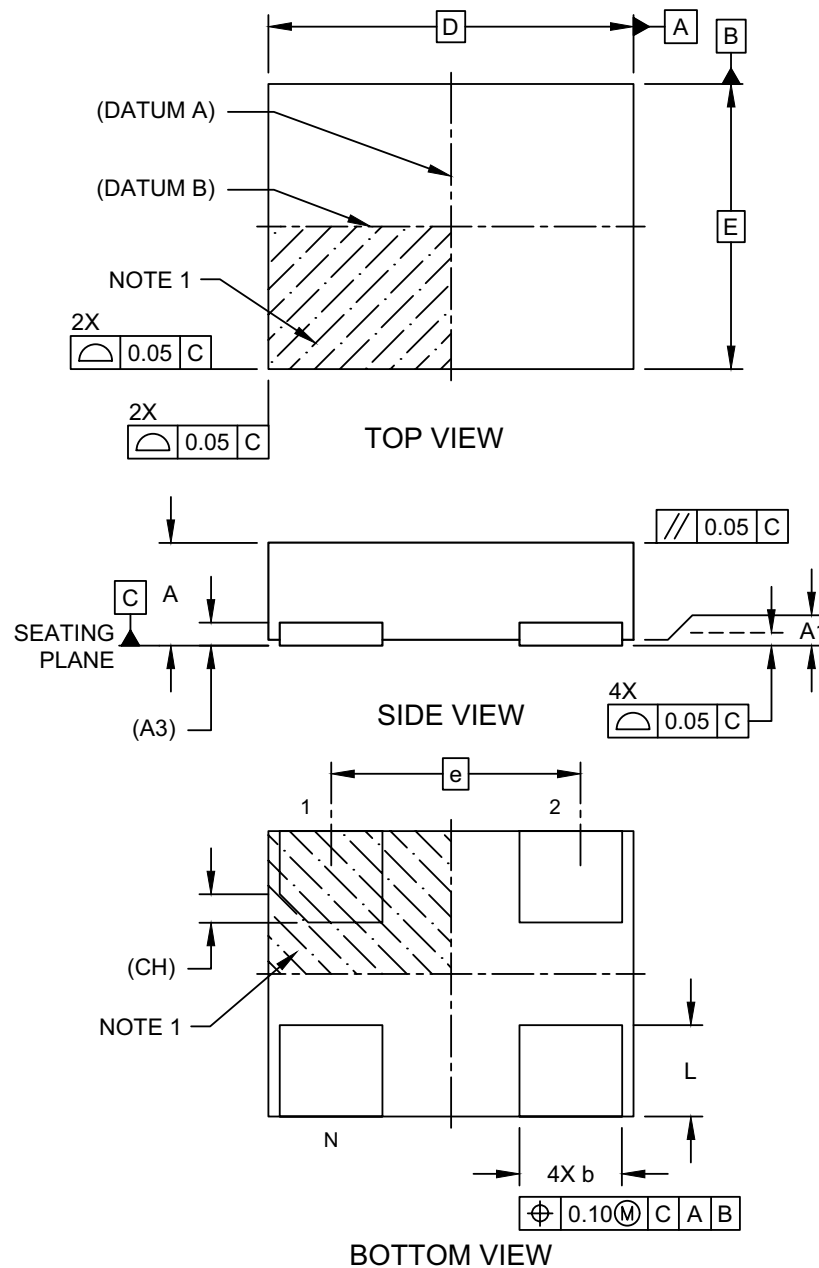
Notes:

1. Dimensioning and tolerancing per ASME Y14.5M
BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-3008 Rev C

4-Lead Very Thin Plastic Dual Flatpack No-Lead (H4A) - 3.2x2.5 mm Body [VDFN]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



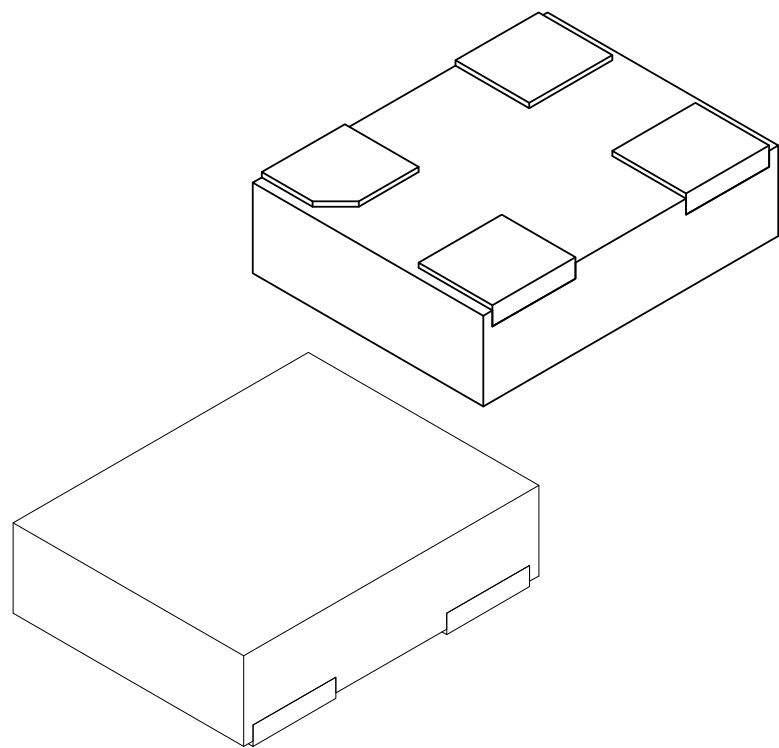
Microchip Technology Drawing C04-1006-H4A Rev C Sheet 1 of 2

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DSC60XXB

4-Lead Very Thin Plastic Dual Flatpack No-Lead (H4A) - 3.2x2.5 mm Body [VDFN]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Units		MILLIMETERS		
Dimension Limits		MIN	NOM	MAX
Number of Terminals	N	4		
Pitch	e	2.10 BSC		
Overall Height	A	0.80	0.85	0.90
Standoff	A1	0.00	0.02	0.05
Overall Length	D	3.20 BSC		
Overall Width	E	2.50 BSC		
Terminal Width	b	0.85	0.90	0.95
Terminal Length	L	0.70	0.80	0.90
Terminal 1 Index Chamfer	CH	0.25 REF		

Notes:

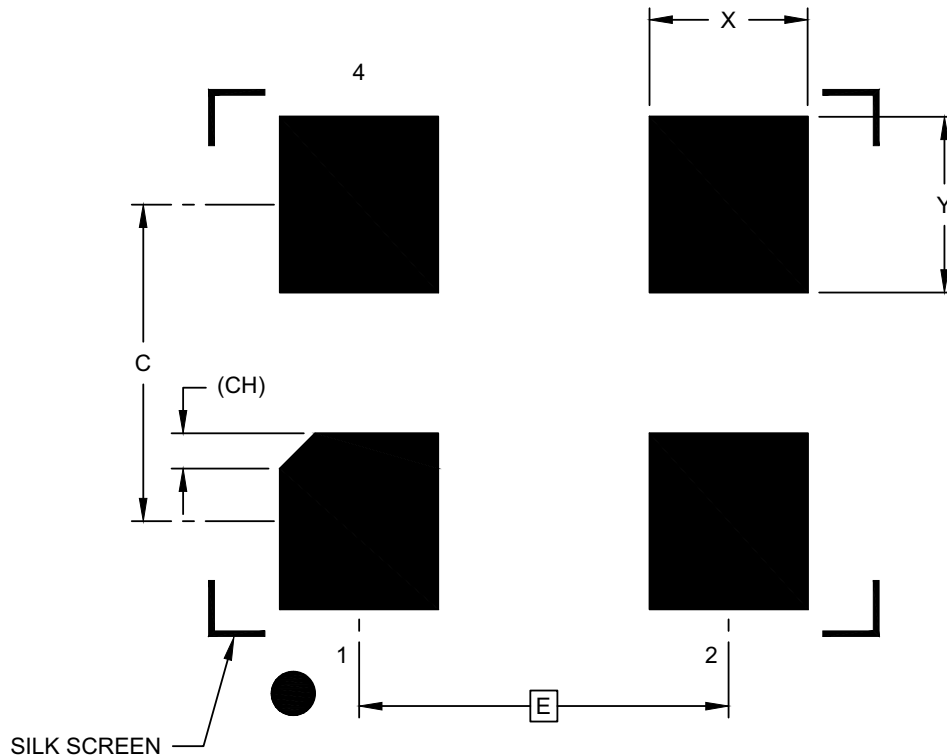
- 1. Pin 1 visual index feature may vary, but must be located within the hatched area.
- 2. Package is saw singulated
- 3. Dimensioning and tolerancing per ASME Y14.5M
 - BSC: Basic Dimension. Theoretically exact value shown without tolerances.
 - REF: Reference Dimension, usually without tolerance, for information purposes only.

Microchip Technology Drawing C04-1006-H4A Rev C Sheet 2 of 2

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4-Lead Very Thin Plastic Dual Flatpack No-Lead (H4A) - 3.2x2.5 mm Body [VDFN]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



RECOMMENDED LAND PATTERN

Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Contact Pitch	E	2.10 BSC		
Contact Pad Spacing	C		1.80	
Contact Pad Width (X4)	X			0.90
Contact Pad Length (X4)	Y			1.00
Contact 1 Index Chamfer	CH	0.20 REF		

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

REF: Reference Dimension, usually without tolerance, for information purposes only.

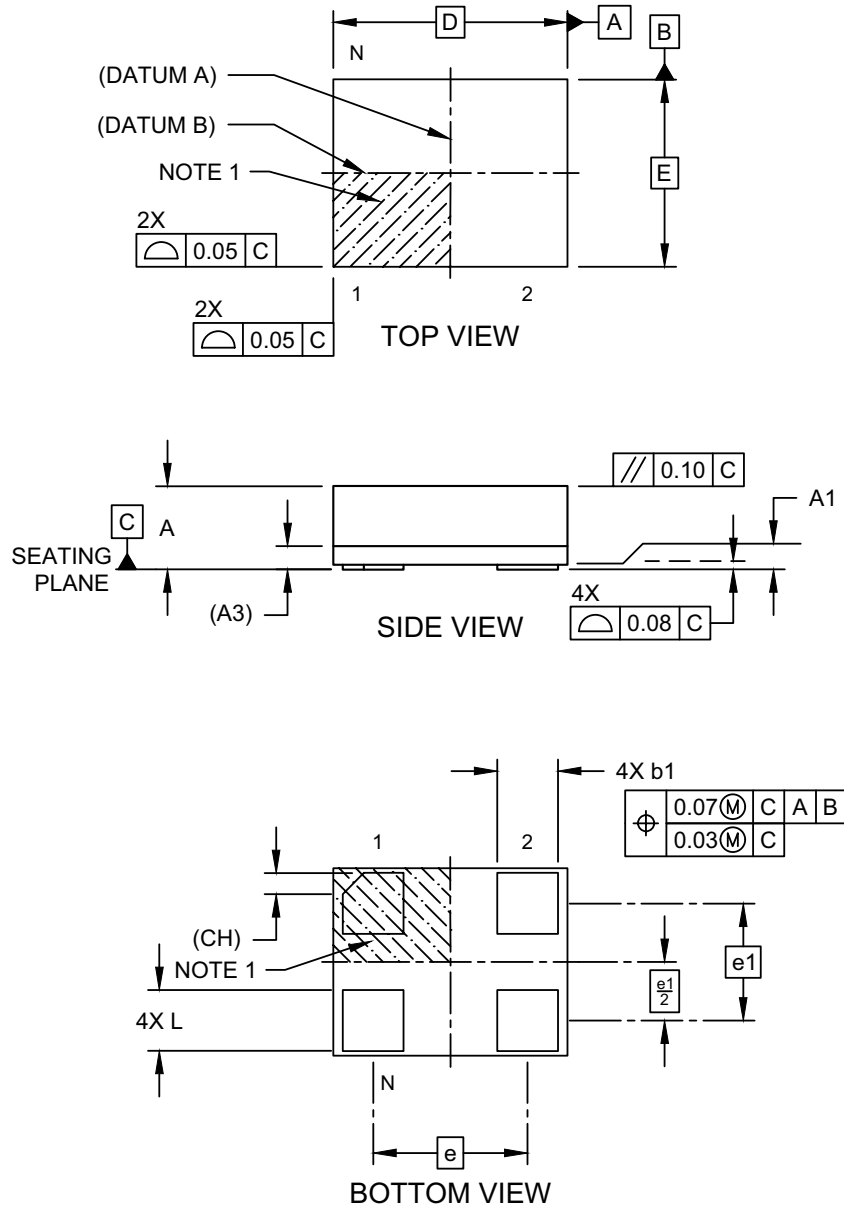
Microchip Technology Drawing C04-3006-H4A Rev C

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DSC60XXB

4-Lead Very Thin Land Grid Array (AUA) - 2.5x2.0 mm Body [VLGA]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>

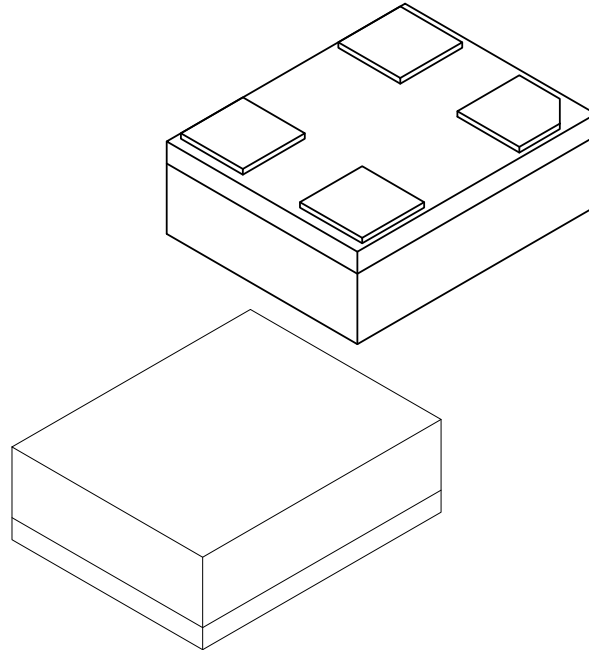


Microchip Technology Drawing C04-1202-AUA Rev C Sheet 1 of 2

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4-Lead Very Thin Land Grid Array (AUA) - 2.5x2.0 mm Body [VLGA]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Units		MILLIMETERS		
Dimension Limits		MIN	NOM	MAX
Number of Terminals	N	4		
Terminal Pitch	e	1.65 BSC		
Terminal Pitch	e1	1.25 BSC		
Overall Height	A	0.79	0.84	0.89
Standoff	A1	0.00	0.02	0.05
Substrate Thickness (with Terminals)	A3	0.20 REF		
Overall Length	D	2.50 BSC		
Overall Width	E	2.00 BSC		
Terminal Width	b1	0.60	0.65	0.70
Terminal Length	L	0.60	0.65	0.70
Terminal 1 Index Chamfer	CH	-	0.225	-

Notes:

- Pin 1 visual index feature may vary, but must be located within the hatched area.
- Package is saw singulated
- Dimensioning and tolerancing per ASME Y14.5M
 - BSC: Basic Dimension. Theoretically exact value shown without tolerances.
 - REF: Reference Dimension, usually without tolerance, for information purposes only.

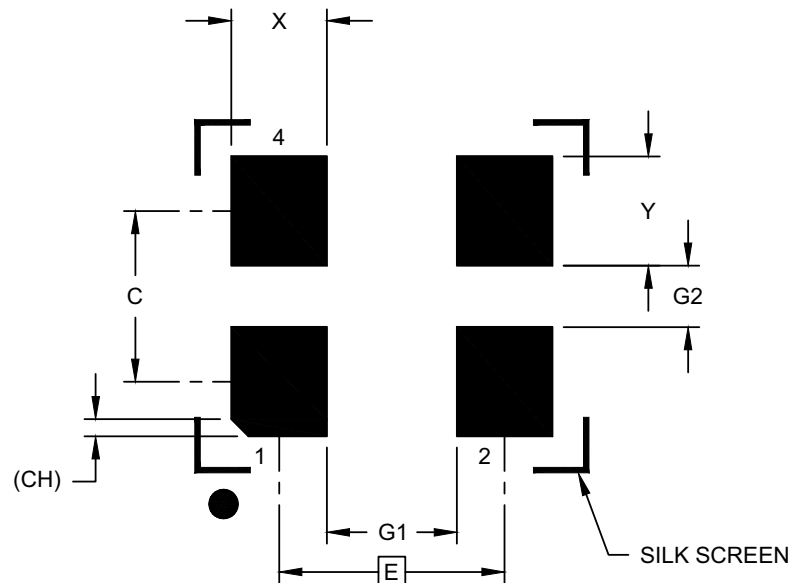
Microchip Technology Drawing C04-1202-AUA Rev C Sheet 2 of 2

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DSC60XXB

4-Lead Very Thin Land Grid Array (AUA) - 2.5x2.0 mm Body [VLGA]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



RECOMMENDED LAND PATTERN

Dimension Limits	Units	MILLIMETERS		
		MIN	NOM	MAX
Contact Pitch	E	1.65 BSC		
Contact Spacing	C		1.25	
Contact Width (X4)	X			0.70
Contact Pad Length (X4)	Y			0.80
Space Between Contacts (X2)	G1	0.95		
Space Between Contacts (X2)	G2	0.45		
Contact 1 Index Chamfer	CH	0.13 X 45° REF		

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

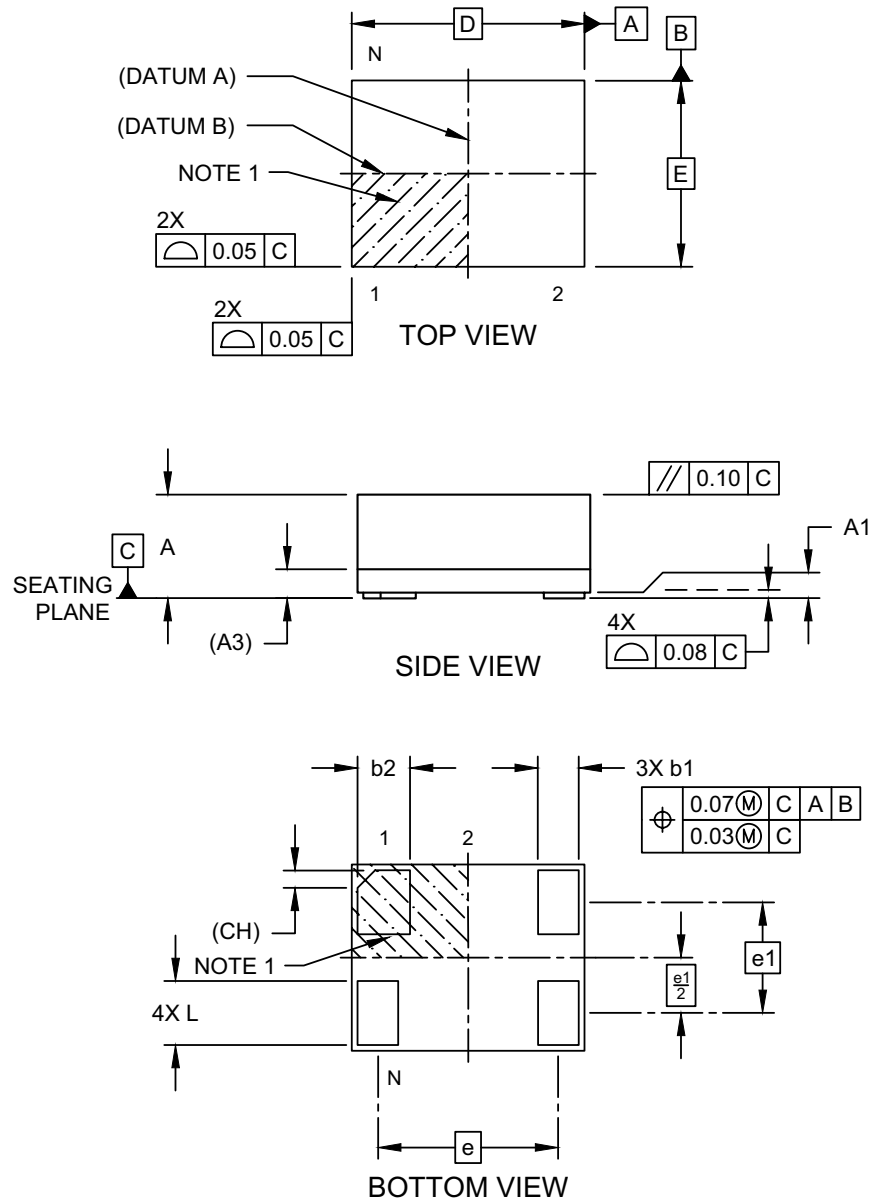
BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-3202-AUA Rev C

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4-Lead Very Thin Fine Pitch Land Grid Array (ASA) - 2.0x1.6 mm Body [VFLGA]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



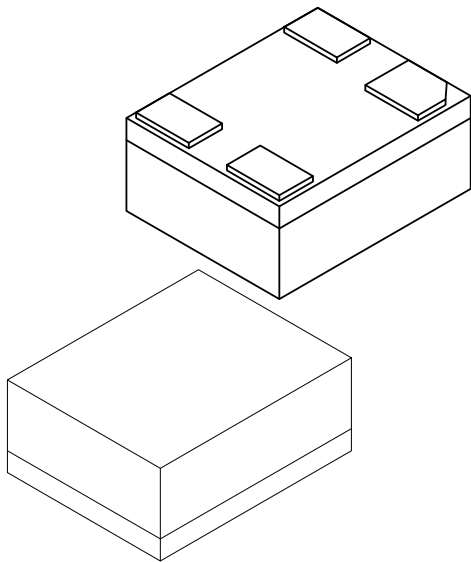
Microchip Technology Drawing C04-1200-ASA Rev E Sheet 1 of 2

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DSC60XXB

4-Lead Very Thin Fine Pitch Land Grid Array (ASA) - 2.0x1.6 mm Body [VFLGA]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Units		MILLIMETERS		
Dimension Limits		MIN	NOM	MAX
Number of Terminals	N	4		
Terminal Pitch	e	1.55 BSC		
Terminal Pitch	e1	0.95 BSC		
Overall Height	A	0.79	0.84	0.89
Standoff	A1	0.00	0.02	0.05
Substrate Thickness (with Terminals)	A3	0.20 REF		
Overall Length	D	2.00 BSC		
Overall Width	E	1.60 BSC		
Terminal Width	b1	0.30	0.35	0.40
Terminal Width	b2	0.40	0.45	0.50
Terminal Length	L	0.50	0.55	0.60
Terminal 1 Index Chamfer	CH	-	0.15	-

Notes:

- 1. Pin 1 visual index feature may vary, but must be located within the hatched area.
- 2. Package is saw singulated
- 3. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

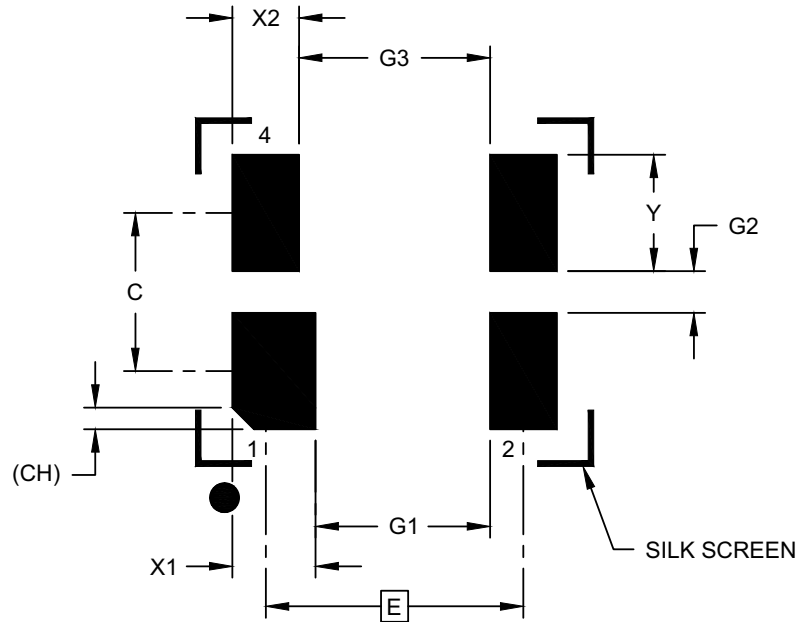
REF: Reference Dimension, usually without tolerance, for information purposes only.

Microchip Technology Drawing C04-1200-ASA Rev E Sheet 2 of 2

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4-Lead Very Thin Fine Pitch Land Grid Array (ASA) - 2.0x1.6 mm Body [VFLGA]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



RECOMMENDED LAND PATTERN

Units		MILLIMETERS		
Dimension Limits		MIN	NOM	MAX
Contact Pitch	E	1.55 BSC		
Contact Spacing	C		0.95	
Contact Width (X1)	X1			0.50
Contact Width (X3)	X2			0.40
Contact Pad Length (X4)	Y			0.70
Space Between Contacts	G1	1.05		
Space Between Contacts	G2	0.25		
Space Between Contacts	G3	1.15		
Contact 1 Index Chamfer	CH	0.13 X 45° REF		

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

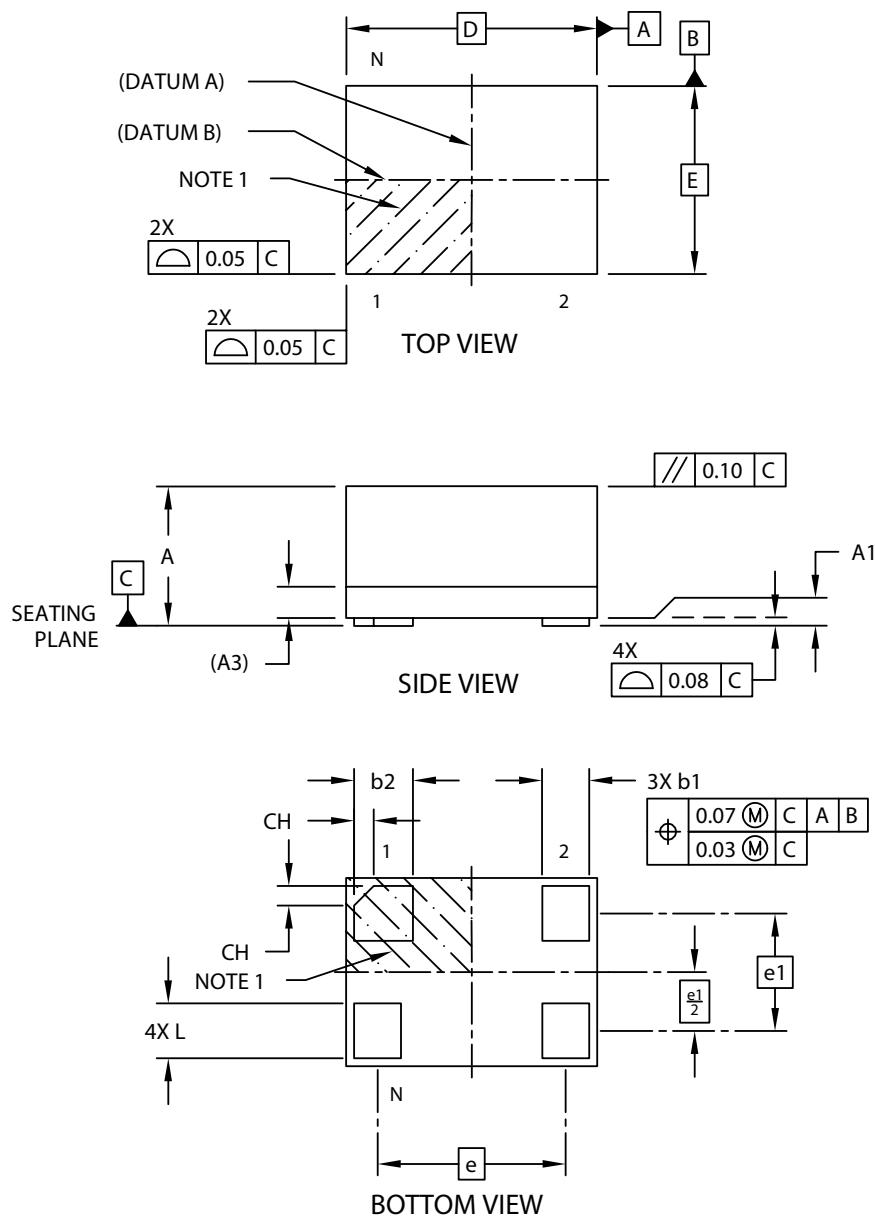
Microchip Technology Drawing C04-3200-ASA Rev E

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DSC60XXB

4-Lead Very Thin Fine Pitch Land Grid Array (ARA) - 1.6x1.2 mm Body [VFLGA]

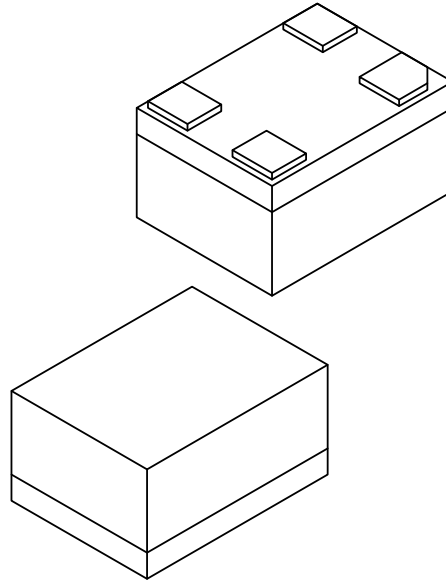
Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Microchip Technology Drawing C04-1199B Sheet 1 of 2

4-Lead Very Thin Fine Pitch Land Grid Array (ARA) - 1.6x1.2 mm Body [VFLGA]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Units		MILLIMETERS		
Dimension Limits		MIN	NOM	MAX
Number of Terminals	N	4		
Terminal Pitch	e	1.20 BSC		
Terminal Pitch	e1	0.75 BSC		
Overall Height	A	0.79	0.84	0.89
Standoff	A1	0.00	0.02	0.05
Substrate Thickness (with Terminals)	A3	0.20 REF		
Overall Length	D	1.60 BSC		
Overall Width	E	1.20 BSC		
Terminal Width	b1	0.25	0.30	0.35
Terminal Width	b2	0.325	0.375	0.425
Terminal Length	L	0.30	0.35	0.40
Terminal 1 Index Chamfer	CH	-	0.125	-

Notes:

1. Pin 1 visual index feature may vary, but must be located within the hatched area.
2. Package is saw singulated
3. Dimensioning and tolerancing per ASME Y14.5M

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

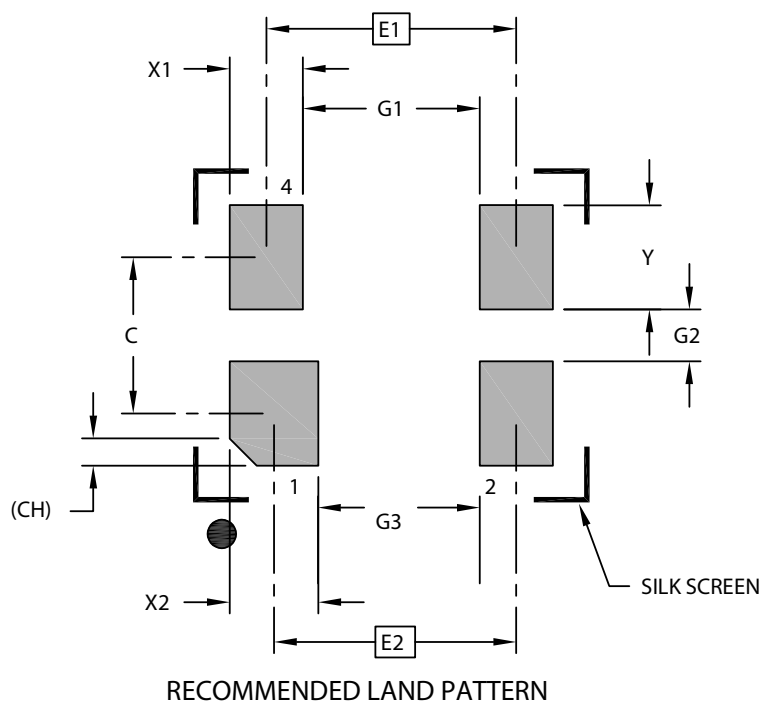
REF: Reference Dimension, usually without tolerance, for information purposes only.

Microchip Technology Drawing C04-1199B Sheet 2 of 2

DSC60XXB

4-Lead Very Thin Fine Pitch Land Grid Array (ARA) - 1.6x1.2 mm Body [VFLGA]

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>



Units		MILLIMETERS		
Dimension Limits		MIN	NOM	MAX
Contact Pitch	E1	1.20 BSC		
Contact Pitch	E2		1.16 BSC	
Contact Spacing	C		0.75	
Contact Width (X3)	X1			0.35
Contact Width	X2			0.43
Contact Pad Length (X4)	Y			0.50
Space Between Contacts	G1	0.85		
Space Between Contacts (X2)	G2	0.25		
Space Between Contacts	G3	0.77		
Contact 1 Index Chamfer	CH	0.13 X 45° REF		

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M
BSC: Basic Dimension. Theoretically exact value shown without tolerances.
2. The value in parenthesis, next to the item description is a unit multiplier.

Microchip Technology Drawing C04-3199B

APPENDIX A: REVISION HISTORY

Revision A (January 2019)

- Initial creation of DSC60xxB Microchip data sheet DS20006133A.

Revision B (November 2022)

- Added the 7.0 mm x 5.0 mm VDFN, 5.0 mm x 3.2 mm VDFN, and 3.2 mm x 2.5 mm VDFN package options throughout the document.
- Updated the previously existing package outline drawings to their most current versions.

Revision C (June 2025)

- Added DSA60xx reference to [Features](#) and the [Product Identification System](#) sections for customers seeking AEC-Q100 qualified parts.
- Updated all package outline drawings to reflect the most current versions.

Revision D (March 2026)

- Added a note regarding bulk quantities to the [Product Identification System](#) section.

Revision E (June 2026)

- Changed typical values for all Period Jitter, RMS, and all Period Jitter, Peak-to-Peak, in the [Electrical Characteristics](#) table.

DSC60XXB

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

PART NO.	X	X	X	X	X	X -	XXX.XXXX	X
Device	Pin 1 Definition	Output Drive Strength	Package	Temperature Range	Frequency Stability	Revision	Frequency	Media Type
Examples:								
a) DSC6013JI3B-80.0000: Ultra-Low Power MEMS Oscillator, Pin1 = $\overline{\text{STDBY}}$ with Internal Pull-Up, Low Drive Strength, 4-Lead 2.5 mm × 2.0 mm VLGA, Industrial Temperature, ±20 ppm Stability, Revision B, 80 MHz Frequency, 140/Tube								
b) DSC6001HE1B-016.0000T: Ultra-Low Power MEMS Oscillator, Pin1 = OE with Internal Pull-Up, Standard Drive Strength, 4-Lead 1.6 mm × 1.2 mm VFLGA, Extended Commercial Temp., ±50 ppm Stability, Revision B, 16 MHz Frequency, 1,000/Reel								
c) DSC6021MI2B-005Q: Ultra-Low Power MEMS Oscillator, Pin1 = Freq. Select with Internal Pull-Up, Standard Drive Strength, 4-Lead 2.0 mm × 1.6 mm VFLGA, Industrial Temperature, ±25 ppm Stability, Revision B, Two Frequencies Configured through ClockWorks, 100/Bag								
Note 1: Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.								

Device:	DSC60:	Ultra-Low Power MEMS Oscillator (Note 1)						
Pin 1 Definition:	Selection	Pin 1	Internal Pull-Up Register					
	0	OE	Pull-up					
	1	$\overline{\text{STDBY}}$	Pull-up					
	2	FS	Pull-up					
	4	OE	None					
	5	$\overline{\text{STDBY}}$	None					
Output Drive Strength:	1	Standard						
	3	Low						
Package:	A	=	4-Lead 7.0 mm × 5.0 mm VDFN					
	B	=	4-Lead 5.0 mm × 3.2 mm VDFN					
	C	=	4-Lead 3.2 mm × 2.5 mm VDFN					
	J	=	4-Lead 2.5 mm × 2.0 mm VLGA					
	M	=	4-Lead 2.0 mm × 1.6 mm VFLGA					
	H	=	4-Lead 1.6 mm × 1.2 mm VFLGA					
Temperature Range:	A	=	-40°C to +125°C (Automotive)					
	L	=	-40°C to +105°C (Extended Industrial)					
	I	=	-40°C to +85°C (Industrial)					
	E	=	-20°C to +70°C (Extended Commercial)					
Frequency Stability:	1	=	± 50 ppm					
	2	=	± 25 ppm					
	3	=	± 20 ppm					
Revision:	B	=	Revision B					
Frequency:	xxx.xxxx	=	User-Defined Frequency between 001.0000 MHz and 80.0000 MHz					
	xxxkxxx	=	User-Defined Frequency between 002.000 kHz and 999.999 kHz					
	xxxx	=	Frequency configuration code when pin 1 = FS. Configure the part online through ClockWorks® configurator.					
Media Type: (Note 3)	<blank>	=	50/Tube (A Package Option)					
	<blank>	=	72/Tube (B Package Option)					
	<blank>	=	110/Tube (C Package Option)					
	<blank>	=	140/Tube (J Package Option)					
	<blank>	=	100/Bag (M & H Package Options)					
	T	=	1,000/Reel					
	B	=	3,000/Reel					

Note 1: For AEC-Q100 qualified parts, refer to the DSA60xx family.

2: Please visit Microchip ClockWorks® Configurator Website to configure the part number for customized frequency. <http://clockworks.microchip.com/timing/>.

3: On orders for bulk CPNs, product may be supplied in either cut tape (without a reel) or tube packaging, depending on availability. For Tape & Reel orders that are <1000 pieces, product may be supplied on cut tape (without a reel) depending on availability.

DSC60XXB

NOTES:

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