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Product Termination Notification

Product Group: SIL/Wednesday May 20, 2026/PTN-SIL-040-2026-REV-0



Conversion to Copper (Cu) Wire – SQ4920EY

For further information, please contact your regional Vishay office.

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Description of Change: The affected part number listed in this notification will be converted from gold (Au) bond wire to a copper (Cu) wire material set. The new ordering code is SQ4920CEY-T1_GE3 which has identical silicon technology and silicon die design as SQ4920EY. Small changes to the data sheet AC parameters are a consequence of lot to lot variation and/or updated characterization methods (reference: SQ4920CEY Doc # 61663 Rev. A). Device performance in the application will not be impacted. There will be no change to the wafer fab location.

Reason for Change: Standardization of materials

Expected Influence on Quality/Reliability/Performance: None

Part Numbers/Series/Families Affected: SQ4920EY-T1_GE3, SQ4920EY-T1_BE3,

Vishay Brand(S): Vishay Siliconix

Time Schedule:

Last Time Buy Date: Monday November 23, 2026

Last Time Ship Date: Monday May 17, 2027

Sample Availability: Qualified samples of replacement product are available on request.

Product Identification: SQ4920CEY-T1_GE3

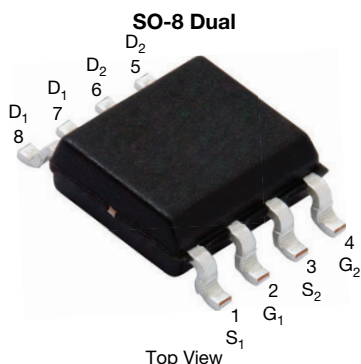
Qualification Data: AEC Q101 qualification data of replacement product is available. Qualification PPAP is available on request.

This PTN is considered approved, without further notification, unless we receive specific customer concerns before Thursday July 23, 2026 or as specified by contract.

Issued By: Lance Gurrola, automostechsupport@vishay.com



Automotive Dual N-Channel 30 V (D-S) 175 °C MOSFET

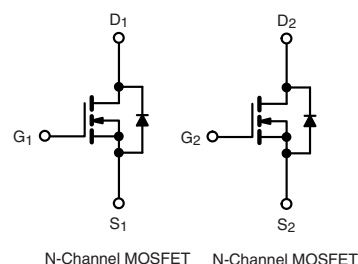


FEATURES

- TrenchFET® Power MOSFET
- AEC-Q101 qualified
- 100 % R_g and UIS tested
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE



PRODUCT SUMMARY

V_{DS} (V)	30
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	0.0145
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V	0.0175
I_D (A) per leg	8
Configuration	Dual

ORDERING INFORMATION

Package	SO-8
Lead (Pb)-free and halogen-free	SQ4920CEY (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V_{DS}	30	V
Gate-source voltage	V_{GS}	± 20	
Continuous drain current ^a	I_D	8	A
		7.2	
Continuous source current (diode conduction) ^a	I_S	4	
Pulsed drain current ^b	I_{DM}	32	
Single pulse avalanche current	I_{AS}	25	
Single pulse avalanche energy	E_{AS}	31	mJ
Maximum power dissipation	P_D	4.4	W
		1.4	
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient	R_{thJA}	110	°C/W
Junction-to-foot (drain)	R_{thJF}	34	

Notes

- a. Package limited
b. Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %
c. When mounted on 1" square PCB (FR-4 material)



SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0, I _D = 250 μA		30	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		1.5	2.0	2.5	
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V		-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 30 V	-	-	1.0	μA
		V _{GS} = 0 V	V _{DS} = 30 V, T _J = 125 °C	-	-	50	
		V _{GS} = 0 V	V _{DS} = 30 V, T _J = 175 °C	-	-	150	
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥ 5 V	30	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V	I _D = 5 A	-	0.0130	0.0175	Ω
		V _{GS} = 10 V	I _D = 6 A	-	0.0115	0.0145	
		V _{GS} = 10 V	I _D = 6 A, T _J = 125 °C	-	-	0.0220	
		V _{GS} = 10 V	I _D = 6 A, T _J = 175 °C	-	-	0.0270	
Forward transconductance ^f	g _{fs}	V _{DS} = 15 V, I _D = 6 A		-	34	-	S
Dynamic ^b							
Input capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = 15 V, f = 1 MHz	-	1302	1650	pF
Output capacitance	C _{Oss}			-	236	295	
Reverse transfer capacitance	C _{rss}			-	83	105	
Total gate charge ^c	Q _g	V _{GS} = 10 V	V _{DS} = 15 V, I _D = 6.1 A	-	20.8	30	nC
Gate-source charge ^c	Q _{gs}			-	4.2	-	
Gate-drain charge ^c	Q _{gd}			-	3	-	
Gate resistance	R _g	f = 1 MHz		2.0	4.0	7.5	Ω
Turn-on delay time ^c	t _{d(on)}	V _{DD} = 15 V, R _L = 15 Ω I _D ≅ 1 A, V _{GEN} = 10 V, R _g = 1 Ω		-	8	10	ns
Rise time ^c	t _r			-	3	15	
Turn-off delay time ^c	t _{d(off)}			-	26	37	
Fall time ^c	t _f			-	6	12	
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed current ^a	I _{SM}			-	-	32	A
Forward voltage	V _{SD}	I _F = 1.8 A, V _{GS} = 0		-	0.74	1.1	V

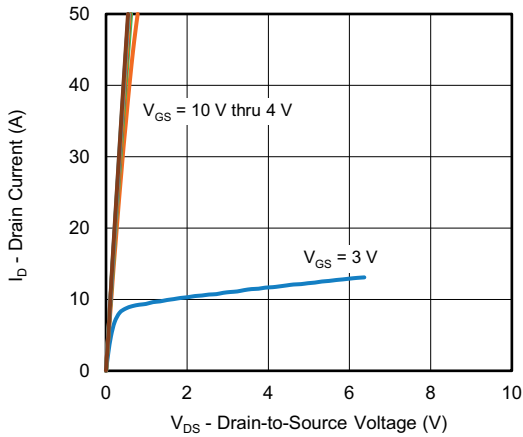
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing
c. Independent of operating temperature

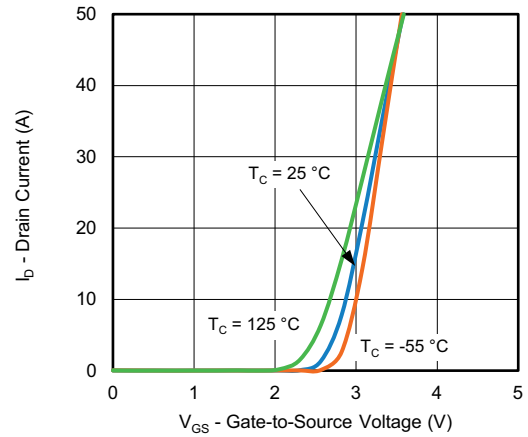
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



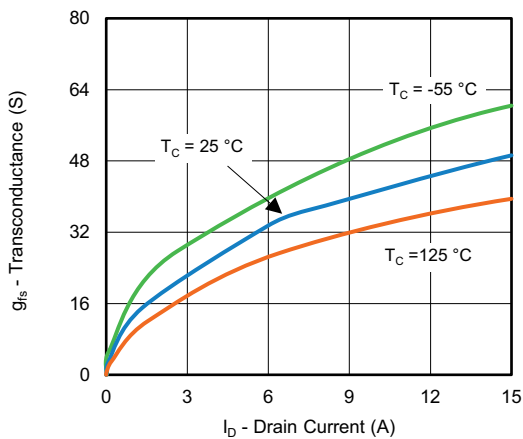
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



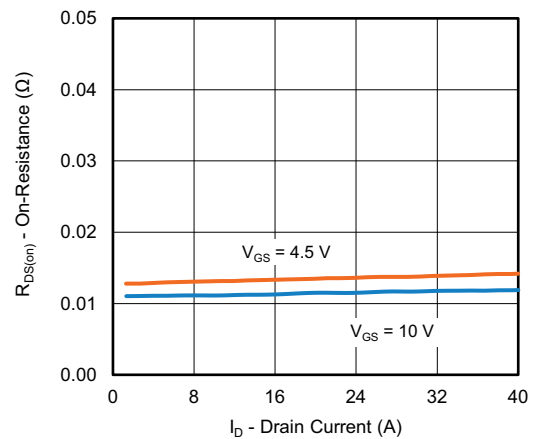
Output Characteristics



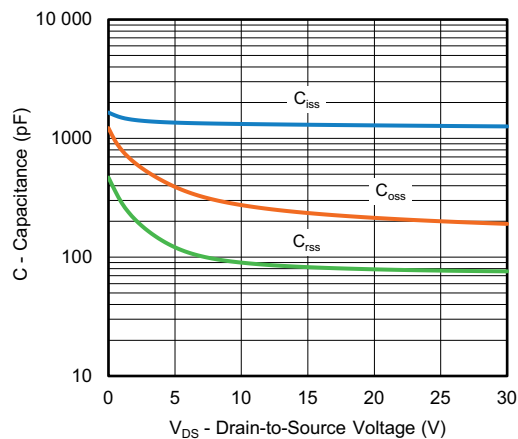
Transfer Characteristics



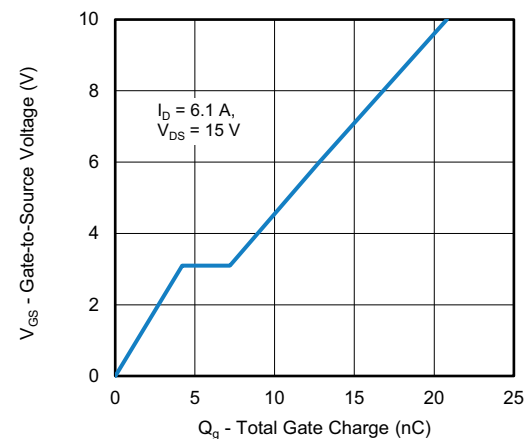
Transconductance



On-Resistance vs. Drain Current



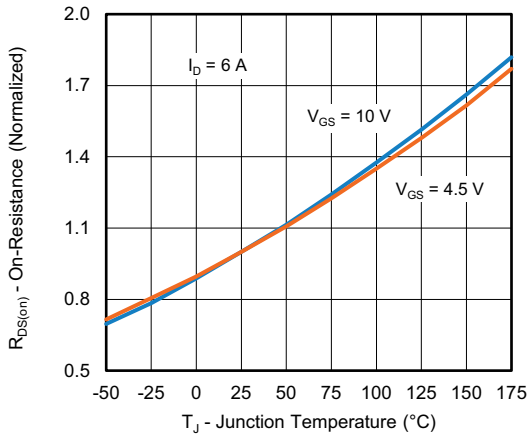
Capacitance



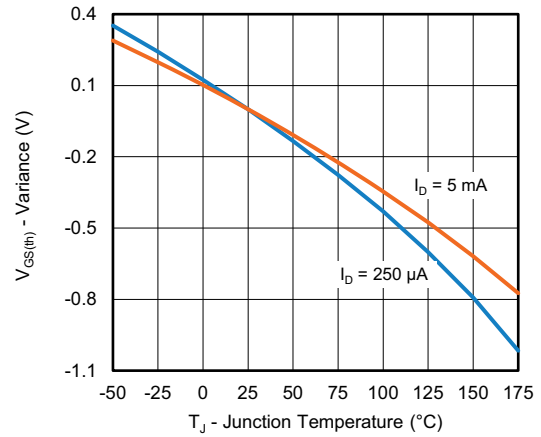
Gate Charge



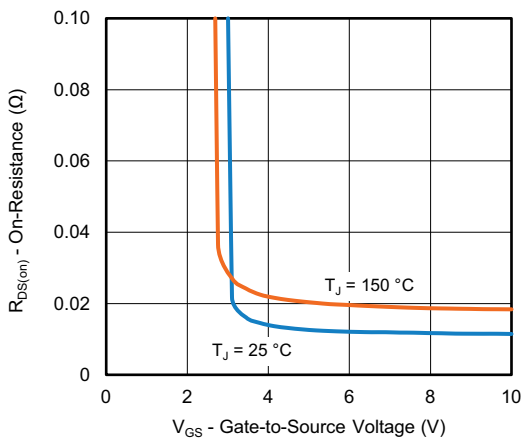
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



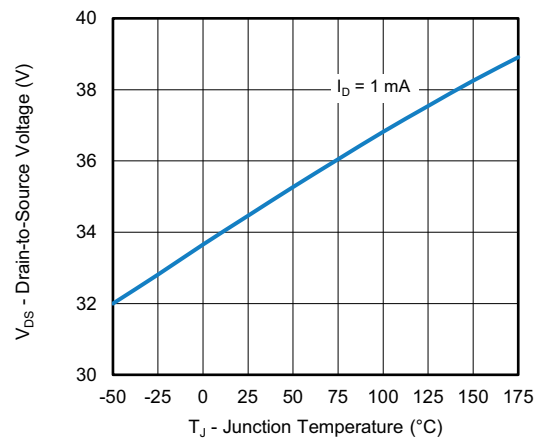
On-Resistance vs. Junction Temperature



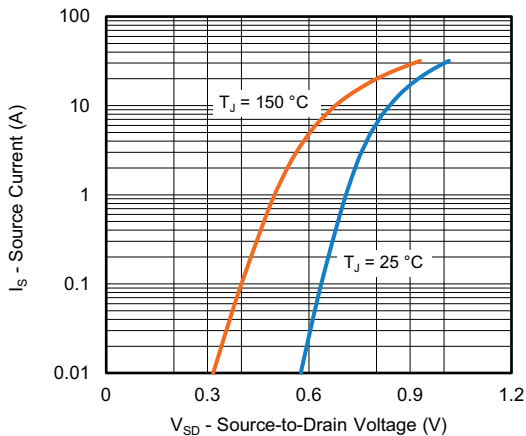
Threshold Voltage



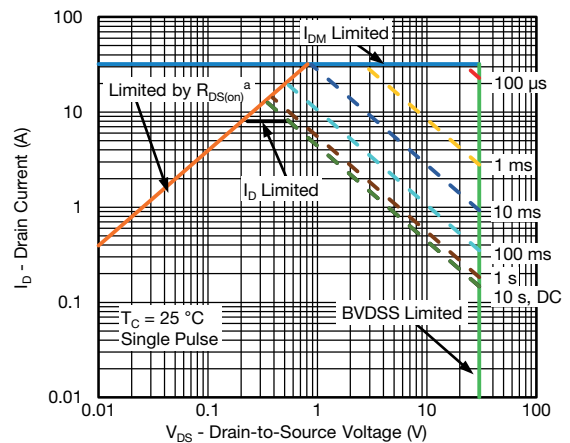
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



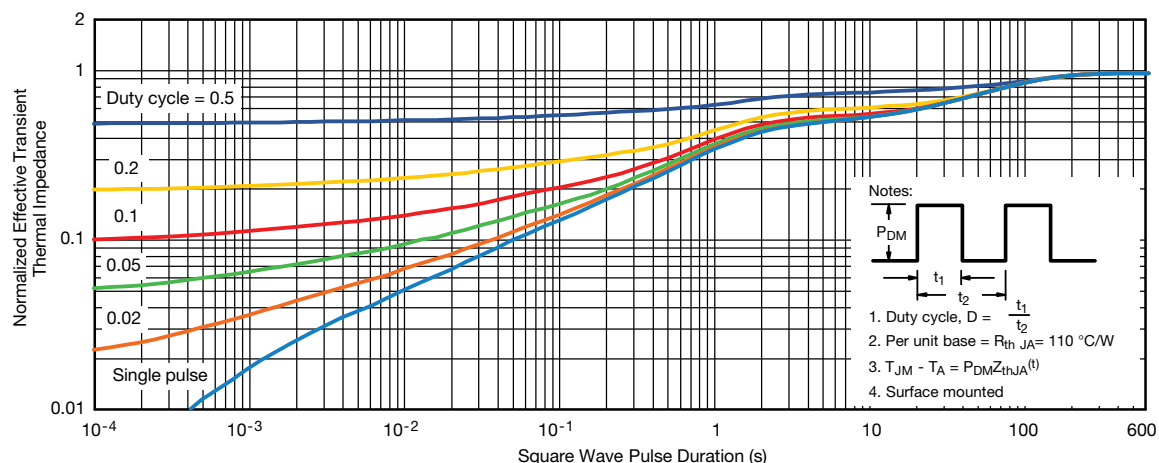
Source Drain Diode Forward Voltage



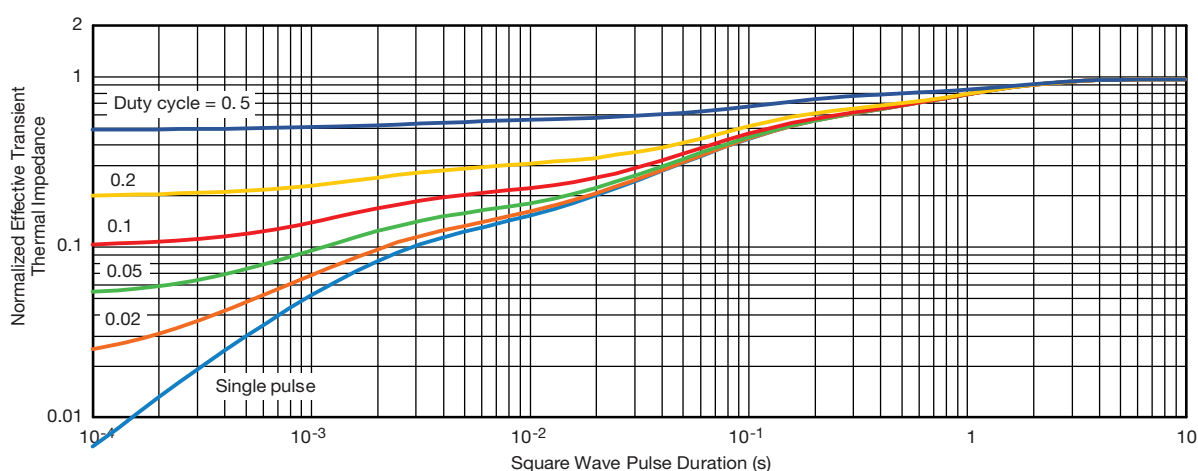
Safe Operating Area



THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Case ($25\text{ }^{\circ}\text{C}$)are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions



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