Customer Information Notification

202607032I : i.MX 91 and i.MX93 Security Reference Manual Update

Note: This notice is NXP Company Proprietary.

Issue Date: Sep 18, 2026 **Effective Date:** Sep 19, 2026

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Change Category

[ ]Wafer Fab Process	[ ]Assembly Process	[ ]Product Marking	[ ]Test Process	[ ]Design
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[ ]Wafer Fab Location	[ ]Assembly Location	[ ]Packing/Shipping/Labeling	[ ]Test Location	[ ]Electrical spec./Test coverage
[ ]Firmware	[X]Other: Security Reference Manual Update			

Notification Overview

Description

This CIN is for i.MX 91 and i.MX93 Security Reference Manual Update. Please see the revision history included in the updated document for a detailed description of the changes.
Security Reference Manuals require login to NXP.com, Go to i.MX 91 / 93 webpage->

Documentation -> Secure -> Security Reference Manual

i.MX 91 SRM:
Security Reference Manual Update for the i.MX 91 Rev. 1 to Rev. 2 -
https://www.nxp.com/products/i.MX91

i.MX 93 SRM:
Security Reference Manual Update for the i.MX 93 Rev. 5 to Rev. 6 -
https://www.nxp.com/products/processors-and-microcontrollers/arm-processors/i-mx-applications-processors/i-mx-9-processors/i-mx-93-applications-processor-family-arm-cortex-a55-ml-acceleration-power-efficient-mpu:i.MX93

Reason

The Security reference manuals have been updated for formatting items and technical clarifications / corrections. Updates listed in the revision history of the documents.

Anticipated Impact on Form, Fit, Function, Reliability or Quality

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No Impact on form, fit, function, reliability or quality

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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