Customer Information Notification

202609010I : i.MX 8M Mini Errata Updated from Rev. 1.2 to Rev. 1.3

Note: This notice is NXP Company Proprietary.

Issue Date: Oct 01, 2026 **Effective Date:** Oct 02, 2026

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Management summary

The i.MX 8M Mini Errata has been updated to Rev. 1.3.

Change Category

[ ]Wafer Fab Process	[ ]Assembly Process	[ ]Product Marking	[ ]Test Process	[ ]Design
[ ]Wafer Fab Materials	[ ]Assembly Materials	[ ]Mechanical Specification	[ ]Test Equipment	[X]Errata
[ ]Wafer Fab Location	[ ]Assembly Location	[ ]Packing/Shipping/Labeling	[ ]Test Location	[ ]Electrical spec./Test coverage
[ ]Firmware	[ ]Other			

Notification Overview

Description

The i.MX 8M Mini Errata has been updated to Rev. 1.3.

Please refer to the latest revision of the errata document for a detailed description of the changes.

The updated errata is attached to this notice, and can be found at:
https://www.nxp.com/webapp/Download?colCode=IMX8MM_0N87W

The following errata were added.
ERR053386: MICFIL: Constraints on DC Remover and Quality Mode.
ERR050192: ECSPI: TXFIFO will be cleared by a master transfer when SS is inactive
ERR052039: Core: Cortex-M hang if stopped from Cortex-A

Reason

The Errata has been updated to provide additional technical clarifications for select device features.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

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No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: view online

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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