Product Change Notification: SYST-02CZGL877

Date:

06-Oct-2026

Product Category:

Clock And Timing - Clock And Data Distribution

Notification Subject:

SY89875UMG Silicon Erratum

Affected CPNs:

SYST-02CZGL877_Affected_CPN_10062026.pdf
SYST-02CZGL877_Affected_CPN_10062026.csv

Notification Text:

SYST-02CZGL877

Microchip has released a new Document for the SY89875UMG Silicon Erratum of devices. If you are using one of these devices please read the document located at **SY89875UMG Silicon Erratum**.

Notification Status: Final

Description of Change:

Initial release of this errata. Risk Release of Fab 5 2D601 mask to inform of propagation delay limit change.

Impacts to Data Sheet: Yes. Table 1-4, Differential Propagation Delay (IN-to-Q), tPd will be updated when Fab 5 has been optimized.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 06 Oct 2026

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

SY89875UMG Silicon Erratum

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to receive Microchip PCNs via email please register for our PCN email service at our **PCN home page** select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the **PCN FAQ** section.

If you wish to change your PCN profile, including opt out, please go to the **PCN home page** select login and sign into your myMicrochip account. Select a profile option from

Affected Catalog Part Numbers (CPN)

SY89875UMG

SY89875UMG-TR