PCN260015 (R1)

Product Change Notice (PCN)

Subject: Add Alternate Fab Location
Publication Date: 10/12/2026
Effective Date: 10/12/2026

Revision Description:
Revision 1 to update latest qualification report. Initial Release of this PCN was issued on May 29, 2026. Effective Date Sept 7, 2026, remains unchanged.

Description of Change:
This PCN is to advise customers that Renesas is in the process of qualifying Taiwan Semiconductor Manufacturing Company (TSMC) Fab4 as an alternate fab location to alleviate supply chain constraints on the products listed in this notice. There is no change to the product design or process technology as compared to the existing location, TSMC Fab 8B.

Renesas will provide a conditional qual report by July 2026 that will include all items except HTOL, which will be completed by August 2026. These products will have the same orderable part number but with a different manufacturing site indicated in the top marking.

There is no expected change to the electrical specifications, package assembly and backend manufacturing process.

Affected Product List:

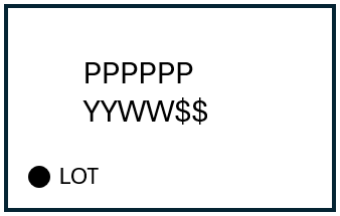
Orderable Part Number	Fab 8B Stepping	Fab 4 Stepping	Orderable Part Number	Fab 8B Stepping	Fab 4 Stepping
RC19016A100GN1#BB0	Z	ZA	RC19008A100GND#BB0	Z	ZA
RC19016A100GN1#KB0			RC19008A100GND#KB0		
RC19013A100GNG#BB0			RC19004A100GNL#BB0		
RC19013A100GNG#KB0			RC19004A100GNL#KB0		
9QXL2001CNHGK	ZA	ZD	9QXL2001CNHGK8/N	ZA	ZD
9QXL2001CNHGK/N			9QXL2001CNHGK8/S		
9QXL2001CNHGK8			9QXL2001CNHGK8/W		
9QXL2001CNHGK8/M			RC19020AGN6#BD0	Z	ZA
RC19004AGNL#BB0	ZA	ZB	RC19020AGN6#KD0		
RC19004AGNL#KB0			RC19020AGN6#KDH		
RC19004AGNL#KBH			RC19002AGNT#BD0		
RC19008AGND#BB0			RC19002AGNT#KD0		
RC19008AGND#KB0			RC19002AGNT#KDH		
RC19008AGND#KBH			RC19202AGNT#BD0		
RC19013AGNG#BB0			RC19202AGNT#KD0		
RC19013AGNG#KB0			RC19202AGNT#KDH		
RC19016AGN1#BB0			RC19204AGNL#BB0		
RC19016AGN1#KB0			RC19204AGNL#KB0		
9QXL1200ALWGK	ZA	ZB	RC19208AGNA#BB0		
9QXL1200ALWGK/W			RC19208AGNA#KB0		
9QXL1200ALWGK8			RC19216AGN6#BD0		
			RC19216AGN6#KD0		

Reason for Change:
To counteract supply constraints fabrication is being moved to Fab4 to meet customer delivery requirements.

Impact on Fit, Form, Function, Quality & Reliability:
No expected impact on fit, form, function, quality, reliability and environmental compliance of the products.

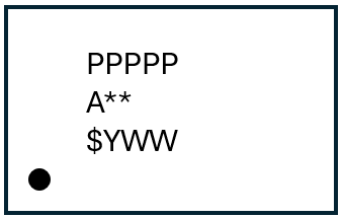
Product Identification:
Refer to the top mark stepping number "#" as outlined in the Affected Product List table. Renesas is revising the marking format for parts that do not presently contain a stepping number:

Current Markings



RC19004AGNL/ RC19004A100GNL

- Line 1: PPPPP = Truncated Product Name
- Line 2:
  - YY = Last 2 digit of year
  - WW = Workweek
  - $$ = 2-Character Mark Code
- Line 3: LOT= Assembly lot no



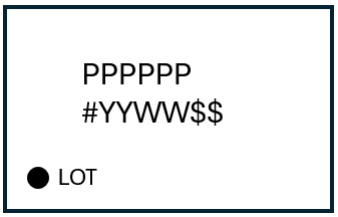
RC19002AGNT/RC19202AGNT

- Line 1: PPPPP- Truncated Product Name
- Line 2:
  - A = Product name
  - ** = 2-Character sequential code
- Line 3: $ = 1-Character Mark Code
  - Y = Last digit of year
  - WW = Workweek



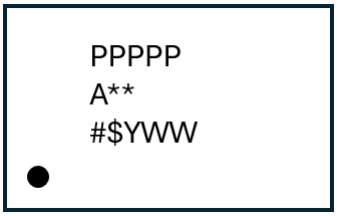
RC19204AGNL

New Markings



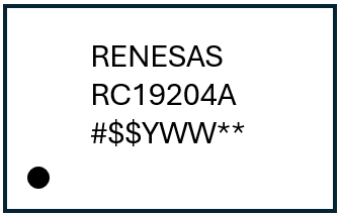
RC19004AGNL/ RC19004A100GNL

- Line 1: PPPPP = Truncated Product Name
- Line 2:
  - # = Device stepping
  - YY = Last 2 digit of year
  - WW = Workweek
  - $$ = 2-Character Mark Code
- Line 3: LOT= Assembly lot no



RC19002AGNT/RC19202AGNT

- Line 1: PPPPP- Truncated Product Name
- Line 2:
  - A = Product name
  - ** = 2- Character sequential code
- Line 3: # = Device stepping
  - $ = 1-Character Mark Code
  - Y = Last digit of year
  - WW = Workweek



- Line 1: Company Name
- Line 2: Product Name
- Line 3: $$ = 2-Character Mark Code
  - Y = Last digit of year
  - WW = Workweek
  - *** = 3-Character sequential code

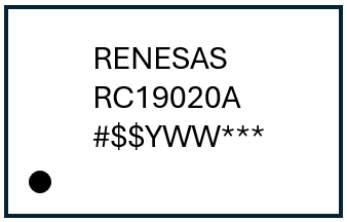


RC19020AGN6

- Line 1: Company Name
- Line 2: Product name
- Line 3: $$ = 2-character Mark Code
  - Y = last digit of year
  - WW = Workweek
  - *** = 3-Character sequential code

RC19204AGNL

- Line 1: Company Name
- Line 2: Product Name
- Line 3: # = Device stepping
  - $$ = 2-Character Mark Code
  - Y = Last digit of year
  - WW = Workweek
  - *** = 2-Character sequential code



RC19020AGN6

- Line 1: Company Name
- Line 2: Product name
- Line 3: # = Device stepping
  - $$ = 2-character Mark Code
  - Y = last digit of year
  - WW = Workweek
  - *** = 3-Character sequential code

Qualification Status: Completed. Refer Appendix A.

Sample Availability Date: Targeting samples to be shipped in August 2026

Test Correlation report: Available August 2026

Note:

1. Acknowledgement must be received by Renesas within 30 days or Renesas will consider the change as approved.

2. If timely acknowledgement is provided by Customer, then Customer shall have 90 days from the date of receipt of this PCN to make any objections to this PCN. If Customer fails to make objections to this PCN within 90 days of the receipt of the PCN then Renesas will consider the PCN changes as approved.

3. If customer cannot accept the PCN then customer must provide Renesas with a last time buy demand and purchase order.

For additional information regarding this notice, please contact your Renesas sales representative.

Appendix A - Qualification Results

To: Generic

Report No. QTP-US-26-0059-MSG-S-N
Date: August 20, 2026

RENESAS SEMICONDUCTOR RELIABILITY REPORT

SERIES: PCIE clock buffer

DEVICE: 9QXL1200X, 9QXL2001x, RC19002x, RC19004x, RC19008x, RC19013x, RC19016X, RC19020x, RC19202x, RC19204x, RC19208x,

Quality Assurance Div.
Renesas Electronics Corporation

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(Rev.5.0-2 October 2020)

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Table. Reliability test results

Test Items	Reference	Test Conditions	Number of Lots	Number of Samples	Results Failure/Size	Comments
High Temperature Operating Life (HTOL)	JESD22-A108, JESD85	Tj=125C, Vccmax, 1000 hrs	1	77	0/77	Pass
High Temperature Storage Life (HTSL)	JESD22-A103	Ta=150C, 1000 hrs	1	77	0/77	Pass
Latch-Up (LU)	JESD78	Pulse Current Injection, I=+/-100 mA and 1.5x Vcc OV stress, Class II (125C)	1	6	0/6	Pass
Electrical Parameter	JESD86	Datasheet	1	30	0/30	Pass
Human Body Model ESD (ESD-HBM)	JS-001	Per Spec	1	3	0/3	Pass 2000V.
Charged Device Model ESD (ESD-CDM)	JS-002	Per Spec	1	3	0/3	Pass 1000V. Class C3
Temperature Cycling (TC)	JESD22-A104	Cond. B. -55°C to 125°C, 700 cyc	1	30	0/30	Pass
Passivation Integrity Test (PIT)	TOS-QS00229	per spec TOS-QS00229	1	15	0/15	Pass

・It is tested to confirm that all the samples are satisfied with an individual product specification.

Note :
Basically qualification tests were performed using a representative product with the same wafer process and the same package structure .

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