PCN260017 (R2)

Product Change Notice (PCN)

Subject: Add Alternate Fab Location
Publication Date: 10/12/2026
Effective Date: 10/12/2026

Revision Description:
Revision 2 to update latest qualification report. Effective Date Sept 17, 2026, remains unchanged.
Revision 1 to add 5PB1102CMGI8 which was inadvertently left out in the initial release. PCN effective date remains unchanged.

Description of Change:
This PCN is to advise customers that Renesas is in the process of qualifying Taiwan Semiconductor Manufacturing Company (TSMC) Fab4 as an alternate fab location to alleviate supply chain constraints on the products listed in this notice. There is no change to the product design or process technology as compared to the existing location, TSMC Fab 8B.

Renesas will provide a conditional qual report by June 2026 that will include all items except HTOL, which will be completed by July 2026. These products will have the same orderable part number but with a different manufacturing site indicated in the top marking.

There is no expected change to the electrical specifications, package assembly and backend manufacturing process.

Affected Product List:

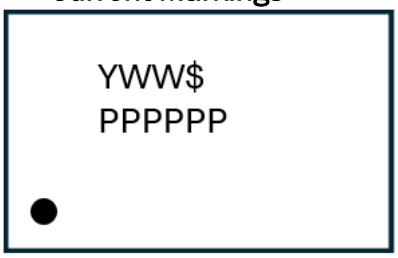
Orderable Part Number	Fab 8B Stepping	Fab 4 Stepping	Orderable Part Number	Fab 8B Stepping	Fab 4 Stepping
5PB1102CMGI	ZC	ZE	5PB1104CMGI	ZC	ZE
5PB1102CMGI8			5PB1104CMGI/W		
5PB1102CMGK		ZD	5PB1104CMGI8		
5PB1102CMGK8			5PB1104CMGK		ZD
5PB1102PGGI		ZF	5PB1104CMGK/W		
5PB1102PGGI8			5PB1104CMGK8		
5PB1102PGGK		ZD	5PB1104PGGI		ZF
5PB1102PGGK8			5PB1104PGGI8		
9DBL0255NLGI	ZA	ZB	5PB1104PGGK		ZD
9DBL0255NLGI8			5PB1104PGGK8		

Reason for Change:
To counteract supply constraints fabrication is being moved to Fab4 to meet customer delivery requirements.

Impact on Fit, Form, Function, Quality & Reliability:
No expected impact on fit, form, function, quality, reliability and environmental compliance of the products.

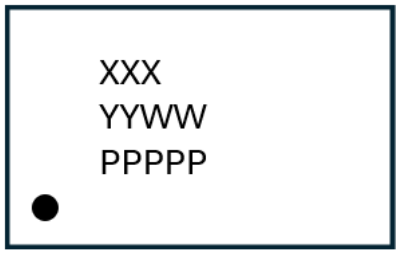
Product Identification:
Refer to the top mark stepping number "#" as outlined in the Affected Product List table. Renesas is revising the marking format for parts that do not presently contain a stepping number:

Current Markings



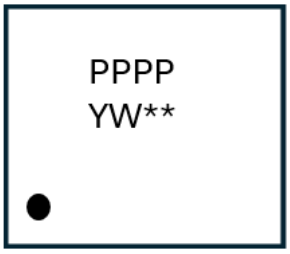
5PB1102PGGI/5PB1102PGGK/5PB1104PGGI/5PB1104PGGK

- Line 1: Y = Last digit of the year
  - WW = Workweek
  - $ = Single Char assembly code Line
- Line 2: Truncated Product Name



9DBL0255NLGI

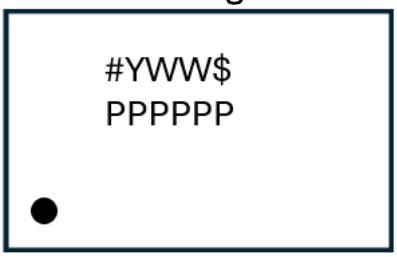
- Line 1: XXX is last 3 digits of assembly lot no
- Line 2: YY = last 2 digit of year
  - WW = Workweek
- Line 3: Truncated Product Name



5PB1102CMGI/ 5PB1102CMGK/5PB1104CMGI/ 5PB1104CMGK

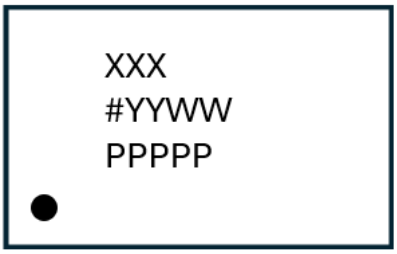
- Line 1: Truncated Product Name
- Line 2: Y = last digit of year
  - W = Single character work week
  - ** = 2-char sequential code

New Markings



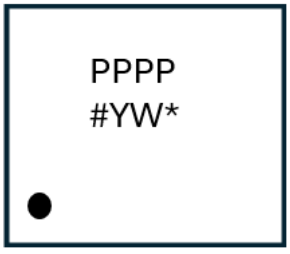
5PB1102PGGI/5PB1102PGGK/5PB1104PGGI/5PB1104PGGK

- Line 1: # = Device stepping
  - Y = Last digit of the year
  - WW = Workweek
  - $ = Single Char assembly code Line
- Line 2: Truncated Product Name



9DBL0255NLGI

- Line 1: XXX is last 3 digits of assembly lot no
- Line 2: # = Device stepping
  - YY = last 2 digit of year
  - WW = Workweek
- Line 3: Truncated Product Name



5PB1102CMGI/ 5PB1102CMGK/5PB1104CMGI/ 5PB1104CMGK

- Line 1: Truncated Product Name
- Line 2: # = Device stepping
  - Y = last digit of year
  - W = Single character work week
  - ** = 2-char sequential code

Qualification Status: Completed. Refer Appendix A.

Sample Availability Date: Target is for samples to be shipped in July 2026

Note:

1. Acknowledgement must be received by Renesas within 30 days or Renesas will consider the change as approved.

2. If timely acknowledgement is provided by Customer, then Customer shall have 90 days from the date of receipt of this PCN to make any objections to this PCN. If Customer fails to make objections to this PCN within 90 days of the receipt of the PCN then Renesas will consider the PCN changes as approved.

3. If customer cannot accept the PCN then customer must provide Renesas with a last time buy demand and purchase order.

For additional information regarding this notice, please contact your Renesas sales representative.

Appendix A - Qualification Results

To: Generic

Report No. QTP-US-26-0046-MSG-S-N
Date: July 30, 2026

RENESAS SEMICONDUCTOR RELIABILITY REPORT

SERIES: PCIe fan out buffer

DEVICE: **5PB1102CMGx, 5PB1102PGGx, 5PB1104CMGx, 5PB1104PGGx, 9DBL0255NLGx**

Quality Assurance Div.
Renesas Electronics Corporation

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(Rev.5.0-2 October 2020)

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Table. Reliability test results

Test Items	Reference	Test Conditions	Number of Lots	Number of Samples	Results Failure/Size	Comments
High Temperature Operating Life (HTOL)	JESD22-A108, JESD85	Tj=125C, Vccmax, 1000 hrs	1	77	0/77	Pass
High Temperature Storage Life (HTSL)	JESD22-A103	Ta=150C, 1000 hrs	1	77	0/77	Pass
Latch-Up (LU)	JESD78	Pulse Current Injection, I=+/-100 mA and 1.5x Vcc OV stress, Class II (125C)	1	6	0/6	Pass
Electrical Parameter	JESD86	Datasheet	1	30	0/30	Pass
Human Body Model ESD (ESD-HBM)	JS-001	Per Spec	1	3	0/3	Pass 2500V. Class 2
Charged Device Model ESD (ESD-CDM)	JS-002	Per Spec	1	3	0/3	Pass 1000V. Class C3
Temperature Cycling (TC)	JESD22-A104	Cond. B. -55°C to 125°C, 700 cyc	1	30	0/30	Pass
Unbiased Highly Accelerated Stress Test (UHST)	JESD22-A118 & A102	Ta=130C, RH=85%, 96 hrs	1	30	0/30	Pass

・It is tested to confirm that all the samples are satisfied with an individual product specification.

Note :
Basically qualification tests were performed using a representative product with the same wafer process and the same package structure .

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