2607221810 SiWx917Y Module HW Revision X Parts and Data Sheets v1.2 and Errata

PRCN Issue Date: Jul 22, 2026 **Last Ship Date:** Jan 28, 2027

Last Order Date: Oct 28, 2026

PCN Type: Datasheet; Discontinuance; Product Revision; Errata

Description of Change

Silicon Labs is pleased to announce a new revision (HW Revision X) of SiWG917Y, SiWN917Y and SiWT917Y modules (jointly referred to as SiWx917Y).

The data sheets are being updated to revision 1.2 to update ordering details for revision X OPNs. Revision X OPNs add support for an external 32.768 kHz crystal through the addition of two dedicated pins on the bottom of the module. See the data sheet revision history for a complete list of changes, key change summary for data sheets and errata appended below.

Links to data sheets:
- v1.2 for SiWG917Y: https://www.silabs.com/documents/public/data-sheets/siwg917y-datasheet.pdf
- v1.2 for SiWN917Y: https://www.silabs.com/documents/public/data-sheets/siwn917y-datasheet.pdf
- v1.2 for SiWT917Y: https://www.silabs.com/documents/public/data-sheets/siwt917y-datasheet.pdf

Links to errata:
- v0.4 for SiWG917Y: https://www.silabs.com/documents/public/errata/siwg917-soc-module-errata.pdf
- v0.2 for SiWN917Y: https://www.silabs.com/documents/public/errata/siwn917-ncp-module-errata.pdf
- v0.1 for SiWT917Y: https://www.silabs.com/documents/public/errata/siwt917-rcp-module-errata.pdf

Silicon Labs announces Last Time Buy (LTB) for prior revision parts listed in Product Identification section.

Existing customers may place LTB orders per the key dates above and guidelines listed in this PRCN form.

All customer orders placed after the PRCN Issue Date will be Non-Cancellable and Non-Returnable (NCNR). Orders placed before the PRCN Issue Date will become NCNR after the Last Order Date. These orders will need to have a Customer Requested Delivery Date (CRD) before the Last Ship Date. As of this PRCN Issue Date, Silicon Labs will no longer accept customer orders with a CRD beyond the Last Ship Date. Additionally, these orders will be Built to Order (BTO) and may have extended lead times. See your sales or distributor representative for new Lead Times.

Note: Silicon Labs will accept customer orders that are received prior to the Last Order Date shown above, but does not guarantee that it will be able to fill all or part of any orders due to limited availability, manufacturing issues or other reasons beyond Silicon Labs' reasonable control. Estimated delivery dates beyond normal lead time should not be considered as a firm acceptance or a guarantee of supply at any time.

If you have questions about this PRCN, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at www.silabs.com.

Software Impact Description

None.

Reason for Change

The SiWx917 requires a 32.768 kHz oscillator or crystal for operation. The original (HW Revision B) modules supported an oscillator only and did not include or support a crystal. The module revision (HW Revision X) allows support of an external 32.768 kHz crystal to be used.

Impact on Form, Fit, Function, Quality, Reliability

No impact on Form, Fit, Quality or Reliability. Function is improved by added support for an external 32.768 kHz crystal in the HW Revision X devices.

Product Identification

Some of the original modules will be discontinued (Existing Part # in the below Product Identification table) while others will remain active. See appendix for details. The new module is pin compatible if the customer followed the PCB landing pattern.

Existing Part #	Replacement Part #	Drop-in Compatible
SIWG917Y110LGABA	SIWG917Y110LGAXA	SEE DATASHEET
SIWG917Y110LGABAR	SIWG917Y110LGAXAR	SEE DATASHEET
SIWG917Y110LGNBA	SIWG917Y110LGNXA	SEE DATASHEET
SIWG917Y110LGNBAR	SIWG917Y110LGNXAR	SEE DATASHEET
SIWG917Y121MGNBA	SIWG917Y121MGNXA	SEE DATASHEET
SIWG917Y121MGNBAR	SIWG917Y121MGNXAR	SEE DATASHEET
SIWT917Y100XGNBA	SIWT917Y100XGNXA	SEE DATASHEET
SIWT917Y100XGNBAR	SIWT917Y100XGNXAR	SEE DATASHEET

Kit Identification

The kits listed under "Existing Kit #" will only be orderable if inventory is available. The kits listed under "Replacement Kit #" are currently available and are relevant to the parts listed above as Replacement Parts. If no kits are listed under Kit Identification, then no kits are affected.

Qualification Samples

Available upon request.

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change,Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at https://www.silabs.com.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCN@silabs.com

User Registration

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, “how-to” and design documents, product change notices (PCN) and other valuable content available only to registered users. https://www.silabs.com/profile

Qualification Data

See Appendix.

Existing OPN	Product Status	Comments
SIWG917Y110LGABA	EOL and LTB	Replace with new revision
SIWG917Y110LGNBA	EOL and LTB	Replace with new revision
SIWG917Y111MGABA	Active	Recommend new designs use new revision
SIWG917Y111MGNBA	Active	Recommend new designs use new revision
SIWG917Y121MGABA	Active	Recommend new designs use new revision
SIWG917Y121MGNBA	EOL and LTB	Replace with new revision
SIWN917Y100LGABA	Active	Recommend new designs use new revision
SIWN917Y100LGNBA	Active	Recommend new designs use new revision
SIWT917Y100XGABA	Active	Recommend new designs use new revision
SIWT917Y100XGNBA	EOL and LTB	Replace with new revision

Module Revision Appendix Table

Key change summary for data sheets:

Common Changes (SoC / NCP / RCP)

- Updated Wi-Fi features to **DL MU-MIMO and DL/UL OFDMA**, improved power consumption (**71 µA → 65 µA**, **4.5 µA → 2.5 µA**, **11 µA → 10 µA**), and updated to **Bluetooth 5.4** where applicable.
- Added **HW Revision X** support and introduced **Table 2.3** for HW Revision X modules.
- Added **pins 72/73** for **XTAL_32KHZ_P/N**, standardized **FLASH_IO_VDD** for in-package Flash/PSRAM, and updated GPIO/UART/JTAG information.
- Clarified **32.768 kHz clock/crystal** requirements and updated WLAN/BLE specifications, including RSSI ranges (**+2 dBm to −87 dBm**, **+2 dBm to −83 dBm**) and current consumption values (**43 µA → 41 µA**, **141 µA → 125 µA** where applicable).
- Updated RF/debug/reset schematics, added crystal implementation guidance, differentiated **HW Revision B vs X** packages, and added **Japan MIC certifications (203-JN1439, 203-JN1440)**.
- Added **GitHub** and **SLT-CLI** documentation resources.

SoC-Specific Changes

- Enhanced Digital, Bluetooth, Host Interface, and UULP feature descriptions; updated block diagram and software architecture.
- Removed **D-CACHE** and **TIMESTAMP**, added **auto host detection**, and updated peripheral architecture (SPI → HSPI, µDMA/UULP updates).
- Refined peripheral capabilities, including **DAC clock frequency reduction (5 MHz → 2.5 MHz)** and ADC/Opamp specification updates (**PSRR 90 dB → 85 dB**).

NCP-Specific Changes

- Removed BLE features including **Extended/Periodic Advertising, LE Secure Connections, and CSA#2**.
- Added support for **UART rates >115200 bps** with mandatory hardware flow control.
- Added **50 Ω external antenna** requirement and related design guidance.

RCP-Specific Changes

- Updated operating mode from **Wi-Fi + BLE** to **Wi-Fi STA + BLE**.
- Corrected **GPIO count (41 → 43)** and updated GPIO defaults.
- Corrected power pin mapping (e.g., **pin 36**) and package pin-location details.

Key change summary for data sheets

Key change summary for errata:

SoC:

- Added 3 new errata: Anti-Rollback firmware version dependency, PSRAM data corruption with D-Cache enabled, and removal of GPIO Timestamp support.
- Updated 32 kHz clock guidance to recommend a 32.768 kHz crystal for HW Rev X designs.
- Enhanced calendar accuracy errata with HW Rev X-specific workaround and resolution.
- Added software workarounds and SDK recommendations to address PSRAM/D-Cache corruption issues.

NCP & RCP:

- Revision adds 32.768 kHz XTAL support and recommends it for new HW Revision X designs.

Key change summary for errata

SiWx917Y Refresh Qualification Report

SILICON LABS

W7104F1-Module Product Qualification Report Record, Rev. I

Module Rev B, Ryder VN Assembly

Test Name	Test Condition	Qualification	Lot ID or Start	Fail/Pass or End	Notes	Summary	Status
Test Group A – Accelerated Environment Stress Tests							
Preconditioning	JESD22-A113 MSL3: 260°C 30°C / 60%RH / 192 Hr	1 lot, N=>10	Q052928	0/15		1 lots 0/15	Pass
Temp Cycle	JA104 Cond G: -40°C to 125°C 850 cycles	1 lot, N=>10	Q052929	0/15	1	1 lots 0/15	Pass
Test Group C – Package Assembly Integrity Tests							
Mechanical Shock	JESD22-B110, Cond. B (1500g)	1 lot, N=>10	Q052924	0/10		1 lots 0/10	Pass
Mechanical Vibration	JESD22-B103, Cond. 1 (20-2000Hz)	1 lot, N=>10	Q052925	0/10		1 lots 0/10	Pass
Cleanliness Assessment	IPC-TM-650 2.3.28B	1 lot, N=>10	Q052926	0/10		1 lots 0/10	Pass
Test Group E – Electrical Verification							
ESD-CDM	JS-002	1 lot, N=>3	Q052927			850 V	Class C2b

Notes:
1. Parts are Pre-conditioned at MSL3/260°C

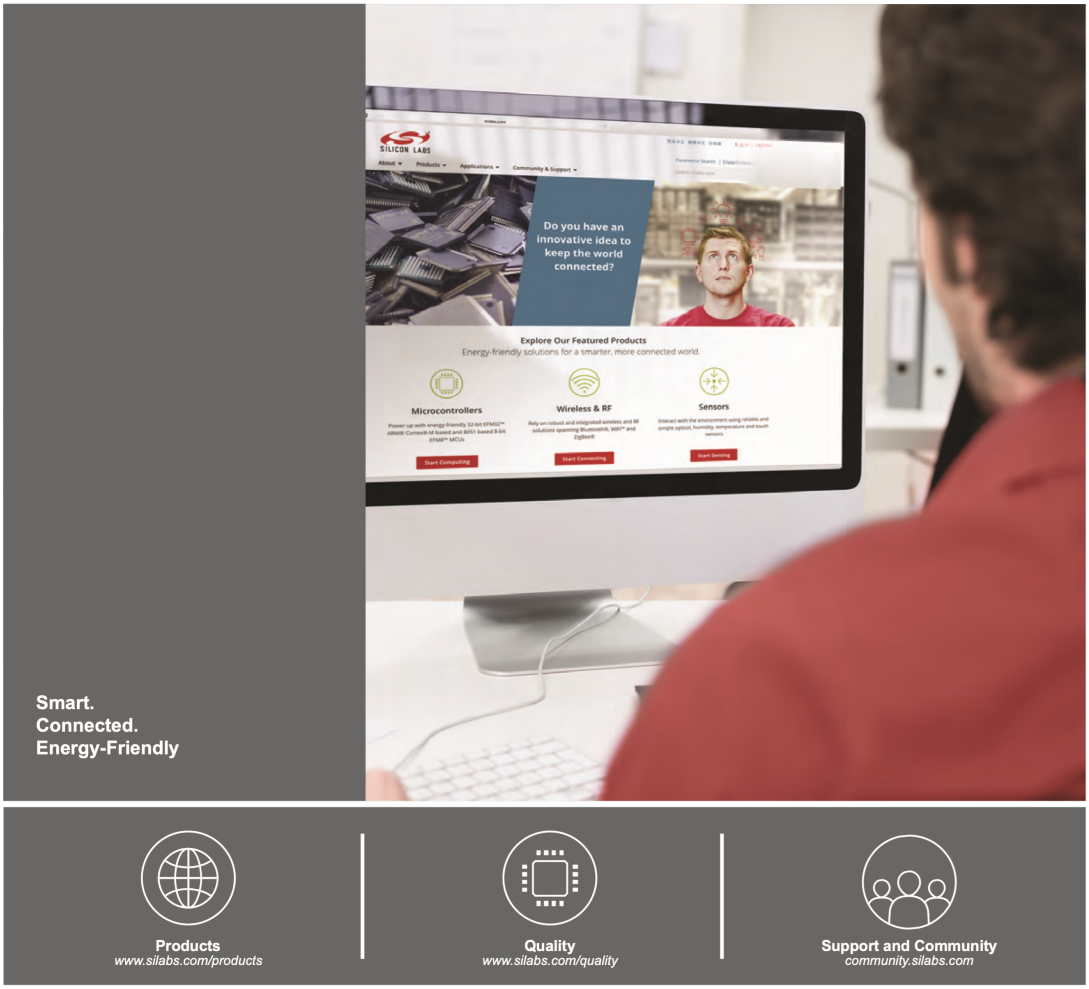
This report applies to the following part numbers:	
SiWT917Y100XGxXA	SiWG917Y110LGxXA
SiWN917Y100LGxXA	SiWG917Y111MGxXA
	SiWG917Y121MGxXA

Approved by: Mark Borlongan
Silicon Labs - Internal use only

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Prepared on: 18-Mar-26

SiWx917Y Qual Report



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