

Description

The new MiniLED Series have been designed in a small white SMT package. The feature of the device is the very small package 2.3 mm x 1.3 mm x 1.4 mm. The MinLED is an obvious solution for small-scale, high-power products that are expected to work reliably in an arduous environment. This is often the case in automotive and industrial application of course.

Features

- · SMD LEDs with exceptional brightness
- · Luminous intensity categorized
- · Compatible with automatic placement equipment
- EIA and ICE standard package
- · IR reflow soldering
- · Available in 8 mm tape
- · Low profile package
- · Non-diffused lens: excellent for coupling to light pipes and backlighting
- · Low power consumption
- · Luminous intensity ratio in one packaging unit $I_{Vmax}/I_{Vmin} \le 2.0$, optional ≤ 1.6



Applications

Automotive: Backlighting in dashboards and switches Telecommunication: Indicator and backlighting in telephone and fax

Indicator and backlight for audio and video equipment Indicator and backlight in office equipment Flat backlight for LCDs, switches and symbols General use

Parts Table

Part	Color, Luminous Intensity	Angle of Half Intensity (±φ)	Technology
TLMS2100	Super red, I _V = 7.5 mcd (typ.)	60 °	GaAsP on GaP
TLMO2100	Soft orange, $I_V = 7.5 \text{ mcd (typ.)}$	60 °	GaAsP on GaP
TLMY2100	Yellow, I _V = 7.5 mcd (typ.)	60 °	GaAsP on GaP
TLMG2100	Green, I _V = 10 mcd (typ.)	60 °	GaP on GaP
TLMP2100	Pure green, I _V = 2.2 mcd (typ.)	60 °	GaP on GaP
TLMB2100	Blue, I _V = 7 mcd (typ.)	60 °	GaN

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Absolute Maximum Ratings

 T_{amb} = 25 °C, unless otherwise specified TLMS2100 ,TLMO2100 ,TLMY2100 ,TLMG2100 ,TLMP2100

Parameter	Test condition	Symbol	Value	Unit
Reverse voltage		V _R	6	V
DC Forward current	T _{amb} ≤ 60 °C	I _F	30	mA
Surge forward current	t _p ≤ 10 μs	I _{FSM}	0.5	Α
Power dissipation	T _{amb} ≤ 60 °C	P _V	95	mW
Junction temperature		Tj	100	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Soldering temperature	according IPC 9501	T _{sd}	245	°C
Thermal resistance junction/ ambient	mounted on PC board (pad size > 5 mm ²)	R _{thJA}	480	K/W

TLMB2100

Parameter	Test condition	Symbol	Value	Unit
Reverse voltage		V _R	5	V
DC Forward current	T _{amb} ≤ 60 °C	I _F	20	mA
Surge forward current	t _p ≤ 10 μs	I _{FSM}	0.1	A
Power dissipation	T _{amb} ≤ 60 °C	P _V	90	mW
Junction temperature		T _j	100	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Soldering temperature	according IPC 9501	T _{sd}	245	°C
Thermal resistance junction/ ambient	mounted on PC board (pad size > 5 mm ²)	R _{thJA}	480	K/W

Optical and Electrical Characteristics

 $T_{amb} = 25$ °C, unless otherwise specified

Super red

TLMS2100

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Luminous intensity 2)	I _F = 10 mA	I _V	2.5	7.5		mcd
Dominant wavelength	I _F = 10 mA	λ_{d}	624	628	636	nm
Peak wavelength	I _F = 10 mA	λ_{p}		640		nm
Angle of half intensity	I _F = 10 mA	φ		± 60		deg
Forward voltage	I _F = 20 mA	V _F		2.1	3.0	V
Reverse voltage	I _R = 10 μA	V _R	6	15		V
Junction capacitance	V _R = 0, f = 1 MHz	C _j		15		pF

 $^{^{2)}}$ in one Packing Unit $I_{Vmax}\!/I_{Vmin} \leq 2.0$

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Soft Orange

TLMO2100

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Luminous intensity 2)	I _F = 10 mA	I _V	3.2	7.5		mcd
Dominant wavelength	I _F = 10 mA	λ_{d}	598	605	611	nm
Peak wavelength	I _F = 10 mA	λ_{p}		605		nm
Angle of half intensity	I _F = 10 mA	φ		± 60		deg
Forward voltage	I _F = 20 mA	V _F		2.1	3	V
Reverse voltage	I _R = 10 μA	V _R	6	15		V
Junction capacitance	V _R = 0, f = 1 MHz	C _j		15		pF

²⁾ in one Packing Unit I_{Vmax}/I_{Vmin} ≤ 2.0

Yellow

TLMY2100

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Luminous intensity ²⁾	I _F = 10 mA	I _V	3.2	7.5		mcd
Dominant wavelength	I _F = 10 mA	λ_{d}	581	588	594	nm
Peak wavelength	I _F = 10 mA	λ_{p}		585		nm
Angle of half intensity	I _F = 10 mA	φ		± 60		deg
Forward voltage	I _F = 20 mA	V_{F}		2.2	3	V
Reverse voltage	I _R = 10 μA	V_{R}	6	15		V
Junction capacitance	V _R = 0, f = 1 MHz	C _j		15		pF

 $[\]frac{1}{2}$ in one Packing Unit $I_{Vmax}/I_{Vmin} \le 2.0$

Green

TLMG2100

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Luminous intensity 2)	I _F = 10 mA	I _V	6.3	10		mcd
Dominant wavelength	I _F = 10 mA	λ_{d}	562	568	575	nm
Peak wavelength	I _F = 10 mA	λ _p		565		nm
Angle of half intensity	I _F = 10 mA	φ		± 60		deg
Forward voltage	I _F = 20 mA	V _F		2.2	3.0	V
Reverse voltage	I _R = 10 μA	V _R	6	15		V
Junction capacitance	V _R = 0, f = 1 MHz	C _j		15		pF

 $^{^{2)}}$ in one Packing Unit $I_{Vmax}/I_{Vmin} \leq 2.0$

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Pure green

TLMP2100

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Luminous intensity 2)	I _F = 10 mA	I _V	1.0	2.2		mcd
Dominant wavelength	I _F = 10 mA	λ_{d}	555	560	565	nm
Peak wavelength	I _F = 10 mA	λ_{p}		555		nm
Angle of half intensity	I _F = 10 mA	φ		± 60		deg
Forward voltage	I _F = 20 mA	V _F		2.4	3	V
Reverse voltage	I _R = 10 μA	V _R	6	15		V
Junction capacitance	V _R = 0, f = 1 MHz	C _j		15		pF

 $^{^{2)}}$ in one Packing Unit $I_{\mbox{Vmax}}/I_{\mbox{Vmin}} \leq 2.0$

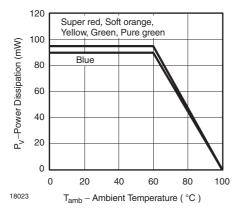
Blue

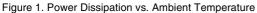
TLMB2100

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Luminous intensity 1)	I _F = 10 mA	I _V	4.0	7.0		mcd
Dominant wavelength	I _F = 10 mA	λ_{d}		465		nm
Peak wavelength	I _F = 10 mA	λ_{p}		428		nm
Angle of half intensity	I _F = 10 mA	φ		± 60		deg
Forward voltage	I _F = 20 mA	V _F		3.9	4.5	V
Reverse voltage	I _R = 10 μA	V _R	5.0			V

 $^{^{1)}}$ in one Packing Unit $I_{Vmax}/I_{Vmin} \le 1.6$

Typical Characteristics (T_{amb} = 25 °C unless otherwise specified)





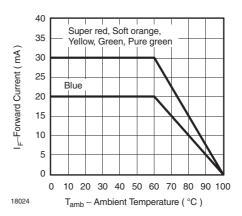


Figure 2. Forward Current vs. Ambient Temperature



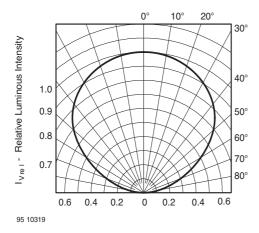


Figure 3. Rel. Luminous Intensity vs. Angular Displacement

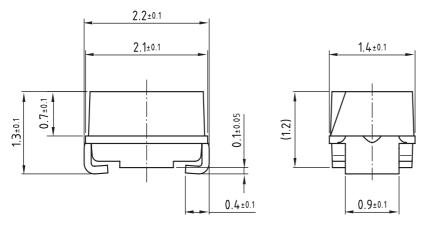
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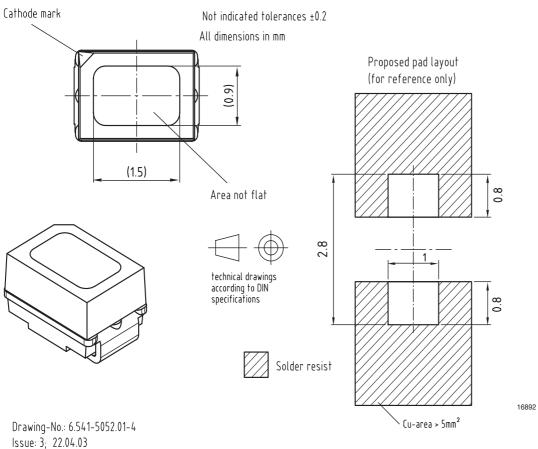
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Package Dimensions in mm



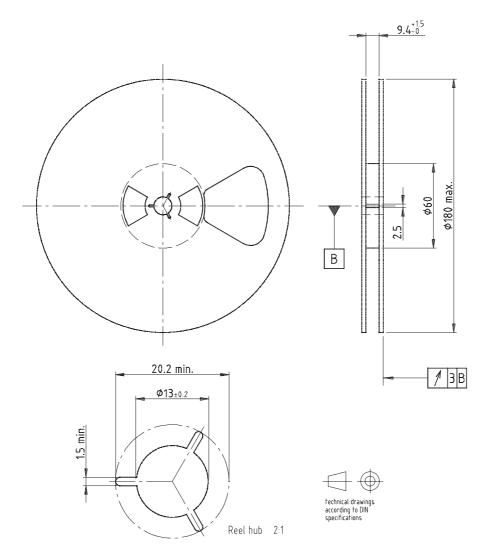




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Reel Dimensions



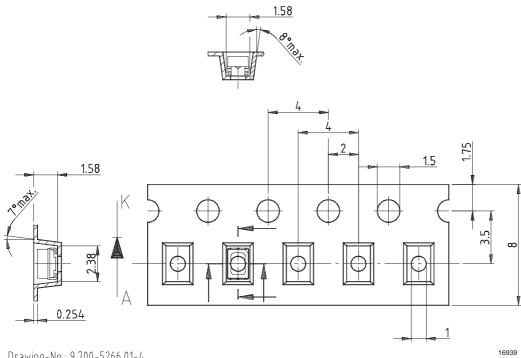
Drawing-No.: 9.800-5051.V5-4

Issue: 1; 25.07.02

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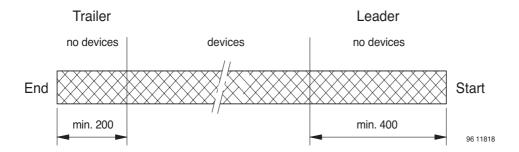
Tape Dimensions



Drawing-No.: 9.700-5266.01-4

Issue: 1; 05.06.02

Leader and Trailer



GS08 = 3000 pcs





Cover Tape Peel Strength

According to DIN EN 60286-3 0.1 to 1.3 N 300 ± 10 mm/min $165 \degree - 180 \degree$ peel angle

Label

Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

Vishay Semiconductor GmbH standard bar code product label (finished goods)

-	•	
Plain Writing	Abbreviation	Length
Item-Description	_	18
Item-Number	INO	8
Selection-Code	SEL	3
LOT-/ Serial-Number	BATCH	10
Data-Code	COD	3 (YWW)
Plant-Code	PTC	2
Quantity	QTY	8
Accepted by:	ACC	-
Packed by:	PCK	_
Mixed Code Indicator	MIXED CODE	_
Origin	xxxxxxx ⁺	Company Logo

Long Bar Code Top	Type	Length
Item-Number	N	8
Plant-Code	N	2
Sequence-Number	X	3
Quantity	N	8
Total Length	_	21

Short Bar Code Bottom	Туре	Length
Selection-Code	X	3
Data-Code	N	3
Batch-Number	Х	10
Filter	_	1
Total Length	_	17

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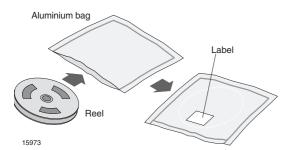
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Dry Packing

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



eal Date: [(If blank, see bar code label) Note: LEVEL defined by EIA JEDEC Standard JESD22-A112

192 hours at 40°C + 5°C/-0°C and <5% RI-

Stored at < 20% RH

CAUTION

This bag contains MOISTURE-SENSITIVE DEVICES aled bag: 12 months at <40°C and <90% relative ed, devices that will be subjected to infrared reflor

evices require baking, before mounting, if: Humidity Indicator Card is >20% when read at 23° C \pm 5° C, or 2a or 2b is not met.

d within 1 year at factory conditions of ≤ 30°C/60% RH or

Example of JESD22-A112 Level 2 label

Final Packing

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

Recommended Method of Storage

Dry box storage is recommended as soon as the aluminium bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 1 year under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 hours at 40 °C + 5 °C/ -0 °C and < 5 % RH (dry air/ nitrogen) or

96 hours at 60 °C +5 °C and < 5 % RH for all device containers or

24 hours at 100 °C +5 °C not suitable for reel or tubes.

An EIA JEDEC Standard JESD22-A112 Level 2 label is included on all dry bags.

ESD Precaution

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the Antistatic Shielding Bag. Electro-Static Sensitive Devices warning labels are on the packaging.

Vishay Semiconductors Standard **Bar-Code Labels**

The Vishay Semiconductors standard bar-code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

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LEVEL

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Assembly Instructions

Reflow Soldering

- Reflow soldering must be done within 1 year stored under max. 30 °C, 60 % RH after opening envelop
- Recommended soldering paste (composition: SN 63 %, Pb 37 %) Melting temperature 178 °C to 192 °C
- Apply solder paste to the specified soldering pads, by using a dispenser or by screen printing.
- Recommended thickness of metal mask is 0.2 mm for screen printing.
- The recommended reflow furnace is a combinationtype with upper and lower heaters.
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown below. Excercise extreme care to keep the maximum temperature below 230 °C. The following temperature profile means the tempera ture at the device surface. Since temperature differ ence occurs between the work and the surface of the circuit board depending on the pes of circuit board or reflow furnace, the operating conditions should be verified prior to start of operation.
- Handling after reflow should be done only after the work surface has been cooled off.

Manual Soldering

- Use the 6/4 solder or the solder containing silver.
- \bullet Use a soldering iron of 25 W or smaller. Adjust the temperature of the soldering iron below 300 $^{\circ}C.$
- Finish soldering within three seconds.
- Handle products only after the temperature is cooled off.

Cleaning

• Perform cleaning after soldering strictly in conformance to the following conditions:

Cleaning agent:

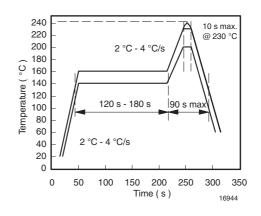
2-propanol (isopropyl alcohol)

Commercially available grades (industrial use) should be used

Demineralized or distilled water having a resistivity of not less than 500 m Ω corresponding to a conductivity of 2 mS/m.

- Temperature and time: 30 seconds under the temperature below 50 °C or 3 minutes below 30 °C.
- Ultrasonic cleaning: Below 20 W.

Reflow Solder Profile



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Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

> Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany Telephone: 49 (0)7131 67 2831, Fax number: 49 (0)7131 67 2423

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