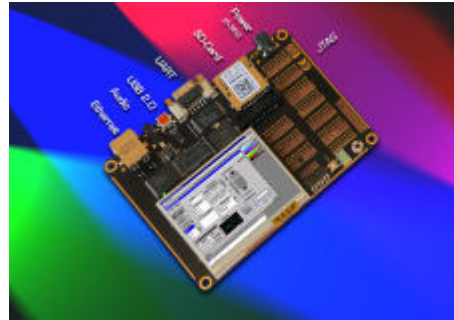


## BMSKTOPAS910 The Starter Kit For TMPA910CRAXBG



Deliverables



### Starter Kit for TMPA910CRAXBG

BMSKTOPASA910(DCE) incl. J-Link

- Supported MCU:  
Toshiba TMPA910CRAXBG (ARM9)

### Features

3,5" Display with Touch Screen

Ethernet Connection

USB 2.0 (480MBps), RS232

Excellent Sound Quality (Audio DAC via I2S)

SD-Card Socket

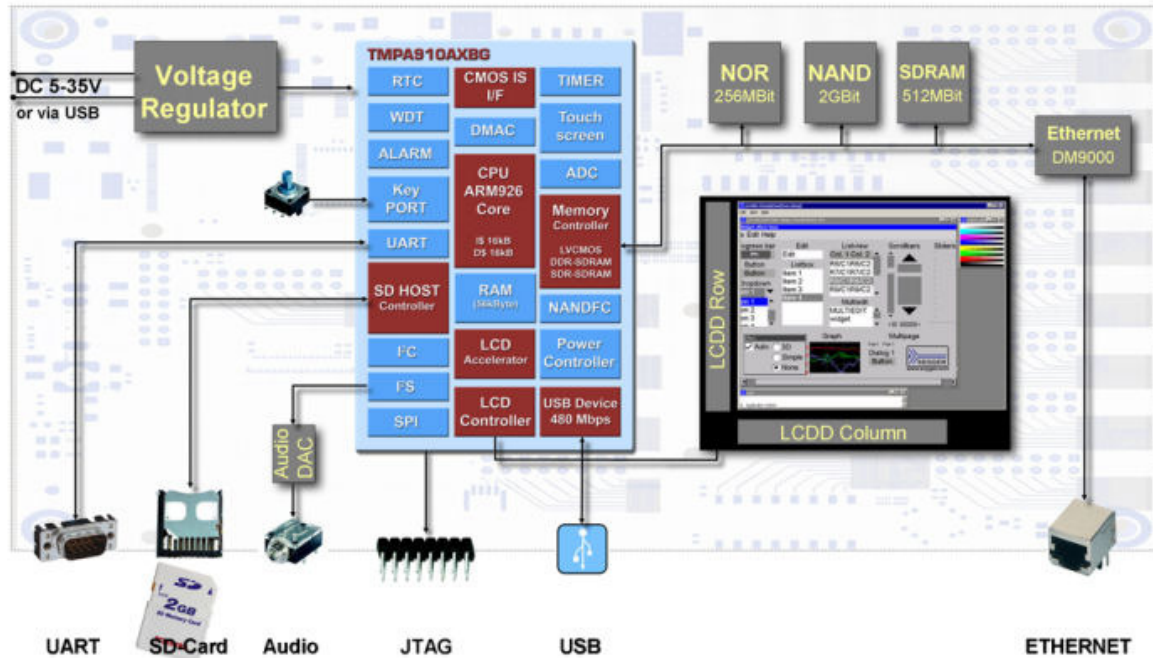
JTAG Interface

- Memory:  
512 MBit SDRAM  
256 MBit NOR Flash  
2 GBit NAND Flash

Single Power Supply (also via USB)

Extensive Software Support by 3rd Party Tools

## Block Diagram Of The Starter Kit



## J-Link Features

- USB 2.0 interface
- Any ARM7/ARM9 core (including thumb mode) and Cortex M3 including SWD supported
- Download speed up to 720 kbytes/s\*
- DCC speed up to 800 kbytes/s\*
- Seamless integration into the IAR workbench
- No power supply required, powered through USB
- Max. JTAG speed 12 MHz

- Automatic core recognition
- Auto speed recognition
- Support for adaptive clocking
- All JTAG signals can be monitored, target voltage can be measured
- Support for multiple devices on scan chain
- Fully plug and play compatible
- 20-pin standard JTAG connector, optional 14-pin adapter
- Optional adapter for 5V targets available
- USB and 20-pin flat cable included
- Wide target voltage range: 1.2V - 3.3V
- Multi core debugging
- J-Mem (live memory view/edit) included
- J-Link server (connects to J-Link via TCP/IP) included
- J-Flash (flash programming software) available
- Software Developer Kit (SDK) available
- RDI support available: Use J-Link with any RDI compliant debugger
- RDI Flash breakpoints available: Unlimited number of breakpoints in flash
- RDI Flash downloader available: Lets your debugger download into flash
- Flash programming DLL (Flash SDK) available: Write your own program for flash programming easily