

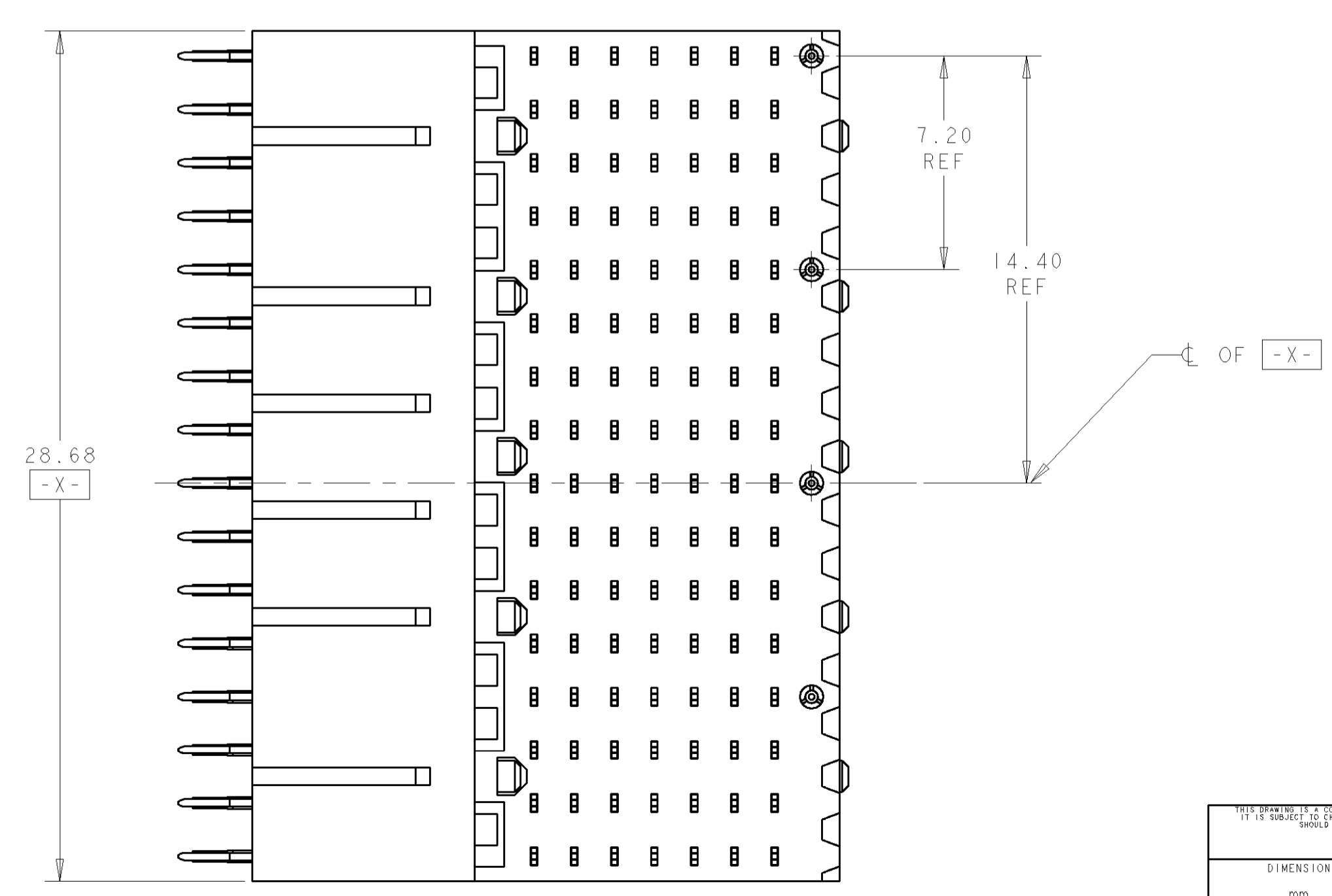
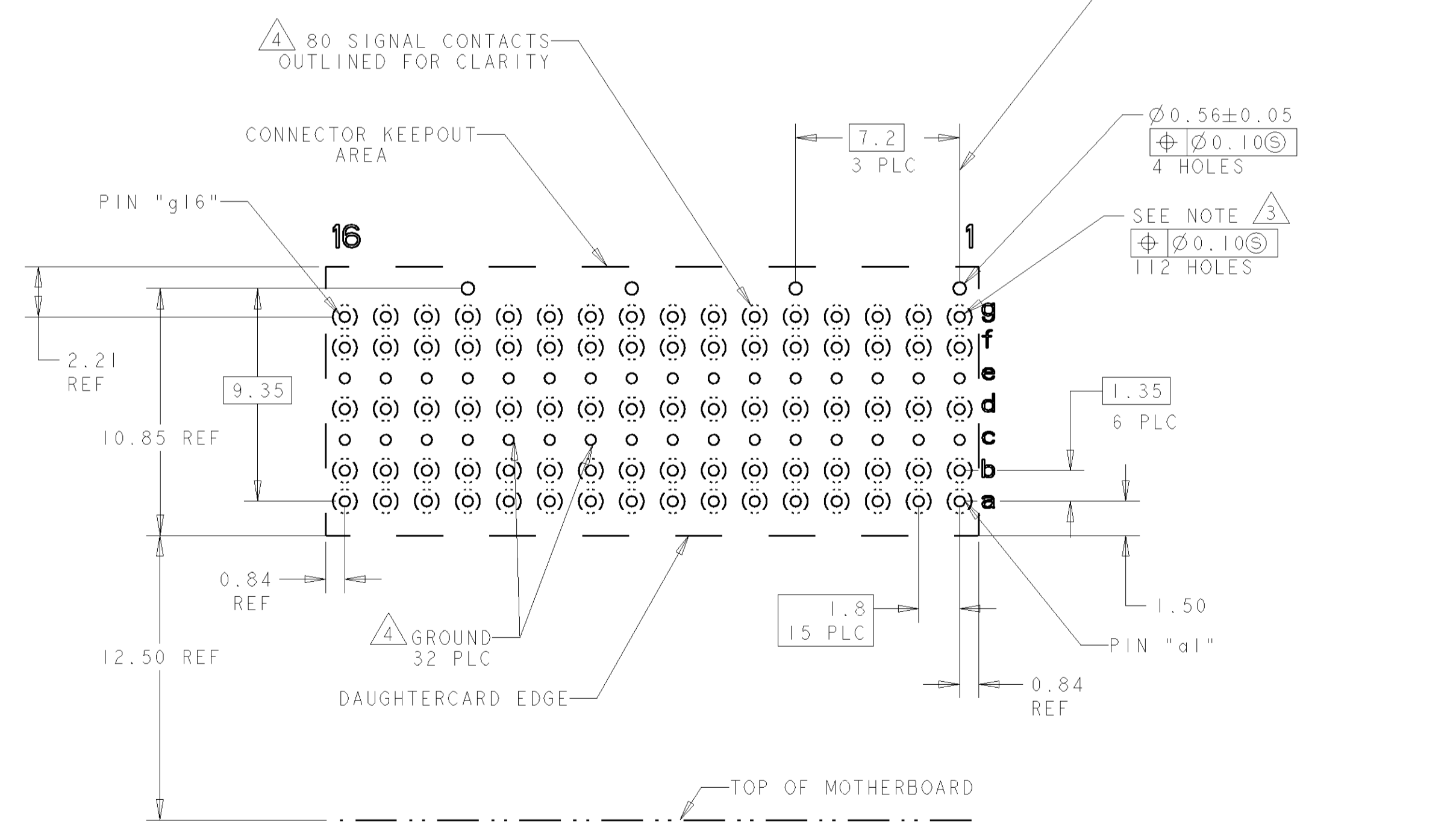
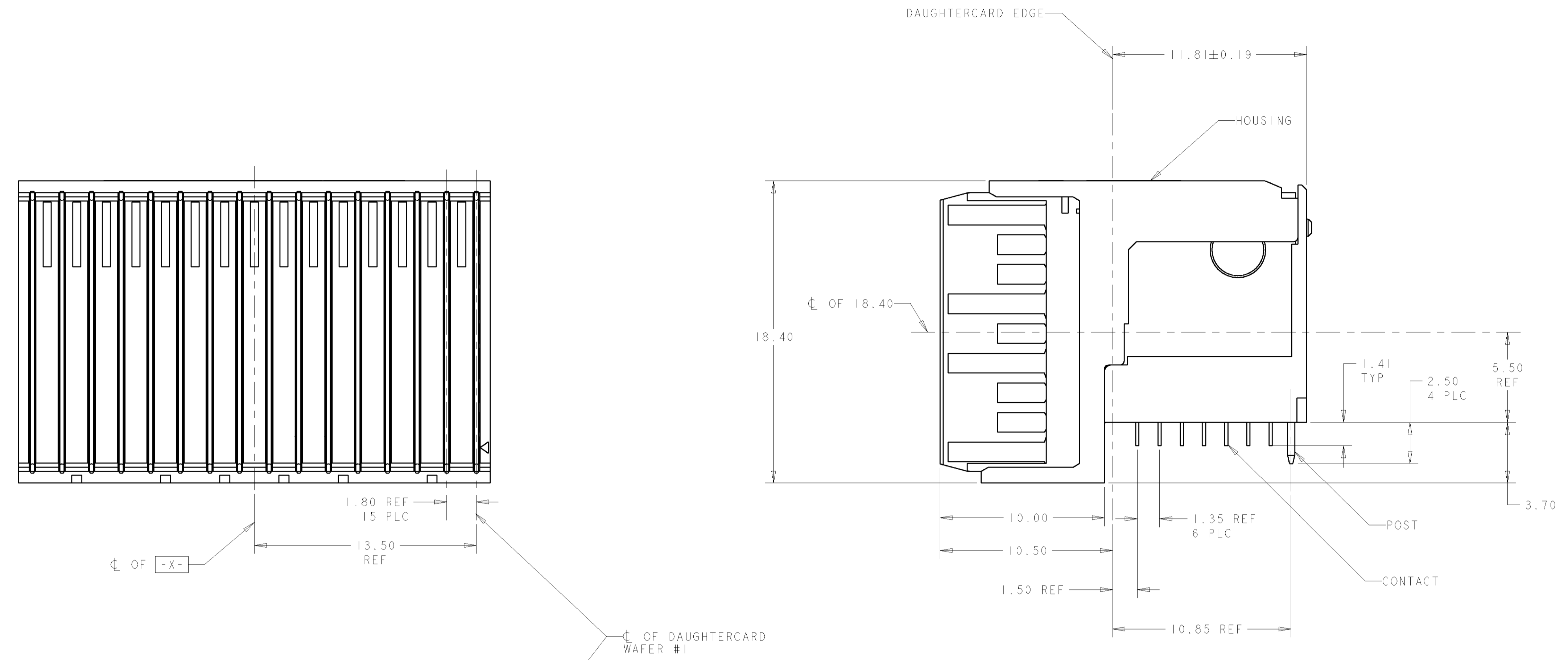
LOC		DIST		REVISIONS			
GP	00	P	LTR	DESCRIPTION	DATE	BY	APPD
		A		REV PER ECO-06-005380	14MAR2006	BC	GG

- 1 HOUSING: LCP, UL94V0. COLOR: BLACK.
CONTACT: PHOSPHOR BRONZE.
POST: BRASS WIRE
- 2 CONTACT: 0.76µm MIN GOLD IN PAD CONTACT AREA,
1.27µm MIN TIN-LEAD ON PCB TAIL
OVER 1.27µm MIN NICKEL OVER ALL.
POST 1.27µm MIN NICKEL PLATED.
- 3 MANUFACTURING TOLERANCE FOR Ø0.46± 0.05
DIAMETER FINISHED HOLEWITH SnPB PLATING:
DRILLED HOLE = Ø0.55±0.02
COPPER PLATING = 0.025-0.050
Sn PB PLATING = 0.0038-0.0124
OR MANUFACTURING TOLERANCE FOR Ø0.475± 0.05
DIAMETER FINISHED HOLE WITHOUT SnPB PLATING:
DRILLED HOLE = Ø0.55±0.02
COPPER PLATING = 0.025-0.050
- 4 SEE TABLE I FOR INTERCONNECTIONS TO
BACKPLANE CONNECTOR.
- 5 CONTACT: 0.76µm MIN GOLD IN PAD CONTACT AREA,
CONTACT: 1.27µm MIN TIN ON PCB TAIL
OVER 1.27µm MIN NICKEL OVER ALL.
POST 1.27µm MIN NICKEL PLATED.
- 6 CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA,
1.27µm MIN TIN-LEAD ON PCB TAIL
OVER 1.27µm MIN NICKEL OVER ALL.
POST 1.27µm MIN NICKEL PLATED.

TABLE I
INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410140

TYPICAL INTERCONNECTIONS FOR EACH COLUMN (WAFER): 1-16		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL	ax	bx
SIGNAL	bx	cx
SIGNAL	dx	ex
SIGNAL	fx	gx
SIGNAL	gx	hx
GROUND	cx, ex, (ALL COMMONED)	ax, dx, fx, ix

NOTE: "x" DESIGNATES THE COLUMN NUMBER



PC BOARD LAYOUT (CONNECTOR SIDE)
SCALE 5:1

DIMENSIONS: mm 		TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 1 PLC ±0.5 2 PLC ±0.13 3 PLC ± 4 PLC ± ANGLES ±1°		DWN LLSIPE 01JUN2004 ENR J. CONSOLI 01JUN2004 APVD G. GRIFFITH 14MAR2006 PRODUCT SPEC 108-2072 APPLICATION SPEC 114-13056 WEIGHT - CUSTOMER DRAWING		Tyco Electronics Harrisburg, PA 17105-3608 NAME RIGHT-ANGLE PLUG ASSEMBLY, 7 ROW, CENTER, 20.3mm, SINGLE ENDED, MultiGig RT2, DAUGHTERCARD CONNECTOR SIZE CAGE CODE DRAWING NO. RESTRICTED TO A1 00779 C=1410190 SCALE 6:1 SHEET 1 OF 1 REV A	
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