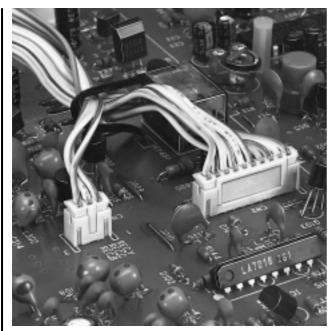


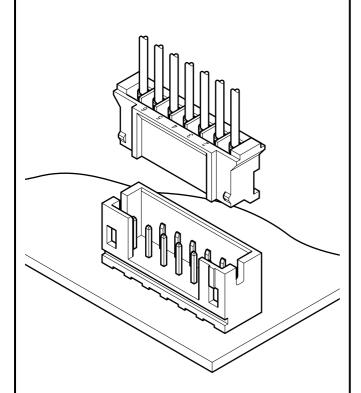
PH CONNECTOR



Disconnectable Crimp style connectors



This is a thin, low-profile 2.0mm (.079") pitch connector 7.5mm (.295") in height after mounting and 4.5mm (.177") in width. It is designed to meet the demand for high-density connection of internal wires to printed circuit boards. It is compact, highly reliable and low in cost.



Features -

Reliable contact

The contact has long dimples near the center to ensure a good connection at all times, even under conditions of vibration and mechanical abuse, and when used with low voltage and low current circuits.

• Easy and effective crimping

Although the contact is compact, it has a long wire strip holding length of 2.6 +/- 0.4mm (.102" +/-.016"). This long length simplifies automatic crimping and the crimping of shielded wires.

• Fully shrouded header

The fully shrouded header has walls on all four sides to prevent improper connection to the mating housing and to prevent the instruction of flux and other contaminants.

High solderable square post

The square post is copper-undercoated and tin/lead-plated for superior solderability and for whisker prevention.

• **Printed circuit board retention mechanism** The solder side of the header has a retention mechanism that prevents it from floating during soldering. As a result, the header can be positioned accurately.

• Compatible with the KR insulation displacement connector

The same shrouded header can be used for either PH crimpstyle connector or KR insulation displacement connector. This allows both types of connector to be used interchangeably without replacing the header.

Surface mount model (SMT)

This connector is also available in a surface mount configuration. Its housing is made of heat resistant resin so that it is not adversely affected during reflow soldering. Because of its tiny size and ability to be surface mounted, this connector meets the demand for high-density mounting of components inside electronic products.

Specifications -

- Current rating: 2A AC, DC (AWG #24)
- Voltage rating: 100V AC, DC
- Temperature range: -25°C to +85°C (including temperature rise in applying electrical current)
- Contact resistance: Initial value/10m Ω max.
 - After environmental testing/20m Ω max.
- Insulation resistance: 1,000M Ω min.
- Withstanding voltage: 800V AC/minute
- Applicable wire: AWG #32 to #24
- Applicable PC board thickness: 0.8 to 1.6mm(.031" to .063")
- * Contact JST if Lead-Free product is required.
- * Refer to "General Instruction and Notice when using Terminals and Connectors" at the end of this catalog.
- * Contact JST for details.

Standards ·

Recognized E60389

- Certified LR20812
- <u>/</u> R75087

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THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO J.S.T. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION.	Recommendable P.C.Board hole layout		A 1.3 A 2 10.1 A 2 10.1 A 10.5 A 10.5		2	C ^{+0.3}		g					1 2 3 4
• • • • • • • • • • • • • • • • • • •					S 2B-PH-K-S Part No.	5 S 5B-PH-K-S (LF) (SN) 3 S 3B-PH-K-S (LF) (SN)	S 7B-PH-K-S (LF) S 6B-PH-K-S (LF)	S BB-PH-K-S	S11B-PH-K-S	14 S14B-PH-K-S (LF) (SN) 13 S13B-PH-K-S (LF) (SN) 12 S12B-PH-K-S (LF) (SN)	S16B-PH-K-S (LF) S15B-PH-K-S (LF)		5 6 7 8
EC4113-2	2 WAFER 66NYLON UL94V-0. NATURAL 1 POST BRASS FIN-FEATED UL94V-0. NATURAL No. FART NAME MATERIAL SUBFACE FINISH REMARKS A2 METRIC 10 Image: Metric mark SUBFACE FINISH REMARKS S.K METRIC 10 Image: Metric mark SUBFACE FINISH REMARKS S.K METRIC Image: Metric mark Image: Metric mark SUBFACE FINISH REMARKS S.K Image: Metric mark Image: Metric mark Image: Metric mark SUBFACE FINISH REMARKS S.K Image: Metric mark Image: Metric mark Image: State Finish SUBFACE FINISH REMARKS S.K Image: Metric mark Image: State Finish Image: State Finish SUBFACE FINISH REMARKS S.K Image: State Finish Image: State Finish Image: State Finish State Finish State Finish J.S.T.MFG.CO., LTD. Image: State Finish KRD-35429 RD RD	NOTE I.Unless otherwise specified, tolerances are ±D.3 2.Clinched pins directions and locations are shown table 1. 3.Tolerance of hole pitch on PCB are not cumulative.		CLINCHED PINS DETAILS KI, K2, O:POST UNCLINCHED	A B C 16151413121110987	4.0 7.9 6.6 KI O O O 4.0 7.9 6.6 KI O O O	12.0 15.9 14.6 O O KI O O KI 10.0 13.9 12.6 O O KI D KI	16.0 19.9 18.6 14.0 17.9 16.6) 26.0 29.9 28.6 OK10000k2000k10) 24.0 27.9 26.6 OK1000k2000k20000k10) 27.9 26.6 OK1000k20000k20000k10	30.0 33.9 32.6 OK1000000K20000		9 10 11 12 Rev description date designed