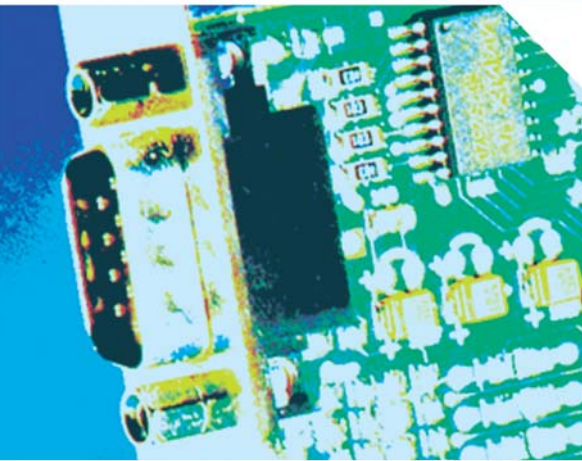




# CC-525 & CC-530 PCI DUAL ULTRA RS422/485

SERIAL CARDS ENGINEERED TO EXCEED EXPECTATIONS



## Features

- 2 Port RS422/485
- 32 bit and 64 bit PCI compatible
- 128 Byte FIFO with user definable trigger level
- 15 and 18 MEGABAUD data rate options
- Standard Profile
- Full Duplex and Half Duplex Autogating
- Transient Spike Protection
- Drivers for all popular operating systems
- Sample Programs, Test & Terminal software - all with source code
- Lifetime Support and 3 year Warranty

[Click on each feature to find similar products](#)

## Options



15 Megabaud:  
CC-530



18 Megabaud:  
CC-525

## Description

PCI card providing 2 industry standard 9 pin RS422/485 serial COM ports in a single PCI slot. On board Zener diodes ensure electrical protection.

Stunning ULTRA 15 or 18 megabaud data transfer rates!! And 128 byte deep FIFO guarantees uncompromising performance and fault-free use.

With Brainboxes Lifetime Support and 3 year warranty!

## Software

Microsoft signed drivers (easy installation & robust operation) for:

- o Windows Server 2003 x32 bit & x64 bit Editions
- o Windows XP x32 bit & x64 bit Editions
- o Windows 2000.



Fully tested drivers also included for:

- o Windows Me, Windows 98 & 95, Windows NT.

As well as: full product documentation, a thorough troubleshooting guide and terminal software. And detailed sample source code for all popular programming languages.

Need support for other operating systems? Please call.

Find out about Brainboxes excellent driver configurability: [Click Here](#)



## ORDER

### Description

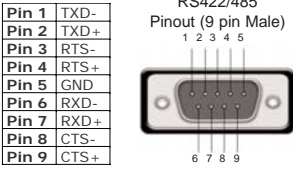


PCI DUAL ULTRA RS422/485 (15 MEGABAUD)  
PCI DUAL ULTRA RS422/485 (18 MEGABAUD)

### Code

CC-530  
CC-525



# PCI DUAL ULTRA RS422/485

Product Code		UC-313
<b>Serial Port</b> 		Ports Connector UART type FIFO size Transmitted Signal Strength Operating Distance Electrical Protection
<b>Serial Port Settings</b>		2 Port RS422/485 DB9 (9 Pin Male) 16C950 UART 128 Byte with user definable trigger level +/- 5 Volts open circuit RS422/485 Standard 4000 ft (1200 metres) RS422/485 Standard +ve transient spikes > 12V, -ve transient spikes >6.8V ,
<b>General</b>		Baud Rate (bits per second) Data Bits Parity Stop Bits Flow Control IRQs Tx/Rx Modes Connection Schemes  RS485 Gating
<b>Interface</b>		up to 15,000,000 (CC-530) or up to 18,000,000 (CC-525) 5,6,7 or 8 Odd, Even, None, Mark or Space 1, 1.5 or 2 CTS/RTS Plug and Play - Shared interrupts for all ports Full Duplex, Half Duplex Autogating Point to Point, One talker; many listeners, (32 Max) Many talkers / listeners Half Duplex (32 Max) Hardware Autogating, TxD always / RTS true enabled, RxD always enabled, RxD RTS true disabled, CTS forced true
<b>Approvals &amp; Accreditations</b>		Standard 4 360mA @ 5V  0.091Kg 95 x 125 mm
<b>Box Contents</b>		BUS Compatibility OS Compatibility  PCI Compliance
<b>OEM option</b>		32/64 bit PCI Windows 98/ NT4/ 2000/ XP/ Server 2003 Windows XP x64 bit Edition / Windows Server 2003 x64 bit Linux Version 2.2
<b>Product Support</b>		UL, EMC: CE, FCC, PCI Power Management 1.1 Compliant, PCI 2.2 Compliant
<b>Warranty</b>		Installation CD including manual, Microsoft signed drivers, utilities and sample programs with source code UNIVERSAL ULTRA VELOCITY RS422/485 Installation Guide
<b>Made In</b>		Available for bulk buy OEM
<b>Customisable</b>		Lifetime Email and Phone Support from Fully qualified, friendly staff 40 hours a week
 		3 years
Manufactured in the UK by Brainboxes Winner 2005 European Electronics Industry Awards "Manufacturer of the Year"		Brainboxes operate a "Perfect Fit Custom Design" policy for volume users More Info: <a href="#">CLICK HERE</a>



# PCI DUAL ULTRA RS422/485

## Glossary: Product Features Explained

### ULTRA Baud Rates

This PCI card supports ULTRA baud rates. Up to 18,000,000 baud. By comparison many RS422/485 cards can only achieve a max data rate of 115,200 baud. Brainboxes ULTRA cards are over 150 times faster!

### Autogating

When the device is set to half duplex mode, transmitted and received data is sent across the same signal lines. Autogating means the Brainboxes card knows when to 'listen' for data and when to transmit data, with a quick and automatic hardware switch between the 2 modes. This is much faster than software switching and ensures no data loss due to turnaround delay.

**More Info:** [Click Here](#)

### Electrical Protection

All Brainboxes RS422/485 cards have transient spike protection. This protects against noise on the signal line which can be created by: electrical surges on the other device, nearby magnetic field interactions and background noise. Assuring safe and reliable operation in almost any environment.

### PCI Compliant

All Brainboxes PCI cards are fully PCI version 2.2 compliant, in accordance with the PCI-Special Interest Group (PCI-SIG).

### Large FIFO

FIFO means First In First Out, a FIFO is memory space on the Brainboxes card. A larger FIFO allows more memory space for buffering data, so calls to the computer processor are less frequent. This significantly increases data throughput and improves CPU availability for other applications.

**More info:** [Click Here](#)

### 4 Layer PCB

Printed Circuit Board with 4 layers of circuitry complies with the PCI SIG best practice guideline and ensures a more reliable card. 1 layer is for power and 1 for ground which leads to greater EMC shielding for the signals. This ultimately gives greater signal integrity for sensitive timing applications.

### Surface Mount Components

Most components on a Brainboxes card are placed by a surface mount machine. The surface mount machine allows for highly accurate and fast production of Brainboxes cards. Orders can be met quickly, reliably and with outstanding quality.

### Capacitors

As required by the PCI-SIG all Brainboxes cards have capacitors connected to all the power pins on the PCI connector, near to the connector. This ensures that the electrical power rails on the Brainboxes card stay at the right voltage, regardless of the power demands placed on them. Data signals keep their integrity.