June 1999



Si4953DY*

Dual P-Channel Enhancement Mode MOSFET

General Description

These P-Channel Enhancement Mode MOSFETs are produced using Fairchild Semiconductor's advance process that has been especially tailored to minimize onstate resistance and yet maintain superior switching performance.

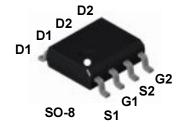
These devices are well suited for low voltage and battery powered applications where low in-line power loss and fast switching are required.

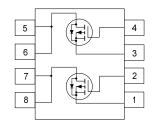
Applications

- · Battery switch
- · Load switch
- · Motor controls

Features

- -4.9 A, -30 V. $R_{DS(on)}$ = 0.053 Ω @ V_{GS} = -10 V $R_{DS(on)}$ = 0.095 Ω @ V_{GS} = -4.5 V.
- · Low gate charge.
- · Fast switching speed.
- · High power and current handling capability.





Absolute Maximum Ratings TA=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		-30	V
V _{GSS}	Gate-Source Voltage		<u>±</u> 20	V
l _D	Drain Current - Continuous	(Note 1a)	-4.9	А
	- Pulsed		-30	
P _D	Power Dissipation for Dual Operation		2.0	W
	Power Dissipation for Single Operation	(Note 1a)	1.6	
		(Note 1b)	1.0	
		(Note 1c)	0.9	
T _J , T _{STG}	Operating and Storage Junction Temperat	-55 to +150	∘C	

Thermal Characteristics

R _{eJA}	Thermal Resistance, Junction-to-Ambient		62.5	∘C/W
R _{eJC}	Thermal Resistance, Junction-to-Case (Not	e 1)	40	∘C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Ouantity
4953	Si4953DY	13"	12mm	2500 units

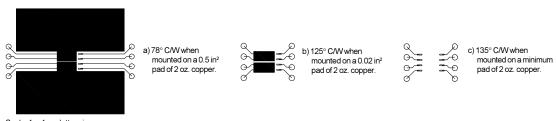
^{*} Die and manufacturing source subject to change without prior notification.

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Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Cha	racteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	-30			٧
ΔBVDSS ΔT.1	Breakdown Voltage Temperature Coefficient	I _D = -250 _μ A,Referenced to 25°C		-20		mV/∘C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -30 V, V _{GS} = 0 V V _{DS} = -30 V, V _{GS} = 0 V, T _J = 55°C			-1 -25	μА
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
On Char	racteristics (Note 2)	•		•		,
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1.0			V
$\frac{\Delta V_{GS(th)}}{\Delta T_{.l}}$	Gate Threshold Voltage Temperature Coefficient	I _D = -250 _μ A,Referenced to 25°C		4		mV/∘C
R _{DS(on)}	Static Drain-Source On-Resistance	$V_{GS} = -10 \text{ V}, I_D = -4.9 \text{ A}$ $V_{GS} = -10 \text{ V}, I_D = -4.9 \text{ A}, T_J = 125 ^{\circ}\text{C}$ $V_{GS} = -4.5 \text{ V}, I_D = -3.6 \text{ A}$		0.040 0.055 0.058	0.053 0.085 0.095	Ω
I _{D(on)}	On-State Drain Current	V _{GS} = -10 V, V _{DS} = -5 V	-20		0.000	Α
g _{FS}	Forward Transconductance	V _{DS} = -15 V, I _D = -4.9 A		11		s
		1 - 50	ļ			
C _{iss}	Characteristics Input Capacitance	V _{DS} = -15 V, V _{GS} = 0 V,		750		pF
Coss	Output Capacitance	f = 1.0 MHz		220		pF
C _{rss}	Reverse Transfer Capacitance			100		pF
Switchir	ng Characteristics (Note 2)	<u> </u>				
t _{d(on)}	Turn-On Delay Time	$V_{DD} = -15 \text{ V}, I_D = -1 \text{ A}, R_L = 15 \Omega$		12	15	ns
t _r	Turn-On Rise Time	$V_{GS} = -10 \text{ V. } R_{GEN} = 6 \Omega$		14	20	ns
t _{d(off)}	Turn-Off Delay Time	1		24	40	ns
t _f	Turn-Off Fall Time	1		16	25	ns
t _m	Drain-Source Reverse Recovery Time	$I_F = -1.7 \text{ A, di/dt} = 100 \text{A/}_{\mu} \text{s}$			90	nS
Qg	Total Gate Charge	$V_{DS} = -15 \text{ V}, I_{D} = -4.9 \text{ A},$		15	25	nC
Q _{gs}	Gate-Source Charge	V _{GS} = -10 V		1.8		nC
Q _{gd}	Gate-Drain Charge			3		nC
Drain-Sa	ource Diode Characteris	tics and Maximum Ratings				
<u> ۱_۶ اما</u>	Maximum Continuous Drain-S	_			-1.7	Α
	1				!	

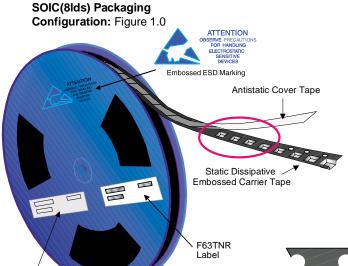
Notes

R_{BJA} is the sum of the junction-to-case and case-to-ambient resistance where the case thermal reference is defined as the solder mounting surface of
the drain pins. R_{BJC} is guaranteed by design while R_{BCA} is determined by the user's board design.



Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%

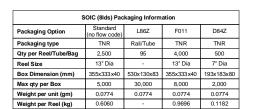


Packaging Description:

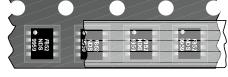
Packaging Description:

SOIC-8 parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 2,500 units per 13° or 330cm diameter reel. The reels are dark blue in color and is made of polystyrene plastic (anti-static coated). Other option comes in 500 units per 7° or 177cm diameter reel. This and some other options are further described in the Packaging Information table.

These full reels are individually barcode labeled and placed inside a standard intermediate box (illustrated in figure 1.0) made of recyclable corrugated brown paper. One box contains two reels maximum. And these boxes are placed inside a barcode labeled shipping box which comes in different sizes depending on the number of parts shipped.

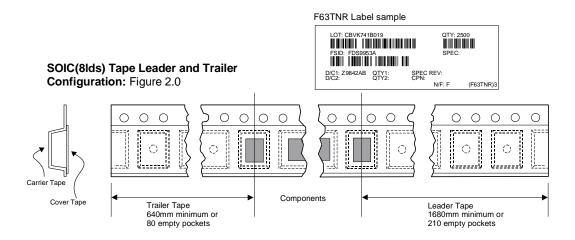


Customized Label



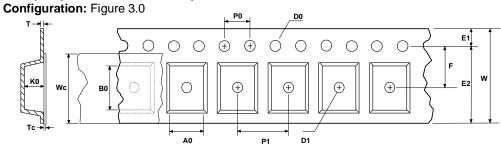


SOIC-8 Unit Orientation





SOIC(8lds) Embossed Carrier Tape



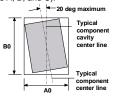


	Dimensions are in millimeter													
Pkg type	Α0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	т	Wc	Тс
SOIC(8lds) (12mm)	5.30 +/-0.10	6.50 +/-0.10	12.0 +/-0.3	1.55 +/-0.05	1.60 +/-0.10	1.75 +/-0.10	10.25 min	5.50 +/-0.05	8.0 +/-0.1	4.0 +/-0.1	2.1 +/-0.10	0.450 +/- 0.150	9.2 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation



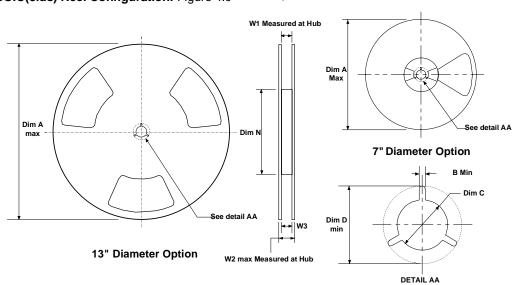
Sketch B (Top View)
Component Rotation



Sketch C (Top View)

Component lateral movement

SOIC(8lds) Reel Configuration: Figure 4.0

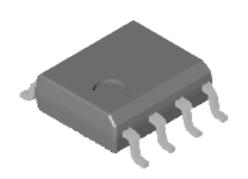


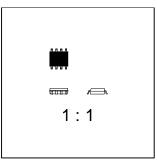
Dimensions are in inches and millimeters									
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
12mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.488 +0.078/-0.000 12.4 +2/0	0.724 18.4	0.469 - 0.606 11.9 - 15.4
12mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	7.00 178	0.488 +0.078/-0.000 12.4 +2/0	0.724 18.4	0.469 - 0.606 11.9 - 15.4

SOIC-8 Package Dimensions



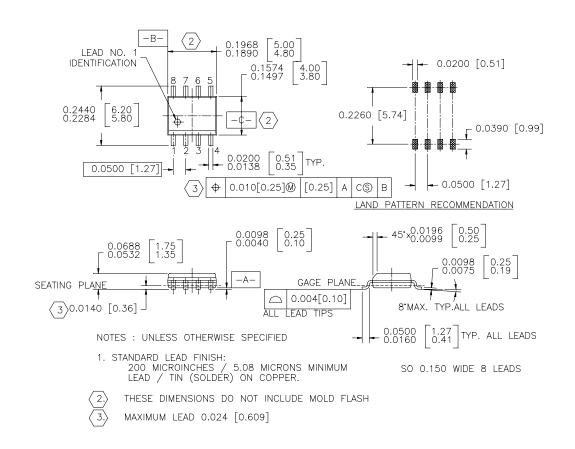
SOIC-8 (FS PKG Code S1)





Scale 1:1 on letter size paper
Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.0774



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DOME™ ISOPLANAR™ Quiet Series™

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