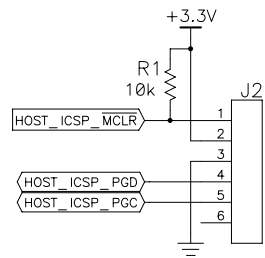
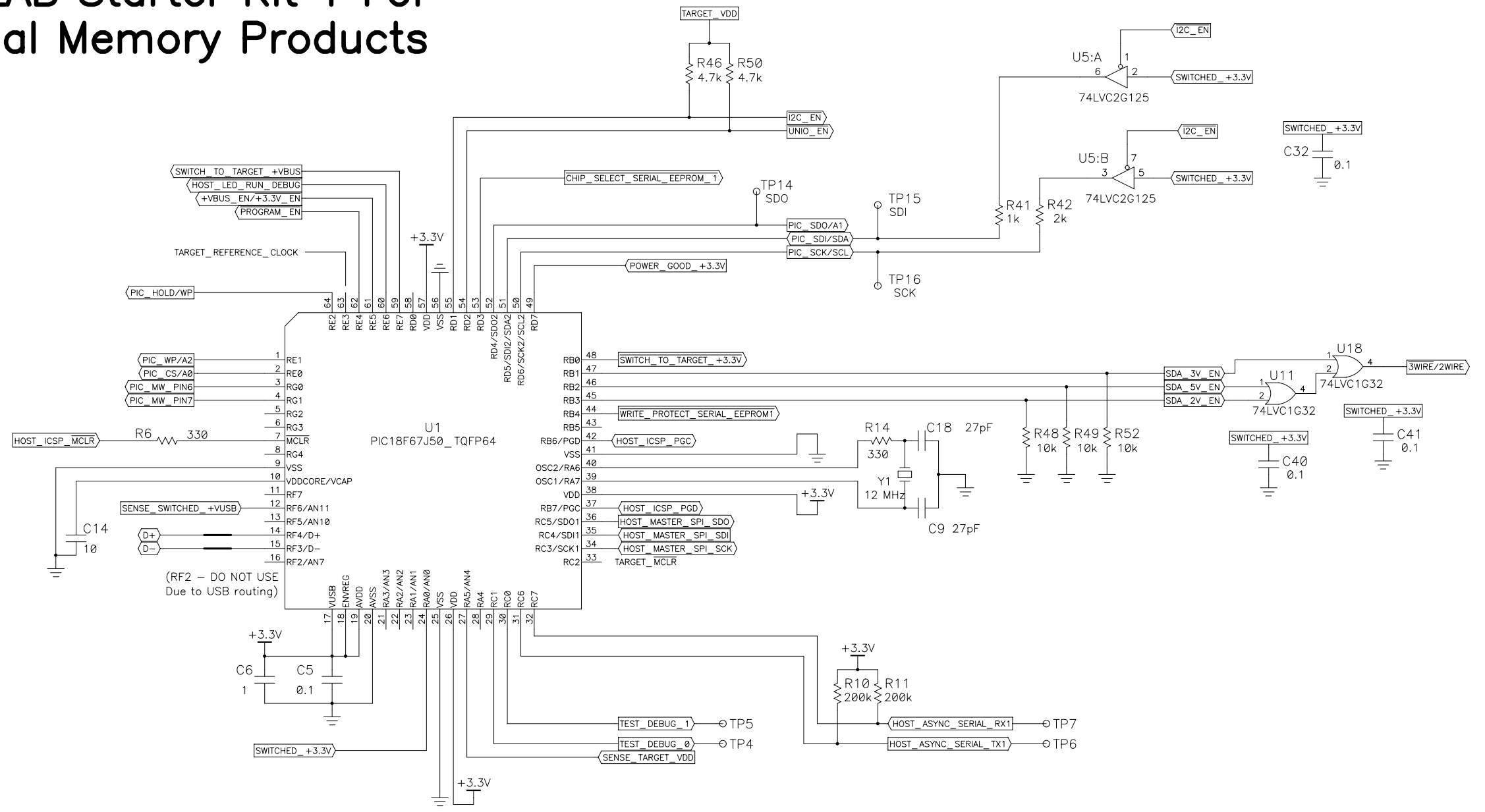


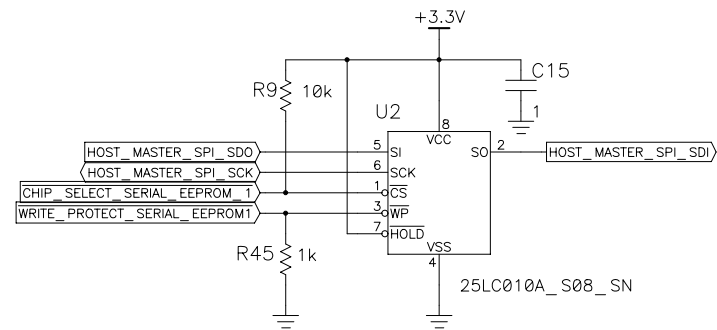
ICSP/TEST/DEBUG HEADER
(PIC18F67J50)



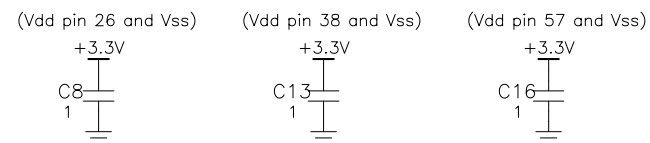
MPLAB Starter Kit 1 For Serial Memory Products



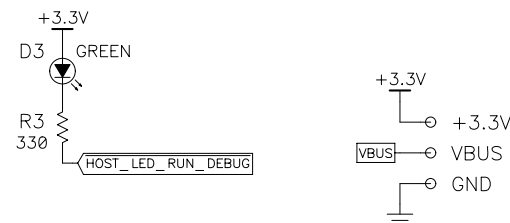
SERIAL EEPROM



PIC18F67J50 BYPASS/DECOUPLING CAPACITORS



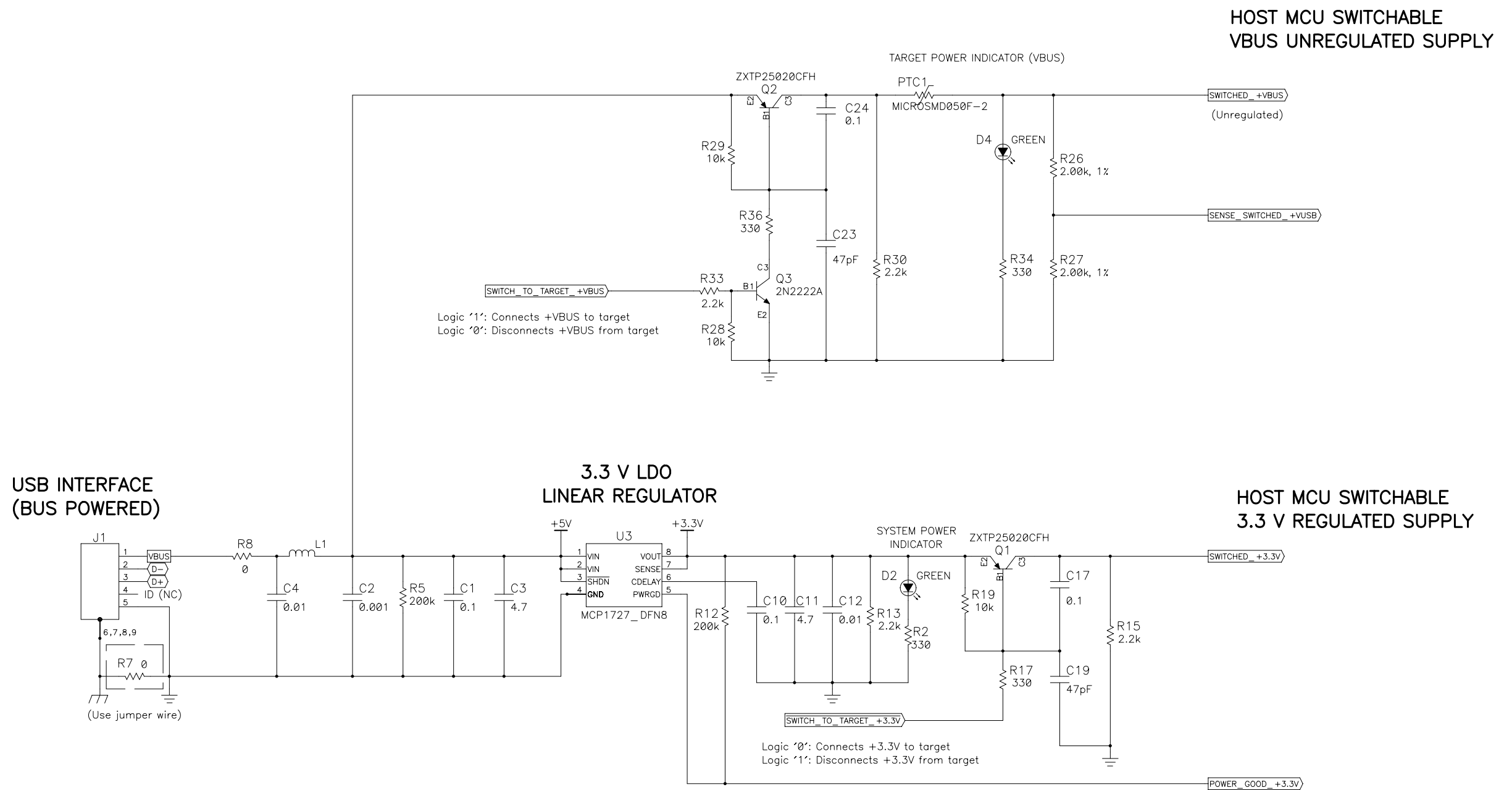
STATUS LEDs



NOTES:
UNLESS OTHERWISE SPECIFIED, RESISTANCE VALUES ARE IN OHMS, 5% 1/10W, AND CAPACITANCE VALUES ARE IN MICROFARADS.
DEVICE NAMES/NUMBERS SHOWN HERE ARE FOR REFERENCE ONLY AND MAY DIFFER FROM ACTUAL NUMBER. ACTUAL NUMBERS ARE FOUND IN THE BOM FOR THIS ASSEMBLY.
ALL COMPONENTS ARE RoHS COMPLIANT.

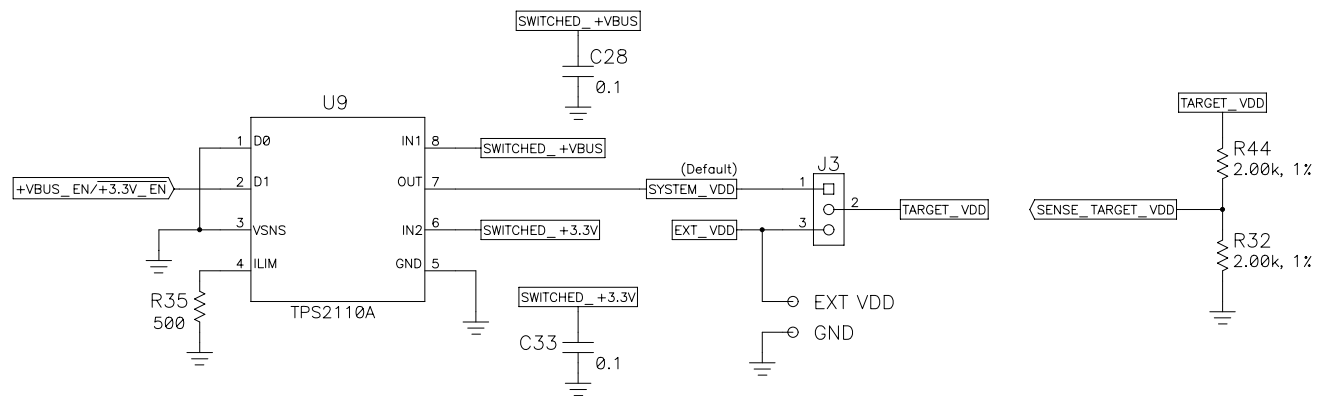
1		Changed power MUX. Changed SDA level translation. Changed 3WIRE/2WIRE MUX. Release to production.	03/04/08	Title MPLAB Starter Kit 1 For Serial Memory Products	
A	REV	ECO#	DATE	Size D	Number
					03-02032
				Date	March 04, 2008
				Drawn by	M. Ragsdale
				Filename	03-02032.sch
				Sheet	1 of 4

USB INTERFACE / TARGET POWER SWITCHING



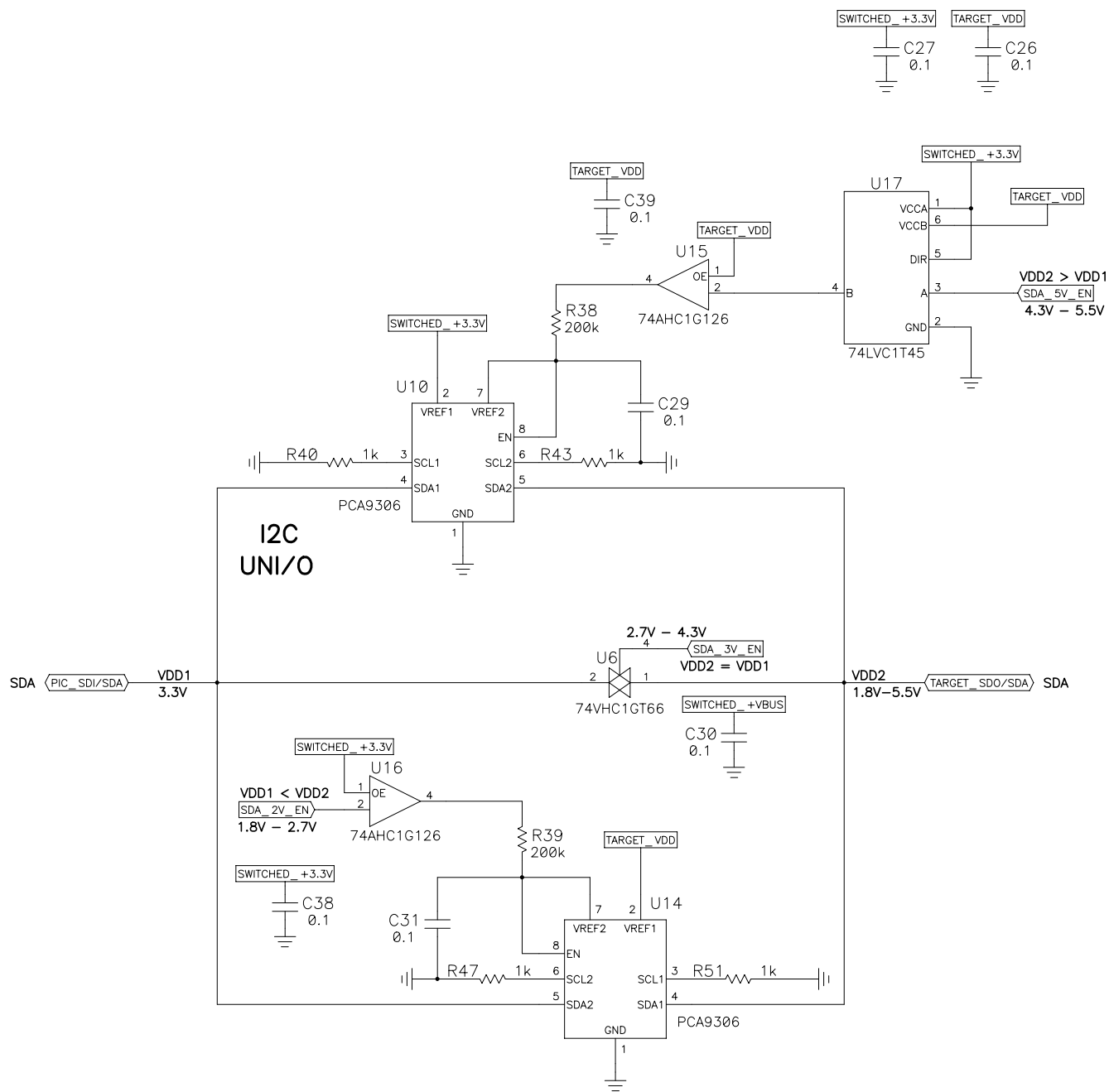
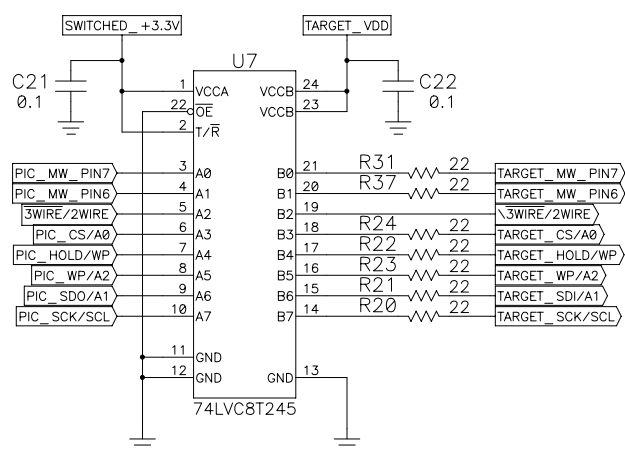
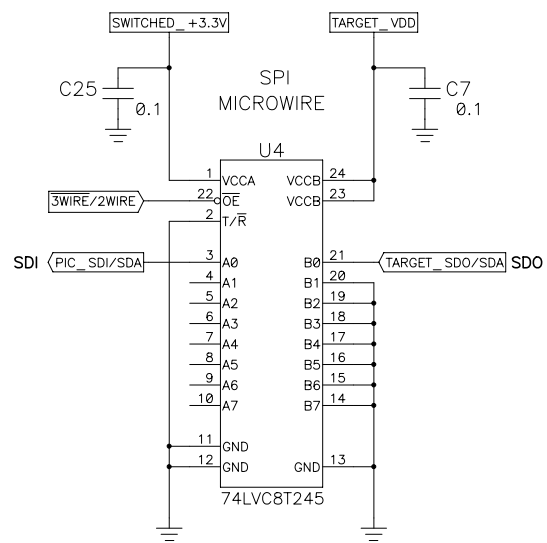
Power Table						
Ref Des	Device(Type)	Package	GND	TARGET_VDD	SWITCHED_ +3.3V	SWITCHED_ +VBUS
U5	74LVC2G125_SSOP8	SSOP08	4		8	
U6	74VHC1GT66_SOT-23_5L	SOT-23_5L	3			5
U11	74LVC1G32_SOT-23_5L	SOT-23_5L	3		5	
U12	74AHC1G125_SOT-23_5L	SOT-23_5L	3	5		
U13	74AHC1G125_SOT-23_5L	SOT-23_5L	3	5		
U15	74AHC1G126_SOT-23_5L	SOT-23_5L	3	5		
U16	74AHC1G126_SOT-23_5L	SOT-23_5L	3	5		
U18	74LVC1G32_SOT-23_5L	SOT-23_5L	3		5	

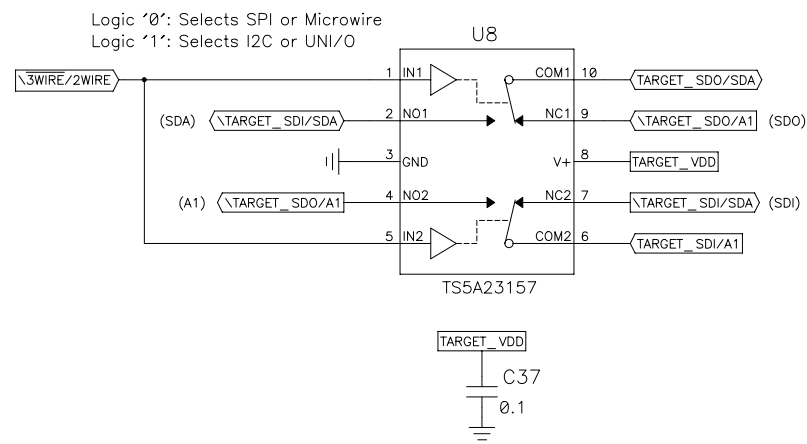
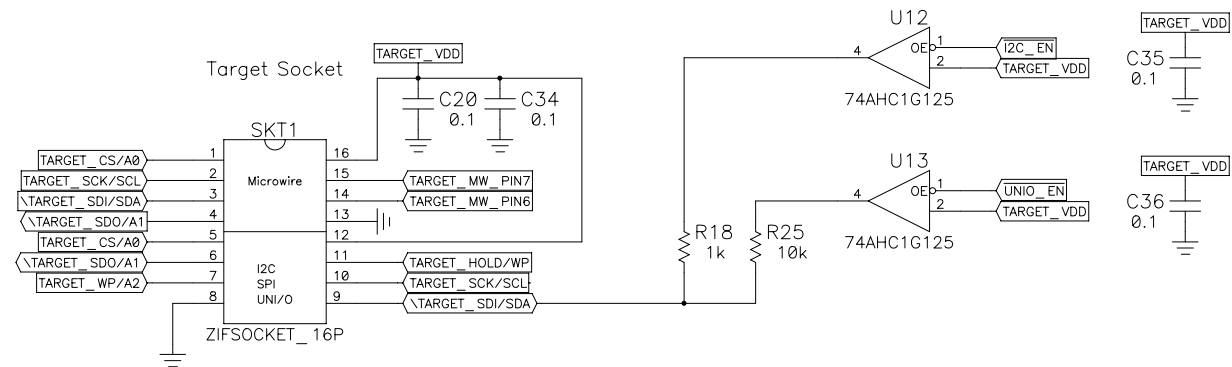
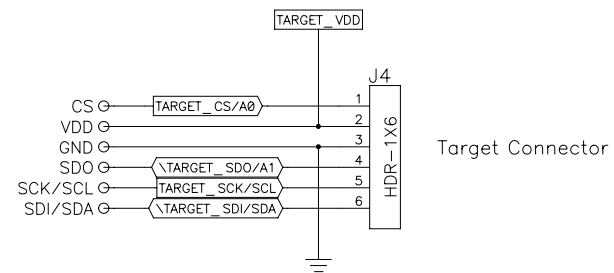
APPLICATIONS CIRCUITRY



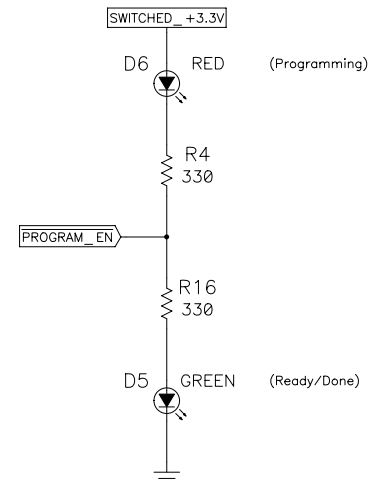
I = 250/R
I = 250/500
I = 500mA

D1	D0	OUT
0	0	IN2
1	0	IN1
1	1	Hi-Z





STATUS LEDs



Title MPLAB Starter Kit - Memory Products		
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